



# SPEC CPU®2017 Floating Point Speed Result

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## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### ProLiant ML350 Gen11

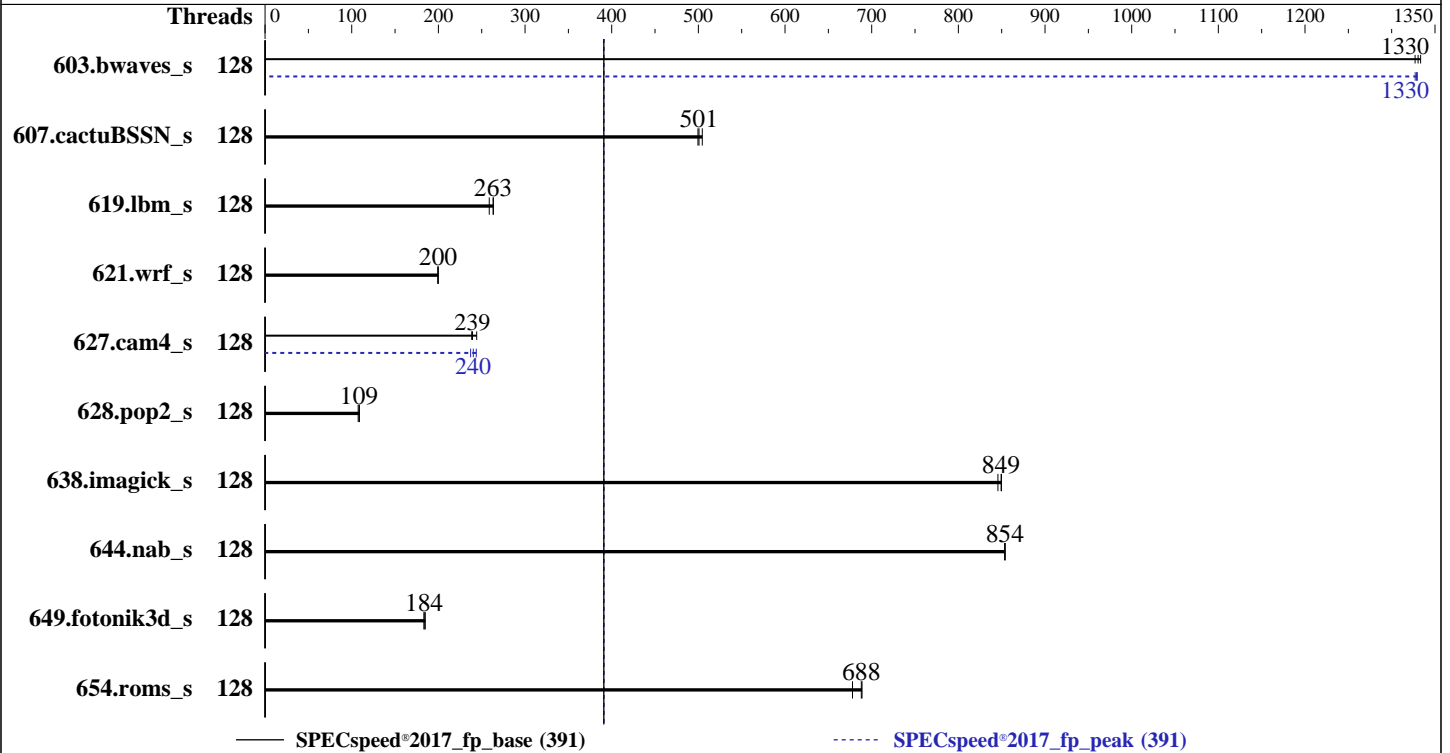
(2.00 GHz, Intel Xeon Platinum 8592V)

SPECspeed®2017\_fp\_base = 391

SPECspeed®2017\_fp\_peak = 391

CPU2017 License: 3  
Test Sponsor: HPE  
Tested by: HPE

Test Date: Feb-2024  
Hardware Availability: Feb-2024  
Software Availability: Dec-2023



### Hardware

CPU Name: Intel Xeon Platinum 8592V  
Max MHz: 3900  
Nominal: 2000  
Enabled: 128 cores, 2 chips  
Orderable: 1, 2 chip(s)  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 320 MB I+D on chip per chip  
Other: None  
Memory: 512 GB (16 x 32 GB 2Rx8 PC5-5600B-R, running at 4800)  
Storage: 1 x 480 GB SATA SSD  
Other: Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP5  
Kernel 5.14.21-150500.53-default  
Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;  
Parallel: Yes  
Firmware: HPE BIOS Version v2.12 12/13/2023 released Dec-2023  
File System: btrfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	128	44.2	1330	44.5	1330	<b>44.3</b>	<b>1330</b>	128	44.4	1330	<b>44.4</b>	<b>1330</b>	44.5	1330
607.cactuBSSN_s	128	33.0	505	<b>33.3</b>	<b>501</b>	33.4	500	128	33.0	505	<b>33.3</b>	<b>501</b>	33.4	500
619.lbm_s	128	<b>19.9</b>	<b>263</b>	20.2	259	19.9	263	128	<b>19.9</b>	<b>263</b>	20.2	259	19.9	263
621.wrf_s	128	<b>66.2</b>	<b>200</b>	66.2	200	66.3	199	128	<b>66.2</b>	<b>200</b>	66.2	200	66.3	199
627.cam4_s	128	<b>37.0</b>	<b>239</b>	37.2	238	36.3	244	128	<b>36.9</b>	<b>240</b>	37.4	237	36.4	244
628.pop2_s	128	111	107	109	109	<b>109</b>	<b>109</b>	128	111	107	109	109	<b>109</b>	<b>109</b>
638.imagick_s	128	17.1	846	<b>17.0</b>	<b>849</b>	17.0	850	128	17.1	846	<b>17.0</b>	<b>849</b>	17.0	850
644.nab_s	128	20.5	853	<b>20.5</b>	<b>854</b>	20.5	854	128	20.5	853	<b>20.5</b>	<b>854</b>	20.5	854
649.fotonik3d_s	128	<b>49.6</b>	<b>184</b>	49.7	183	49.3	185	128	<b>49.6</b>	<b>184</b>	49.7	183	49.3	185
654.roms_s	128	<b>22.9</b>	<b>688</b>	23.2	678	22.9	689	128	<b>22.9</b>	<b>688</b>	23.2	678	22.9	689

**SPECSpeed®2017\_fp\_base = 391**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
tuned-adm profile was set to throughput-performance using "tuned-adm profile throughput-performance"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,compact"  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"  
MALLOC\_CONF = "retain:true"  
OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)



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## General Notes (Continued)

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

The system ROM used for this result contains Intel microcode version 0x21000200 for the Intel Xeon Platinum 8592V processor.

BIOS Configuration:

Workload Profile set to General Peak Frequency Compute  
Thermal Configuration set to Maximum Cooling  
Intel Hyper-Threading set to Disabled  
Memory Patrol Scrubbing set to Disabled  
Enhanced Processor Performance Profile set to Aggressive  
Dead Block Predictor set to Enabled  
Last Level Cache (LLC) Prefetch set to Enabled  
Last Level Cache (LLC) Dead Line Allocation set to Disabled  
Workload Profile set to Custom  
Adjacent Sector Prefetch set to Disabled  
Minimum Processor Idle Power Package C-State set to No Package State

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Tue Feb 27 00:44:28 2024

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

-----  
1. uname -a  
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT\_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)  
x86\_64 x86\_64 x86\_64 GNU/Linux

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```
-----
2. w
   00:44:28 up 3 min,  0 users,  load average: 0.08, 0.03, 0.00
USER      TTY      FROM          LOGIN@   IDLE   JCPU   PCPU   WHAT
```

```
-----
3. Username
   From environment variable $USER:  root
```

```
-----
4. ulimit -a
   core file size          (blocks, -c) unlimited
   data seg size           (kbytes, -d) unlimited
   scheduling priority     (-e) 0
   file size               (blocks, -f) unlimited
   pending signals        (-i) 2062646
   max locked memory       (kbytes, -l) 64
   max memory size         (kbytes, -m) unlimited
   open files              (-n) 1024
   pipe size               (512 bytes, -p) 8
   POSIX message queues    (bytes, -q) 819200
   real-time priority      (-r) 0
   stack size              (kbytes, -s) unlimited
   cpu time                (seconds, -t) unlimited
   max user processes      (-u) 2062646
   virtual memory          (kbytes, -v) unlimited
   file locks              (-x) unlimited
```

```
-----
5. sysinfo process ancestry
   /usr/lib/systemd/systemd --switched-root --system --deserialize 29
   sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
   sshd: root@notty
   bash -c cd $SPEC/ && $SPEC/fpspeedTP.sh
   runcpu --nobuild --action validate --define default-platform-flags -c
     ic2023.2.3-lin-core-avx512-speed-20231121.cfg --define cores=128 --tune base,peak -o all --define
     drop_caches fpspeed
   runcpu --nobuild --action validate --define default-platform-flags --configfile
     ic2023.2.3-lin-core-avx512-speed-20231121.cfg --define cores=128 --tune base,peak --output_format all
     --define drop_caches --nopower --runmode speed --tune base:peak --size refspeed fpspeed --nopreenv
     --note-preenv --logfile $SPEC/tmp/CPU2017.001/templogs/preenv.fpspeed.001.0.log --lognum 001.0
     --from_runcpu 2
   specperl $SPEC/bin/sysinfo
   $SPEC = /home/cpu2017
```

```
-----
6. /proc/cpuinfo
   model name      : INTEL(R) XEON(R) PLATINUM 8592V
   vendor_id       : GenuineIntel
   cpu family      : 6
   model           : 207
   stepping        : 2
   microcode       : 0x21000200
   bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
   cpu cores       : 64
   siblings        : 64
   2 physical ids (chips)
   128 processors (hardware threads)
   physical id 0: core ids 0-63
```

(Continued on next page)



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## Platform Notes (Continued)

physical id 1: core ids 0-63  
physical id 0: apicids  
0, 2, 4, 6, 8, 10, 12, 14, 16, 18, 20, 22, 24, 26, 28, 30, 32, 34, 36, 38, 40, 42, 44, 46, 48, 50, 52, 54, 56, 58, 60, 62, 64, 66, 68, 70, 72, 74, 76, 78, 80, 82, 84, 86, 88, 90, 92, 94, 96, 98, 100, 102, 104, 106, 108, 110, 112, 114, 116, 118, 120, 122, 124, 126  
physical id 1: apicids  
128, 130, 132, 134, 136, 138, 140, 142, 144, 146, 148, 150, 152, 154, 156, 158, 160, 162, 164, 166, 168, 170, 172, 174, 176, 178, 180, 182, 184, 186, 188, 190, 192, 194, 196, 198, 200, 202, 204, 206, 208, 210, 212, 214, 216, 218, 220, 222, 224, 226, 228, 230, 232, 234, 236, 238, 240, 242, 244, 246, 248, 250, 252, 254  
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.37.4:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                128
On-line CPU(s) list:   0-127
Vendor ID:             GenuineIntel
Model name:            INTEL(R) XEON(R) PLATINUM 8592V
CPU family:            6
Model:                 207
Thread(s) per core:    1
Core(s) per socket:    64
Socket(s):             2
Stepping:              2
BogoMIPS:              4000.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                        ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                        sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                        lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                        invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                        vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep
                        bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                        xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                        cqm_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts hfi
                        avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                        avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
                        cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
                        arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities

Virtualization:        VT-x
L1d cache:             6 MiB (128 instances)
L1i cache:             4 MiB (128 instances)
L2 cache:              256 MiB (128 instances)
L3 cache:              640 MiB (2 instances)
NUMA node(s):         2
NUMA node0 CPU(s):    0-31,64-95
NUMA node1 CPU(s):    32-63,96-127
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:    Not affected
Vulnerability Mds:     Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
```

(Continued on next page)



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## Platform Notes (Continued)

Vulnerability Retbleed: Not affected  
 Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp  
 Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization  
 Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS SW sequence  
 Vulnerability Srbds: Not affected  
 Vulnerability Tsx async abort: Not affected

From `lscpu --cache:`

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	6M	12	Data	1	64	1	64
L1i	32K	4M	8	Instruction	1	64	1	64
L2	2M	256M	16	Unified	2	2048	1	64
L3	320M	640M	20	Unified	3	262144	1	64

8. `numactl --hardware`

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 2 nodes (0-1)
node 0 cpus: 0-31,64-95
node 0 size: 257691 MB
node 0 free: 256663 MB
node 1 cpus: 32-63,96-127
node 1 size: 257993 MB
node 1 free: 257319 MB
node distances:
node  0  1
  0:  10  20
  1:  20  10
```

9. `/proc/meminfo`

MemTotal: 528060852 kB

10. `who -r`

run-level 3 Feb 27 00:43

11. Systemd service manager version: `systemd 249 (249.16+suse.171.gdad0071f15)`

```
Default Target Status
multi-user      running
```

12. Services, from `systemctl list-unit-files`

```
STATE UNIT FILES
enabled apparmor auditd cron getty@ irqbalance issue-generator kbdsettings lvm2-monitor postfix
purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4
wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs
disabled blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell
grub2-once haveged haveged-switch-root issue-add-ssh-keys kexec-load lunmask
rpmconfigcheck serial-getty@ systemd-boot-check-no-failures systemd-network-generator
systemd-sysext systemd-time-wait-sync systemd-timesyncd tuned
indirect wickedd
```

13. Linux kernel boot-time arguments, from `/proc/cmdline`

```
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
root=UUID=930f8b53-ff67-403e-9053-e3aa4fef0050
```

(Continued on next page)



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## Platform Notes (Continued)

```
splash=silent
mitigations=auto
quiet
security=apparmor
```

```
-----
14. cpupower frequency-info
analyzing CPU 0:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes
```

```
-----
15. tuned-adm active
  Current active profile: throughput-performance
```

```
-----
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space     2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio     10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 20
vm.dirty_writeback_centisecs  500
vm.dirtytime_expire_seconds   43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio         1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy     0
vm.nr_overcommit_hugepages    0
vm.swappiness                  10
vm.watermark_boost_factor     15000
vm.watermark_scale_factor     10
vm.zone_reclaim_mode          0
```

```
-----
17. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled        [always] madvice never
hpage_pmd_size 2097152
shmem_enabled  always within_size advise [never] deny force
```

```
-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs  10000
```

```
-----
19. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP5
```

(Continued on next page)



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## Platform Notes (Continued)

### 20. Disk information

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda3	btrfs	446G	212G	234G	48%	/home

### 21. /sys/devices/virtual/dmi/id

Vendor: HPE  
 Product: ProLiant ML350 Gen11  
 Product Family: ProLiant  
 Serial: CNX20800P4

### 22. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x Hynix HMC88AGBRA193N 32 GB 2 rank 5600, configured at 4800

### 23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE  
 BIOS Version: 2.12  
 BIOS Date: 12/13/2023  
 BIOS Revision: 2.12  
 Firmware Revision: 1.54

## Compiler Version Notes

C | 619.lbm\_s(base, peak) 638.imagick\_s(base, peak) 644.nab\_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C++, C, Fortran | 607.cactuBSSN\_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak) 654.roms\_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

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Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak) 628.pop2\_s(base, peak)  
-----

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
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-----

## Base Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64

607.cactuBSSN\_s: -DSPEC\_LP64

619.lbm\_s: -DSPEC\_LP64

621.wrf\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian

627.cam4\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG

628.pop2\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian

-assume byterecl

638.imagick\_s: -DSPEC\_LP64

644.nab\_s: -DSPEC\_LP64

649.fotonik3d\_s: -DSPEC\_LP64

654.roms\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math

-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant ML350 Gen11**

(2.00 GHz, Intel Xeon Platinum 8592V)

**SPECspeed®2017\_fp\_base = 391**

**SPECspeed®2017\_fp\_peak = 391**

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Feb-2024

**Hardware Availability:** Feb-2024

**Software Availability:** Dec-2023

## Base Optimization Flags (Continued)

C benchmarks (continued):

```
-DSPEC_OPENMP -Wno-implicit-int -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

## Peak Compiler Invocation

C benchmarks:

```
icx
```

Fortran benchmarks:

```
ifx
```

Benchmarks using both Fortran and C:

```
ifx icx
```

Benchmarks using Fortran, C, and C++:

```
icpx icx ifx
```

## Peak Portability Flags

Same as Base Portability Flags



# SPEC CPU®2017 Floating Point Speed Result

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**ProLiant ML350 Gen11**

(2.00 GHz, Intel Xeon Platinum 8592V)

**SPECSpeed®2017\_fp\_base = 391**

**SPECSpeed®2017\_fp\_peak = 391**

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Feb-2024  
**Hardware Availability:** Feb-2024  
**Software Availability:** Dec-2023

## Peak Optimization Flags

C benchmarks:

619.lbm\_s: basepeak = yes

638.imagick\_s: basepeak = yes

644.nab\_s: basepeak = yes

Fortran benchmarks:

603.bwaves\_s: -w -m64 -Wl,-z,muldefs -DSPEC\_OPENMP -xCORE-AVX512  
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs  
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib  
-ljemalloc

649.fotonik3d\_s: basepeak = yes

654.roms\_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf\_s: basepeak = yes

627.cam4\_s: -w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC\_OPENMP  
-Wno-implicit-int -nostandard-realloc-lhs  
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib  
-ljemalloc

628.pop2\_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.xml>



# SPEC CPU®2017 Floating Point Speed Result

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ProLiant ML350 Gen11

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SPECspeed®2017\_fp\_base = 391

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**CPU2017 License:** 3

**Test Sponsor:** HPE

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**Test Date:** Feb-2024

**Hardware Availability:** Feb-2024

**Software Availability:** Dec-2023

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