



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Supermicro

GPU SuperServer SYS-741GE-TNRT
(X13DEG-QT , Intel Xeon Gold 5520+)

SPECSpeed®2017_fp_base = 286

SPECSpeed®2017_fp_peak = 287

CPU2017 License: 001176

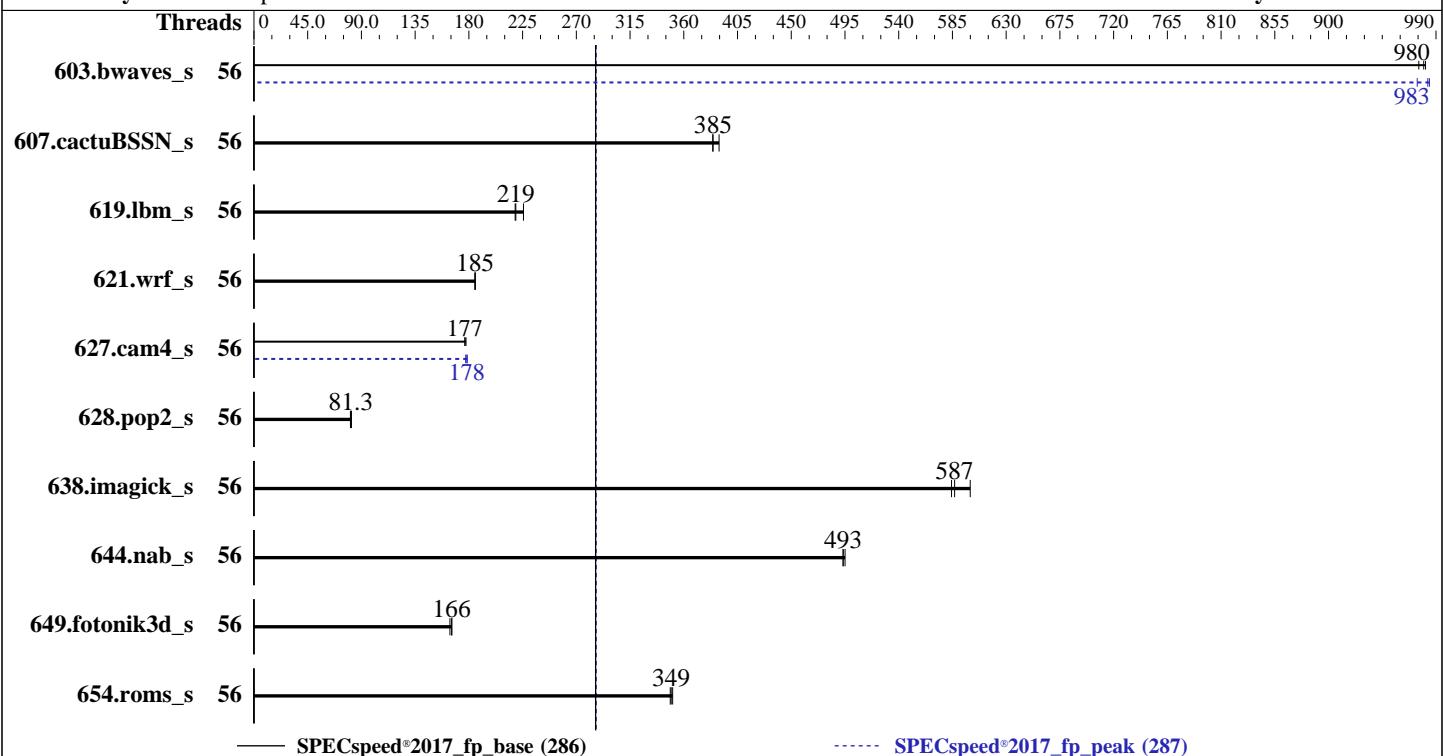
Test Date: Jan-2024

Test Sponsor: Supermicro

Hardware Availability: Dec-2023

Tested by: Supermicro

Software Availability: Dec-2023



Hardware		Software	
CPU Name:	Intel Xeon Gold 5520+	OS:	SUSE Linux Enterprise Server 15 SP5
Max MHz:	4000	Compiler:	Kernel 5.14.21-150500.53-default
Nominal:	2200	Parallel:	C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;
Enabled:	56 cores, 2 chips, 2 threads/core	Firmware:	Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;
Orderable:	2 chips	File System:	Yes
Cache L1:	32 KB I + 48 KB D on chip per core	System State:	Version 2.1 released Dec-2023
L2:	2 MB I+D on chip per core	Base Pointers:	xfs
L3:	52.5 MB I+D on chip per chip	Peak Pointers:	Run level 3 (multi-user)
Other:	None	Other:	64-bit
Memory:	1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 4800)	Power Management:	64-bit
Storage:	1 x 512 GB M.2 NVMe SSD	jemalloc memory allocator V5.0.1	
Other:	None	BIOS set to prefer performance at the cost of additional power usage.	



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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	56	60.2	980	60.5	976	60.1	981	56	59.9	984	60.6	974	60.0	983
607.cactuBSSN_s	56	43.4	384	42.8	390	43.4	385	56	43.4	384	42.8	390	43.4	385
619.lbm_s	56	23.9	219	23.2	226	23.9	219	56	23.9	219	23.2	226	23.9	219
621.wrf_s	56	71.4	185	71.4	185	71.5	185	56	71.4	185	71.4	185	71.5	185
627.cam4_s	56	49.9	178	50.2	177	50.2	177	56	49.7	178	50.0	177	49.6	179
628.pop2_s	56	146	81.3	147	80.8	145	81.6	56	146	81.3	147	80.8	145	81.6
638.imagick_s	56	24.6	587	24.0	600	24.7	584	56	24.6	587	24.0	600	24.7	584
644.nab_s	56	35.4	493	35.4	493	35.3	495	56	35.4	493	35.4	493	35.3	495
649.fotonik3d_s	56	55.5	164	55.0	166	55.0	166	56	55.5	164	55.0	166	55.0	166
654.roms_s	56	45.2	349	45.1	349	44.9	351	56	45.2	349	45.1	349	44.9	351

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"

MALLOC_CONF = "retain:true"

OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



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Platform Notes

BIOS Settings:

Power Performance Tuning = BIOS Controls EPB
ENERGY_PERF_BIAS_CFG mode = Performance
DCU Streamer Prefetcher = Disable
LLC Dead Line Alloc = Disable
KTI Prefetch = Enable
Stale AtoS = Disable
Patrol Scrub = Disable

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on 135-172-248 Fri Jan 19 17:50:08 2024
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

- 1. uname -a
- 2. w
- 3. Username
- 4. ulimit -a
- 5. sysinfo process ancestry
- 6. /proc/cpuinfo
- 7. lscpu
- 8. numactl --hardware
- 9. /proc/meminfo
- 10. who -r
- 11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
- 12. Services, from systemctl list-unit-files
- 13. Linux kernel boot-time arguments, from /proc/cmdline
- 14. cpupower frequency-info
- 15. sysctl
- 16. /sys/kernel/mm/transparent_hugepage
- 17. /sys/kernel/mm/transparent_hugepage/khugepaged
- 18. OS release
- 19. Disk information
- 20. /sys/devices/virtual/dmi/id
- 21. dmidecode
- 22. BIOS

```
1. uname -a
Linux 135-172-248 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
17:50:08 up 3:17, 1 user, load average: 6.26, 6.32, 3.79
USER   TTY      FROM          LOGIN@    IDLE    JCPU   PCPU WHAT
root   tty1     -           14:34    3:14m  0.91s  0.01s -bash
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
```

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Platform Notes (Continued)

```
data seg size          (kbytes, -d) unlimited
scheduling priority   (-e) 0
file size             (blocks, -f) unlimited
pending signals       (-i) 4126860
max locked memory    (kbytes, -l) 64
max memory size      (kbytes, -m) unlimited
open files            (-n) 1024
pipe size              (512 bytes, -p) 8
POSIX message queues  (bytes, -q) 819200
real-time priority    (-r) 0
stack size             (kbytes, -s) unlimited
cpu time               (seconds, -t) unlimited
max user processes    (-u) 4126860
virtual memory         (kbytes, -v) unlimited
file locks             (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags -c
  ic2023.2.3-lin-core-avx512-speed-20231121.cfg --define cores=56 --tune base,peak -o all --define smt-on
  --define drop_caches fpspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
  ic2023.2.3-lin-core-avx512-speed-20231121.cfg --define cores=56 --tune base,peak --output_format all
  --define smt-on --define drop_caches --nopower --runmode speed --tune base:peak --size refspeed fpspeed
  --nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.006/templogs/preenv.fpspeed.006.0.log --lognum 006.0
  --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

```
6. /proc/cpuinfo
model name      : INTEL(R) XEON(R) GOLD 5520+
vendor_id       : GenuineIntel
cpu family     : 6
model          : 207
stepping        : 2
microcode       : 0x21000200
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrp_pbrsb
cpu cores       : 28
siblings        : 56
2 physical ids (chips)
112 processors (hardware threads)
physical id 0: core ids 0-27
physical id 1: core ids 0-27
physical id 0: apicids 0-55
physical id 1: apicids 128-183
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

```
7. lscpu
```

```
From lscpu from util-linux 2.37.4:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
```

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Platform Notes (Continued)

Byte Order:	Little Endian
CPU(s):	112
On-line CPU(s) list:	0-111
Vendor ID:	GenuineIntel
Model name:	INTEL(R) XEON(R) GOLD 5520+
CPU family:	6
Model:	207
Thread(s) per core:	2
Core(s) per socket:	28
Socket(s):	2
Stepping:	2
Frequency boost:	enabled
CPU max MHz:	2201.0000
CPU min MHz:	800.0000
BogoMIPS:	4400.00
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmpfperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsaved xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts avx512vbmi umip pkru ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpocntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities
Virtualization:	VT-x
L1d cache:	2.6 MiB (56 instances)
L1i cache:	1.8 MiB (56 instances)
L2 cache:	112 MiB (56 instances)
L3 cache:	105 MiB (2 instances)
NUMA node(s):	2
NUMA node0 CPU(s):	0-27,56-83
NUMA node1 CPU(s):	28-55,84-111
Vulnerability Itlb multihit:	Not affected
Vulnerability Lltf:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Mmio stale data:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:	Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW sequence
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

```
From lscpu --cache:
  NAME  ONE-SIZE  ALL-SIZE  WAYS  TYPE      LEVEL  SETS  PHY-LINE  COHERENCY-SIZE
  L1d    48K       2.6M     12 Data        1      64          1            64
  L1i    32K       1.8M     8 Instruction   1      64          1            64
  L2     2M        112M    16 Unified      2     2048          1            64
  L3    52.5M      105M    15 Unified      3     57344         1            64
```

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Platform Notes (Continued)

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 2 nodes (0-1)
node 0 cpus: 0-27,56-83
node 0 size: 515684 MB
node 0 free: 514572 MB
node 1 cpus: 28-55,84-111
node 1 size: 516060 MB
node 1 free: 508298 MB
node distances:
node    0    1
 0:   10   21
 1:   21   10
```

9. /proc/meminfo

```
MemTotal:      1056507788 kB
```

10. who -r

```
run-level 3 Jan 19 14:33
```

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)

```
Default Target      Status
multi-user          running
```

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance issue-generator kbdsettings klog lvm2-monitor nsqd nvmefc-boot-connections postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore wickedd wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info firewalld gpm grub2-once haveged haveged-switch-root ipmi ipmievfd issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nvmf-autocreate rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2 vncserver@
indirect	wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
```

```
root=UUID=9de9855c-b179-4e5b-8330-3742dedc18b2
```

```
splash=silent
```

```
mitigations=auto
```

```
quiet
```

```
security=apparmor
```

14. cpupower frequency-info

```
analyzing CPU 0:
```

```
current policy: frequency should be within 800 MHz and 2.20 GHz.
```

```
The governor "ondemand" may decide which speed to use
```

```
within this range.
```

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Platform Notes (Continued)

```
boost state support:  
Supported: yes  
Active: yes
```

```
-----  
15. sysctl  
kernel.numa_balancing          1  
kernel.randomize_va_space      2  
vm.compaction_proactiveness   20  
vm.dirty_background_bytes     0  
vm.dirty_background_ratio     10  
vm.dirty_bytes                0  
vm.dirty_expire_centisecs    3000  
vm.dirty_ratio                20  
vm.dirty_writeback_centisecs  500  
vm.dirtytime_expire_seconds   43200  
vm.extfrag_threshold          500  
vm.min_unmapped_ratio         1  
vm.nr_hugepages               0  
vm.nr_hugepages_mempolicy     0  
vm.nr_overcommit_hugepages   0  
vm.swappiness                 60  
vm.watermark_boost_factor    15000  
vm.watermark_scale_factor     10  
vm.zone_reclaim_mode          0
```

```
-----  
16. /sys/kernel/mm/transparent_hugepage  
defrag           always defer defer+madvise [madvise] never  
enabled          [always] madvise never  
hpage_pmd_size  2097152  
shmem_enabled   always within_size advise [never] deny force
```

```
-----  
17. /sys/kernel/mm/transparent_hugepage/khugepaged  
alloc_sleep_millisecs  60000  
defrag                  1  
max_ptes_none          511  
max_ptes_shared        256  
max_ptes_swap          64  
pages_to_scan          4096  
scan_sleep_millisecs   10000
```

```
-----  
18. OS release  
From /etc/*-release /etc/*-version  
os-release SUSE Linux Enterprise Server 15 SP5
```

```
-----  
19. Disk information  
SPEC is set to: /home/cpu2017  
Filesystem  Type  Size  Used Avail Use% Mounted on  
/dev/nvme0n1p2  xfs  475G  34G  441G  8% /
```

```
-----  
20. /sys/devices/virtual/dmi/id  
Vendor:       Supermicro  
Product:      X13DEG-QT  
Product Family: Family  
Serial:       1234567890
```

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Platform Notes (Continued)

21. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

14x SK Hynix HMC94AGBRA181N 64 GB 2 rank 5600, configured at 4800
2x SK Hynix HMC94AGBRA184N 64 GB 2 rank 5600, configured at 4800

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 2.1
BIOS Date: 12/12/2023
BIOS Revision: 5.32

Compiler Version Notes

=====

C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
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=====

=====

C++, C, Fortran | 607.cactuBSSN_s(base, peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
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=====

=====

Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)

=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

=====

Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)

=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
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Base Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp
-DSPEC_OPENMP -Wno-implicit-int -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math

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Supermicro

GPU SuperServer SYS-741GE-TNRT
(X13DEG-QT , Intel Xeon Gold 5520+)

SPECspeed®2017_fp_base = 286

SPECspeed®2017_fp_peak = 287

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Jan-2024

Hardware Availability: Dec-2023

Software Availability: Dec-2023

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp
-DSPEC_OPENMP -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP -Wno-implicit-int
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Peak Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

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Peak Optimization Flags (Continued)

Fortran benchmarks:

```
603.bwaves_s: -w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512
-Ofast -ffast-math -futto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc
```

```
649.fotonik3d_s: basepeak = yes
```

```
654.roms_s: basepeak = yes
```

Benchmarks using both Fortran and C:

```
621.wrf_s: basepeak = yes
```

```
627.cam4_s: -w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -futto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc
```

```
628.pop2_s: basepeak = yes
```

Benchmarks using Fortran, C, and C++:

```
607.cactuBSSN_s: basepeak = yes
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-EMR-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-EMR-revC.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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