



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS520A-E12-RS12U
(2.20 GHz, AMD EPYC 9734)

SPECSpeed®2017_fp_base = 292

SPECSpeed®2017_fp_peak = 293

CPU2017 License: 9016

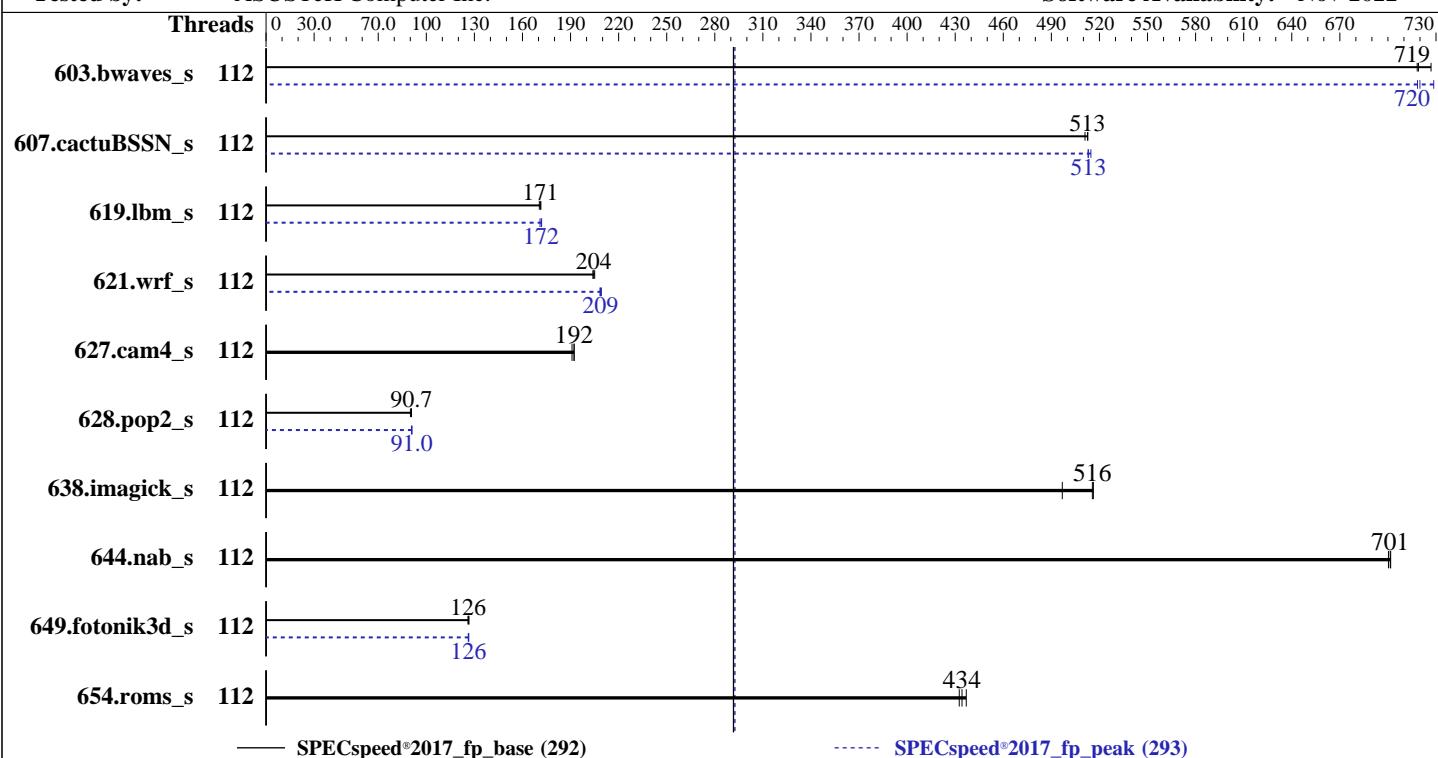
Test Date: Nov-2023

Test Sponsor: ASUSTeK Computer Inc.

Hardware Availability: Jun-2023

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2022



Hardware

CPU Name: AMD EPYC 9734
Max MHz: 3000
Nominal: 2200
Enabled: 112 cores, 1 chip
Orderable: 1 chip
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 256 MB I+D on chip per chip,
16 MB shared / 7 cores
Other: None
Memory: 768 GB (12 x 64 GB 2Rx4 PC5-4800B-R)
Storage: 1 x 1.6 TB PCIe NVMe SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP4 (x86_64)
Kernel 5.14.21-150400.22-default
Compiler: C/C++/Fortran: Version 4.0.0 of AOCC
Parallel: Yes
Firmware: Version 1201 released Aug-2023
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS520A-E12-RS12U
(2.20 GHz, AMD EPYC 9734)

SPECspeed®2017_fp_base = 292

SPECspeed®2017_fp_peak = 293

CPU2017 License: 9016

Test Date: Nov-2023

Test Sponsor: ASUSTeK Computer Inc.

Hardware Availability: Jun-2023

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2022

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	112	81.2	727	82.1	718	82.0	719	112	81.0	729	81.9	720	82.1	718
607.cactubSSN_s	112	32.5	513	32.5	513	32.6	511	112	32.4	515	32.5	513	32.5	513
619.lbm_s	112	30.5	171	30.6	171	30.7	171	112	30.5	172	30.5	172	30.6	171
621.wrf_s	112	64.9	204	64.7	204	64.5	205	112	63.4	209	63.5	208	63.2	209
627.cam4_s	112	46.1	192	46.1	192	46.4	191	112	46.1	192	46.1	192	46.4	191
628.pop2_s	112	131	90.7	131	90.7	132	90.1	112	130	91.0	131	91.0	131	90.7
638.imagick_s	112	29.0	497	27.9	516	28.0	516	112	29.0	497	27.9	516	28.0	516
644.nab_s	112	24.9	701	24.9	700	24.9	702	112	24.9	701	24.9	700	24.9	702
649.fotonik3d_s	112	72.0	127	72.3	126	72.2	126	112	72.1	126	72.3	126	72.1	126
654.roms_s	112	36.0	437	36.3	434	36.4	433	112	36.0	437	36.3	434	36.4	433
SPECspeed®2017_fp_base = 292							SPECspeed®2017_fp_peak = 293							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit
OS set to performance mode via cpupower frequency-set -g performance
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
To always enable THP for peak runs of:
603.bwaves_s, 607.cactubSSN_s, 619.lbm_s, 627.cam4_s, 628.pop2_s, 638.imagick_s, 644.nab_s, 649.fotonik3d_s:
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled; echo always > /sys/kernel/mm/transparent_hugepage/defrag'
run as root.
To disable THP for peak runs of 621.wrf_s:

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS520A-E12-RS12U
(2.20 GHz, AMD EPYC 9734)

SPECSpeed®2017_fp_base = 292

SPECSpeed®2017_fp_peak = 293

CPU2017 License: 9016

Test Date: Nov-2023

Test Sponsor: ASUSTeK Computer Inc.

Hardware Availability: Jun-2023

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2022

Operating System Notes (Continued)

```
'echo never > /sys/kernel/mm/transparent_hugepage/enabled; echo always > /sys/kernel/mm/transparent_hugepage/defrag'  
run as root.
```

To enable THP only on request for peak runs of 654.roms_s:

```
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled; echo madvise > /sys/kernel/mm/transparent_hugepage/defrag'  
run as root.
```

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-111"  
LD_LIBRARY_PATH = "/aocczn4/amd_speed_aocc400_znver4_A_lib/lib:  
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"  
MALLOC_CONF = "oversize_threshold:0,retain:true"  
OMP_DYNAMIC = "false"  
OMP_SCHEDULE = "static"  
OMP_STACKSIZE = "128M"  
OMP_THREAD_LIMIT = "112"
```

Environment variables set by runcpu during the 603.bwaves_s peak run:

```
GOMP_CPU_AFFINITY = "0-111"
```

Environment variables set by runcpu during the 607.cactubSSN_s peak run:

```
GOMP_CPU_AFFINITY = "0-111"
```

Environment variables set by runcpu during the 619.lbm_s peak run:

```
GOMP_CPU_AFFINITY = "0-111"
```

Environment variables set by runcpu during the 621.wrf_s peak run:

```
GOMP_CPU_AFFINITY = "0-111"
```

Environment variables set by runcpu during the 628.pop2_s peak run:

```
GOMP_CPU_AFFINITY = "0-111"
```

Environment variables set by runcpu during the 649.fotonik3d_s peak run:

```
GOMP_CPU_AFFINITY = "0-111"
```

```
PGHPF_ZMEM = "yes"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configuration:
SR-IOV Support = Disabled
SVM Mode = Disabled

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS520A-E12-RS12U
(2.20 GHz, AMD EPYC 9734)

SPECSpeed®2017_fp_base = 292

SPECSpeed®2017_fp_peak = 293

CPU2017 License: 9016

Test Date: Nov-2023

Test Sponsor: ASUSTeK Computer Inc.

Hardware Availability: Jun-2023

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2022

Platform Notes (Continued)

NUMA nodes per socket = NPS1

Determinism Control = Manual

Determinism Enable = Power

Engine Boost = Aggressive

TDP Control = Manual

TDP = 400

PPT Control = Manual

PPT = 400

SMT Control = Disable

BMC Configuration:

Fan mode = Full speed mode

```
Sysinfo program /aocczn4/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Thu Nov  9 15:21:15 2023
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

- 1. uname -a
- 2. w
- 3. Username
- 4. ulimit -a
- 5. sysinfo process ancestry
- 6. /proc/cpuinfo
- 7. lscpu
- 8. numactl --hardware
- 9. /proc/meminfo
- 10. who -r
- 11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
- 12. Services, from systemctl list-unit-files
- 13. Linux kernel boot-time arguments, from /proc/cmdline
- 14. cpupower frequency-info
- 15. tuned-adm active
- 16. sysctl
- 17. /sys/kernel/mm/transparent_hugepage
- 18. /sys/kernel/mm/transparent_hugepage/khugepaged
- 19. OS release
- 20. Disk information
- 21. /sys/devices/virtual/dmi/id
- 22. dmidecode
- 23. BIOS

```
1. uname -a
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
15:21:15 up 1:02, 1 user, load average: 60.38, 93.51, 99.26
USER   TTY      FROM          LOGIN@    IDLE   JCPU   PCPU WHAT
root   tty1     -           14:20     1:00m  1.30s  0.10s /bin/bash ./amd_speed_aocc400_znver4_A1.sh
```

```
3. Username
From environment variable $USER: root
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS520A-E12-RS12U
(2.20 GHz, AMD EPYC 9734)

SPECspeed®2017_fp_base = 292

SPECspeed®2017_fp_peak = 293

CPU2017 License: 9016

Test Date: Nov-2023

Test Sponsor: ASUSTeK Computer Inc.

Hardware Availability: Jun-2023

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2022

Platform Notes (Continued)

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 3093198
max locked memory       (kbytes, -l) 2097152
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size               (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes       (-u) 3093198
virtual memory           (kbytes, -v) unlimited
file locks              (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
/bin/bash ./speed.sh
python3 ./run_amd_speed_aocc400_znver4_A1.py
/bin/bash ./amd_speed_aocc400_znver4_A1.sh
runcpu --config amd_speed_aocc400_znver4_A1.cfg --tune all --reportable --iterations 3 fpspeed
runcpu --configfile amd_speed_aocc400_znver4_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.182/templogs/preenv.fpspeed.182.0.log --lognum 182.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /aocczn4
```

```
6. /proc/cpuinfo
model name      : AMD EPYC 9734 112-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 25
model          : 160
stepping        : 2
microcode       : 0xa000212
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 3584 4K pages
cpu cores       : 112
siblings        : 112
1 physical ids (chips)
112 processors (hardware threads)
physical id 0: core ids
0-6,16-22,32-38,48-54,64-70,80-86,96-102,112-118,128-134,144-150,160-166,176-182,192-198,208-214,224-230,
240-246
physical id 0: apicids
0-6,16-22,32-38,48-54,64-70,80-86,96-102,112-118,128-134,144-150,160-166,176-182,192-198,208-214,224-230,
240-246
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

```
7. lscpu
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS520A-E12-RS12U
(2.20 GHz, AMD EPYC 9734)

SPECspeed®2017_fp_base = 292

SPECspeed®2017_fp_peak = 293

CPU2017 License: 9016

Test Date: Nov-2023

Test Sponsor: ASUSTeK Computer Inc.

Hardware Availability: Jun-2023

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2022

Platform Notes (Continued)

From lscpu from util-linux 2.37.2:

```

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 52 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 112
On-line CPU(s) list: 0-111
Vendor ID: AuthenticAMD
Model name: AMD EPYC 9734 112-Core Processor
CPU family: 25
Model: 160
Thread(s) per core: 1
Core(s) per socket: 112
Socket(s): 1
Stepping: 2
Frequency boost: enabled
CPU max MHz: 2999.2180
CPU min MHz: 1500.0000
BogoMIPS: 4399.75
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
       clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
       constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmpf rapl
       pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
       popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy
       abm sse4a misalignsns 3dnopprefetch osvw ibs skinit wdt tce topoext
       perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3
       invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmil
       avx2 smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adix smap
       avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
       xsaves xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local
       avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd and_ppin arat npt lbrv
       svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
       pausefilter pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi
       umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
       avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_l1d
Virtualization: AMD-V
L1d cache: 3.5 MiB (112 instances)
L1i cache: 3.5 MiB (112 instances)
L2 cache: 112 MiB (112 instances)
L3 cache: 256 MiB (16 instances)
NUMA node(s): 1
NUMA node0 CPU(s): 0-111
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP disabled, RSB
filling
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	3.5M	8	Data	1	64	1	64
L1i	32K	3.5M	8	Instruction	1	64	1	64
L2	1M	112M	8	Unified	2	2048	1	64
L3	16M	256M	16	Unified	3	16384	1	64

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS520A-E12-RS12U
(2.20 GHz, AMD EPYC 9734)

SPECSpeed®2017_fp_base = 292

SPECSpeed®2017_fp_peak = 293

CPU2017 License: 9016

Test Date: Nov-2023

Test Sponsor: ASUSTeK Computer Inc.

Hardware Availability: Jun-2023

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2022

Platform Notes (Continued)

```
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
available: 1 nodes (0)
node 0 cpus: 0-111
node 0 size: 773324 MB
node 0 free: 772244 MB
node distances:
node 0
 0: 10
```

```
9. /proc/meminfo
MemTotal:      791884092 kB
```

```
10. who -r
run-level 3 Nov 9 14:19
```

```
11. Systemd service manager version: systemd 249 (249.11+use.124.g2bc0b2c447)
Default Target      Status
multi-user          running
```

```
12. Services, from systemctl list-unit-files
STATE            UNIT FILES
enabled          YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ haveged
                  irqbalance issue-generator kbdsettings klog lvm2-monitor nsqd nvmefc-boot-connections
                  postfix purge-kernels rollback rsyslog smartd sshd wicked wickedd-auto4 wickedd-dhcp4
                  wickedd-dhcp6 wickedd-nanny
enabled-runtime   systemd-remount-fs
disabled         autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                  chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
                  firewalld gpm grub2-once haveged-switch-root hwloc-dump-hwdata ipmi ipmiev
                  issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap
                  nvmf-autoconnect rdisc rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts
                  snmpd snmptrapd svnservice systemd-boot-check-no-failures systemd-network-generator
                  systemd-sysext systemd-time-wait-sync systemd-timesyncd tuned udisks2
indirect          wickedd
```

```
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
root=UUID=9bcf0374-b29f-4a4c-932e-9c0e90fb0803
splash=silent
mitigations=auto
quiet
```

```
14. cpupower frequency-info
analyzing CPU 0:
current policy: frequency should be within 1.50 GHz and 2.20 GHz.
The governor "performance" may decide which speed to use
within this range.
boost state support:
Supported: yes
Active: yes
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS520A-E12-RS12U
(2.20 GHz, AMD EPYC 9734)

SPECspeed®2017_fp_base = 292

SPECspeed®2017_fp_peak = 293

CPU2017 License: 9016

Test Date: Nov-2023

Test Sponsor: ASUSTeK Computer Inc.

Hardware Availability: Jun-2023

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2022

Platform Notes (Continued)

15. tuned-adm active

It seems that tuned daemon is not running, preset profile is not activated.
Preset profile: throughput-performance

16. sysctl

kernel.numa_balancing	0
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	1
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	1

17. /sys/kernel/mm/transparent_hugepage

defrag	[always] defer defer+madvise madvise never
enabled	[always] madvise never
hpae_pmd_size	2097152
shmem_enabled	always within_size advise [never] deny force

18. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

19. OS release

From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP4

20. Disk information

SPEC is set to: /aocczn4

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/nvme0n1p8	xfs	500G	290G	211G	58%	/

21. /sys/devices/virtual/dmi/id

Vendor:	ASUSTeK COMPUTER INC.
Product:	RS520A-E12-RS12U
Product Family:	Server

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS520A-E12-RS12U
(2.20 GHz, AMD EPYC 9734)

SPECspeed®2017_fp_base = 292

SPECspeed®2017_fp_peak = 293

CPU2017 License: 9016

Test Date: Nov-2023

Test Sponsor: ASUSTeK Computer Inc.

Hardware Availability: Jun-2023

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2022

Platform Notes (Continued)

Serial: 123456789012

22. dmidecode

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

12x Samsung M321R8GA0BB0-CQKVG 64 GB 2 rank 4800

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends Inc.
BIOS Version: 1201
BIOS Date: 08/25/2023
BIOS Revision: 12.1

Compiler Version Notes

=====

C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)

=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aoxx-compiler-4.0.0/bin

=====

C++, C, Fortran | 607.cactusBSSN_s(base, peak)

=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aoxx-compiler-4.0.0/bin

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aoxx-compiler-4.0.0/bin

=====

Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)

=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aoxx-compiler-4.0.0/bin

=====

Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)

=====

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS520A-E12-RS12U
(2.20 GHz, AMD EPYC 9734)

SPECspeed®2017_fp_base = 292

SPECspeed®2017_fp_peak = 293

CPU2017 License: 9016

Test Date: Nov-2023

Test Sponsor: ASUSTeK Computer Inc.

Hardware Availability: Jun-2023

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2022

Compiler Version Notes (Continued)

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
```

Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

```
603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64
628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -fsto -fstruct-layout=7
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS520A-E12-RS12U
(2.20 GHz, AMD EPYC 9734)

SPECspeed®2017_fp_base = 292

SPECspeed®2017_fp_peak = 293

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Nov-2023

Hardware Availability: Jun-2023

Software Availability: Nov-2022

Base Optimization Flags (Continued)

C benchmarks (continued):

```
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver4
-fveclib=AMDLIB -ffast-math -fopenmp -flto -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-mllvm -reduce-array-computations=3 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4
-fveclib=AMDLIB -ffast-math -fopenmp -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
-fveclib=AMDLIB -ffast-math -fopenmp -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang
```

Base Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS520A-E12-RS12U
(2.20 GHz, AMD EPYC 9734)

SPECspeed®2017_fp_base = 292

SPECspeed®2017_fp_peak = 293

CPU2017 License: 9016

Test Date: Nov-2023

Test Sponsor: ASUSTeK Computer Inc.

Hardware Availability: Jun-2023

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2022

Base Other Flags (Continued)

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
619.lbm_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIB -ffast-math -fopenmp
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fopenmp=libomp -lomp -lamdlibm -lamdaloc -lflang
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS520A-E12-RS12U
(2.20 GHz, AMD EPYC 9734)

SPECSpeed®2017_fp_base = 292

SPECSpeed®2017_fp_peak = 293

CPU2017 License: 9016

Test Date: Nov-2023

Test Sponsor: ASUSTeK Computer Inc.

Hardware Availability: Jun-2023

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2022

Peak Optimization Flags (Continued)

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver4 -fveclib=AMDLIBM -ffast-math
-fopenmp -Mrecursive -mllvm -reduce-array-computations=3
-fvector-transform -fscalar-transform -fopenmp=libomp
-lomp -lamdlibm -lamdaloc -flang

649.fotonik3d_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver4 -fveclib=AMDLIBM -ffast-math
-fopenmp -flto -Mrecursive
-mllvm -reduce-array-computations=3 -zopt -fopenmp=libomp
-lomp -lamdlibm -lamdaloc -flang

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-O3 -Mrecursive -funroll-loops -mllvm -lsr-in-nested-loop
-fopenmp=libomp -lomp -lamdlibm -lamdaloc -flang

627.cam4_s: basepeak = yes

628.pop2_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS520A-E12-RS12U
(2.20 GHz, AMD EPYC 9734)

SPECspeed®2017_fp_base = 292

SPECspeed®2017_fp_peak = 293

CPU2017 License: 9016

Test Date: Nov-2023

Test Sponsor: ASUSTeK Computer Inc.

Hardware Availability: Jun-2023

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2022

Peak Optimization Flags (Continued)

628.pop2_s (continued):

```
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-Mrecursive -fvector-transform -fscalar-transform
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver4
-fveclib=AMDLIB -ffast-math -fopenmp -flto -fstruct-layout=9
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -finline-aggressive -mllvm -unroll-threshold=100
-Mrecursive -fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Peak Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-return-type -Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-AMD-K14-V1.6.html>
<http://www.spec.org/cpu2017/flags/aocc400-flags-asusv01.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-AMD-K14-V1.6.xml>
<http://www.spec.org/cpu2017/flags/aocc400-flags-asusv01.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2023-11-09 02:21:15-0500.

Report generated on 2024-01-03 17:30:35 by CPU2017 PDF formatter v6716.

Originally published on 2024-01-02.