



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL345 Gen11

(4.10 GHz, AMD EPYC 9174F)

**SPECSpeed®2017\_fp\_base = 165**

**SPECSpeed®2017\_fp\_peak = 169**

CPU2017 License: 3

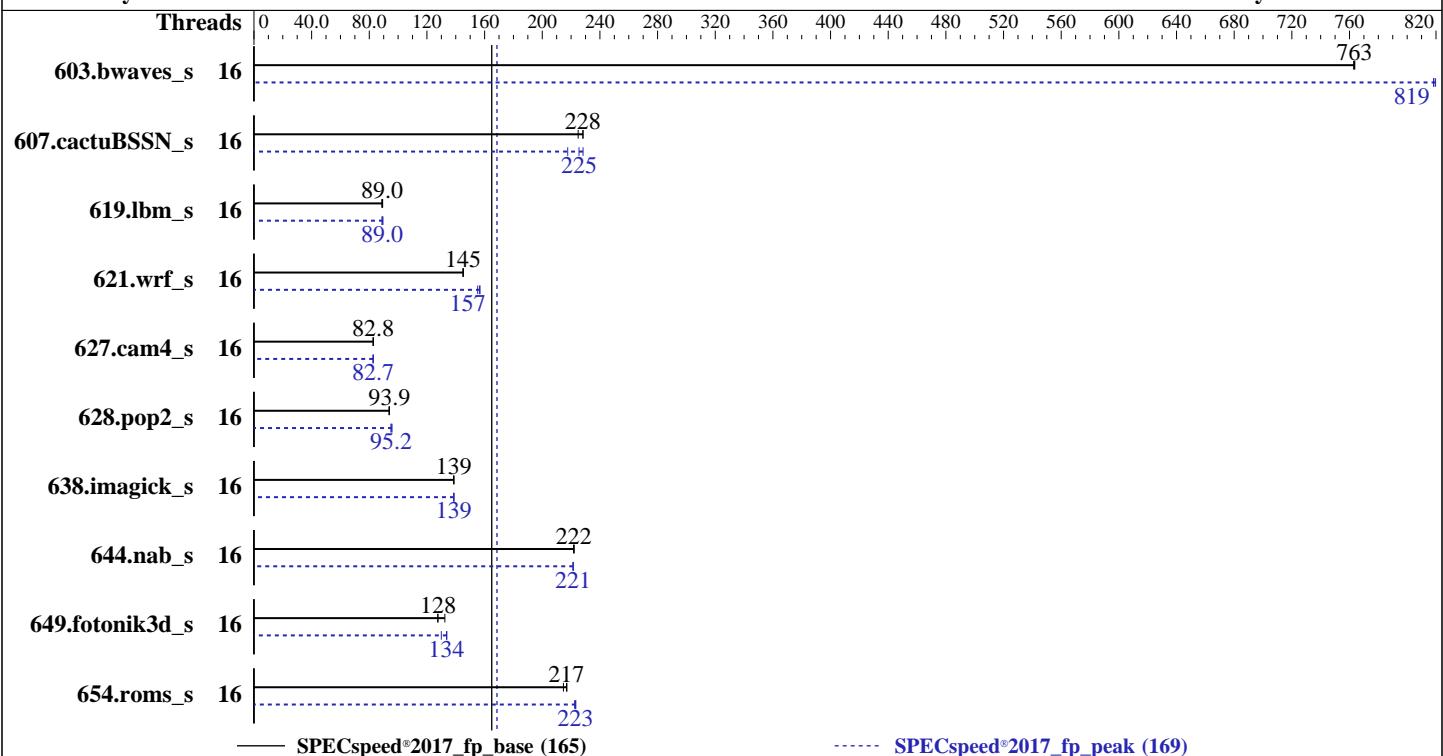
**Test Date:** Feb-2023

**Test Sponsor:** HPE

**Hardware Availability:** Nov-2022

**Tested by:** HPE

**Software Availability:** Nov-2022



## Hardware

CPU Name: AMD EPYC 9174F  
 Max MHz: 4400  
 Nominal: 4100  
 Enabled: 16 cores, 1 chip  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 256 MB I+D on chip per chip,  
     32 MB shared / 2 cores  
 Other: None  
 Memory: 384 GB (12 x 32 GB 2Rx8 PC5-4800B-R)  
 Storage: 1 x 1.6 TB NVMe SSD, RAID 0  
 Other: None

## Software

OS: Ubuntu 22.04.1 LTS  
 Compiler: Kernel 5.15.0-53-generic  
 Parallel: C/C++/Fortran: Version 4.0.0 of AOCC  
 Firmware: Yes  
 File System: HPE BIOS Version v1.12 11/24/2022 released  
 System State: Nov-2022  
 Base Pointers: ext4  
 Peak Pointers: Run level 5 (multi-user)  
 Other: 64-bit  
 Power Management: Peak Pointers: 64-bit  
 Other: None  
 Power Management: BIOS and OS set to prefer performance at  
                   the cost of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	16	77.3	764	<b>77.3</b>	<b>763</b>	77.3	763	16	72.1	818	<b>72.1</b>	<b>819</b>	72.0	820
607.cactuBSSN_s	16	73.0	228	<b>73.1</b>	<b>228</b>	74.1	225	16	76.6	218	73.0	228	<b>73.9</b>	<b>225</b>
619.lbm_s	16	58.7	89.3	<b>58.8</b>	<b>89.0</b>	59.0	88.7	16	58.9	88.9	58.5	89.5	<b>58.8</b>	<b>89.0</b>
621.wrf_s	16	91.1	145	91.2	145	<b>91.1</b>	<b>145</b>	16	<b>84.5</b>	<b>157</b>	84.3	157	85.2	155
627.cam4_s	16	107	82.7	107	82.8	<b>107</b>	<b>82.8</b>	16	107	82.7	107	82.7	<b>107</b>	<b>82.7</b>
628.pop2_s	16	127	93.7	<b>126</b>	<b>93.9</b>	126	94.0	16	125	95.1	124	95.8	<b>125</b>	<b>95.2</b>
638.imagick_s	16	104	138	<b>104</b>	<b>139</b>	104	139	16	104	139	104	139	<b>104</b>	<b>139</b>
644.nab_s	16	78.7	222	<b>78.7</b>	<b>222</b>	78.8	222	16	78.9	221	78.9	221	<b>78.9</b>	<b>221</b>
649.fotonik3d_s	16	<b>71.4</b>	<b>128</b>	71.5	127	68.8	132	16	68.1	134	70.1	130	<b>68.2</b>	<b>134</b>
654.roms_s	16	72.5	217	<b>72.6</b>	<b>217</b>	73.3	215	16	<b>70.6</b>	<b>223</b>	70.5	223	70.8	222

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
 'numactl' was used to bind copies to the cores.  
 See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
 numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
 To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
 To free node-local memory and avoid remote memory usage,  
 'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
 To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
 To disable address space layout randomization (ASLR) to reduce run-to-run  
 variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

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## Operating System Notes (Continued)

To enable Transparent Hugepages (THP) for all allocations,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.

To always enable THP for peak runs of:

603.bwaves\_s, 607.cactubSSN\_s, 619.lbm\_s, 627.cam4\_s, 628.pop2\_s, 638.imagick\_s, 644.nab\_s, 649.fotonik3d\_s:  
'echo madvise > /sys/kernel/mm/transparent\_hugepage/enabled; echo always > /sys/kernel/mm/transparent\_hugepage/defrag'  
run as root.

To disable THP for peak runs of 621.wrf\_s:

'echo never > /sys/kernel/mm/transparent\_hugepage/enabled; echo always > /sys/kernel/mm/transparent\_hugepage/defrag'  
run as root.

To enable THP only on request for peak runs of 654.roms\_s:

'echo madvise > /sys/kernel/mm/transparent\_hugepage/enabled; echo madvise > /sys/kernel/mm/transparent\_hugepage/defrag'  
run as root.

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP\_CPU\_AFFINITY = "0-15"  
LD\_LIBRARY\_PATH = "/home/cpu2017/amd\_speed\_aocc400\_genoa\_B\_lib/lib:"  
LIBOMP\_NUM\_HIDDEN\_HELPER\_THREADS = "0"  
MALLOC\_CONF = "oversize\_threshold:0,retain:true"  
OMP\_DYNAMIC = "false"  
OMP\_SCHEDULE = "static"  
OMP\_STACKSIZE = "128M"  
OMP\_THREAD\_LIMIT = "16"

Environment variables set by runcpu during the 603.bwaves\_s peak run:

GOMP\_CPU\_AFFINITY = "0-15"

Environment variables set by runcpu during the 607.cactubSSN\_s peak run:

GOMP\_CPU\_AFFINITY = "0-15"

Environment variables set by runcpu during the 619.lbm\_s peak run:

GOMP\_CPU\_AFFINITY = "0-15"

Environment variables set by runcpu during the 621.wrf\_s peak run:

GOMP\_CPU\_AFFINITY = "0-15"

Environment variables set by runcpu during the 627.cam4\_s peak run:

GOMP\_CPU\_AFFINITY = "0-15"

Environment variables set by runcpu during the 628.pop2\_s peak run:

GOMP\_CPU\_AFFINITY = "0-15"

Environment variables set by runcpu during the 638.imagick\_s peak run:

GOMP\_CPU\_AFFINITY = "0-15"

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## Environment Variables Notes (Continued)

Environment variables set by runcpu during the 644.nab\_s peak run:

GOMP\_CPU\_AFFINITY = "0-15"

Environment variables set by runcpu during the 649.fotonik3d\_s peak run:

GOMP\_CPU\_AFFINITY = "0-15"

PGHPF\_ZMEM = "yes"

Environment variables set by runcpu during the 654.roms\_s peak run:

GOMP\_CPU\_AFFINITY = "0 8 1 9 2 10 3 11 4 12 5 13 6 14 7 15"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS Configuration

Workload Profile set to General Peak Frequency Compute

Determinism Control set to Manual

Performance Determinism set to Power Deterministic

AMD SMT Option set to Disabled

Last-Level Cache (LLC) as NUMA Node set to Enabled

ACPI CST C2 Latency set to 18 microseconds

Memory PStates set to Disabled

Thermal Configuration set to Maximum Cooling

Workload Profile set to Custom

Power Regulator set to OS Control Mode

The system ROM used for this result contains microcode version 0x0A10110e for the AMD EPYC 9nn4X family of processors. The reference code/AGESA version used in this ROM is version GenoAPI 1.0.0.1-L6

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on admin1 Mon Jun 27 18:31:54 2022

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## Platform Notes (Continued)

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
  10. who -r
  11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.4)
  12. Failed units, from systemctl list-units --state=failed
  13. Services, from systemctl list-unit-files
  14. Linux kernel boot-time arguments, from /proc/cmdline
  15. cpupower frequency-info
  16. tuned-adm active
  17. sysctl
  18. /sys/kernel/mm/transparent\_hugepage
  19. /sys/kernel/mm/transparent\_hugepage/khugepaged
  20. OS release
  21. Disk information
  22. /sys/devices/virtual/dmi/id
  23. dmidecode
  24. BIOS
- 

-----  
1. uname -a  
Linux admin1 5.15.0-53-generic #59-Ubuntu SMP Mon Oct 17 18:53:30 UTC 2022 x86\_64 x86\_64 x86\_64 GNU/Linux

-----  
2. w  
18:31:54 up 3 min, 2 users, load average: 0.04, 0.05, 0.02  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
admin1 pts/0 172.16.0.100 18:31 32.00s 0.00s 0.00s sshd: admin1 [priv]  
admin1 pts/1 172.16.0.100 18:31 10.00s 0.77s 0.00s sudo -ii

-----  
3. Username  
From environment variable \$USER: root  
From the command 'logname': admin1

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## Platform Notes (Continued)

```
4. ulimit -a
time(seconds)      unlimited
file(blocks)       unlimited
data(kbytes)        unlimited
stack(kbytes)       unlimited
coredump(blocks)    0
memory(kbytes)      unlimited
locked memory(kbytes) 2097152
process            1546279
nofiles             1024
vmmemory(kbytes)    unlimited
locks               unlimited
rtprio              0
```

```
5. sysinfo process ancestry
/sbin/init
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: admin1 [priv]
sshd: admin1@pts/0
-bash
sudo -ii
sudo -ii
-bash
python3 ./run_fpspeed.py
/bin/bash ./amd_speed_aocc400_genoa_B1.sh
runcpu --config amd_speed_aocc400_genoa_B1.cfg --tune all --reportable --iterations 3 fpspeed
runcpu --configfile amd_speed_aocc400_genoa_B1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.004/templogs/preenv.fpspeed.004.0.log --lognum 004.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

```
6. /proc/cpuinfo
model name          : AMD EPYC 9174F 16-Core Processor
vendor_id           : AuthenticAMD
cpu family          : 25
model               : 17
stepping             : 1
microcode           : 0xa10110e
bugs                : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size             : 3584 4K pages
cpu cores           : 16
siblings             : 16
1 physical ids (chips)
```

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## Platform Notes (Continued)

16 processors (hardware threads)

physical id 0: core ids 0-15

physical id 0: apicids 0-15

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

-----  
7. lscpu

From lscpu from util-linux 2.37.2:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	52 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	16
On-line CPU(s) list:	0-15
Vendor ID:	AuthenticAMD
Model name:	AMD EPYC 9174F 16-Core Processor
CPU family:	25
Model:	17
Thread(s) per core:	1
Core(s) per socket:	16
Socket(s):	1
Stepping:	1
Frequency boost:	enabled
CPU max MHz:	4409.0000
CPU min MHz:	400.0000
BogoMIPS:	8187.52
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mttr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmpfperf rapl pnpi pcclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_13 cdp_13 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmil avx2 smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock nrrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_lld
Virtualization:	AMD-V
L1d cache:	512 Kib (16 instances)
L1i cache:	512 Kib (16 instances)

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## Platform Notes (Continued)

L2 cache:	16 MiB (16 instances)
L3 cache:	256 MiB (8 instances)
NUMA node(s):	8
NUMA node0 CPU(s):	0,1
NUMA node1 CPU(s):	8,9
NUMA node2 CPU(s):	4,5
NUMA node3 CPU(s):	12,13
NUMA node4 CPU(s):	6,7
NUMA node5 CPU(s):	14,15
NUMA node6 CPU(s):	2,3
NUMA node7 CPU(s):	10,11
Vulnerability Itlb multihit:	Not affected
Vulnerability L1tf:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Mmio stale data:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:	Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP disabled, RSB filling, PBRSSB-eIBRS Not affected
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	512K	8	Data	1	64	1	64
L1i	32K	512K	8	Instruction	1	64	1	64
L2	1M	16M	8	Unified	2	2048	1	64
L3	32M	256M	16	Unified	3	32768	1	64

-----  
8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 8 nodes (0-7)
node 0 cpus: 0-1
node 0 size: 48072 MB
node 0 free: 47918 MB
node 1 cpus: 8-9
node 1 size: 48383 MB
node 1 free: 48296 MB
node 2 cpus: 4-5
node 2 size: 48383 MB
node 2 free: 48215 MB
node 3 cpus: 12-13
node 3 size: 48383 MB
node 3 free: 48215 MB
```

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## Platform Notes (Continued)

```
node 4 cpus: 6-7
node 4 size: 48347 MB
node 4 free: 48182 MB
node 5 cpus: 14-15
node 5 size: 48383 MB
node 5 free: 48210 MB
node 6 cpus: 2-3
node 6 size: 48383 MB
node 6 free: 48299 MB
node 7 cpus: 10-11
node 7 size: 48343 MB
node 7 free: 48184 MB
node distances:
node   0   1   2   3   4   5   6   7
  0: 10 11 12 12 12 12 12 12
  1: 11 10 12 12 12 12 12 12
  2: 12 12 10 11 12 12 12 12
  3: 12 12 11 10 12 12 12 12
  4: 12 12 12 12 10 11 12 12
  5: 12 12 12 12 11 10 12 12
  6: 12 12 12 12 12 12 10 11
  7: 12 12 12 12 12 12 11 10
```

---

9. /proc/meminfo

MemTotal: 395961172 kB

---

10. who -r

run-level 5 Jun 27 18:30

---

11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.4)

Default Target Status  
graphical degraded

---

12. Failed units, from systemctl list-units --state=failed

UNIT	LOAD	ACTIVE	SUB	DESCRIPTION
* systemd-networkd-wait-online.service	loaded	failed	Wait for Network to be Configured	

---

13. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager apparmor blk-availability cloud-config cloud-final cloud-init cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager grub-common grub-initrd-fallback irqbalance keyboard-setup lm-sensors lvm2-monitor

(Continued on next page)



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## Platform Notes (Continued)

```
lxd-agent multipathd networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog
secureboot-db setvtrgb ssh systemd-networkd systemd-networkd-wait-online systemd-pstore
systemd-resolved systemd-timesyncd thermald tuned ua-reboot-cmds ubuntu-advantage udisks2
ufw vgaauth
enabled-runtime netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
disabled console-getty debug-shell iscsid nftables rsync serial-getty@
systemd-boot-check-no-failures systemd-network-generator systemd-sysext
systemd-time-wait-sync upower
generated apport
indirect uidd
masked cryptdisks cryptdisks-early hwclock lvm2 multipath-tools-boot rc rcs screen-cleanup sudo
x11-common
```

---

14. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=/vmlinuz-5.15.0-53-generic  
root=/dev/mapper/ubuntu--vg-ubuntu--lv  
ro

---

15. cpupower frequency-info  
analyzing CPU 0:  
current policy: frequency should be within 400 MHz and 4.41 GHz.  
The governor "performance" may decide which speed to use  
within this range.  
boost state support:  
Supported: yes  
Active: yes  
Boost States: 0  
Total States: 3  
Pstate-P0: 4100MHz

---

16. tuned-adm active  
Current active profile: throughput-performance

---

17. sysctl  
kernel.numa\_balancing 1  
kernel.randomize\_va\_space 0  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000  
vm.dirty\_ratio 8  
vm.dirty\_writeback\_centisecs 500

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL345 Gen11

(4.10 GHz, AMD EPYC 9174F)

**SPECspeed®2017\_fp\_base = 165**

**SPECspeed®2017\_fp\_peak = 169**

CPU2017 License: 3

**Test Date:** Feb-2023

Test Sponsor: HPE

**Hardware Availability:** Nov-2022

Tested by: HPE

**Software Availability:** Nov-2022

## Platform Notes (Continued)

```
vm.dirtytime_expire_seconds      43200
vm.extfrag_threshold            500
vm.min_unmapped_ratio           1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy        0
vm.nr_overcommit_hugepages       0
vm.swappiness                     1
vm.watermark_boost_factor        15000
vm.watermark_scale_factor         10
vm.zone_reclaim_mode              1
```

---

```
18. /sys/kernel/mm/transparent_hugepage
    defrag          [always] defer defer+madvise madvise never
    enabled         [always] madvise never
    hpage_pmd_size 2097152
    shmem_enabled   always within_size advise [never] deny force
```

---

```
19. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs     60000
    defrag                   1
    max_ptes_none             511
    max_ptes_shared            256
    max_ptes_swap               64
    pages_to_scan              4096
    scan_sleep_millisecs       10000
```

---

```
20. OS release
    From /etc/*-release /etc/*-version
    os-release Ubuntu 22.04.1 LTS
```

---

```
21. Disk information
SPEC is set to: /home/cpu2017
Filesystem           Type  Size  Used Avail Use% Mounted on
/dev/mapper/ubuntu--vg-ubuntu--lv ext4   98G   37G   57G  40% /
```

---

```
22. /sys/devices/virtual/dmi/id
Vendor:          HPE
Product:         ProLiant DL345 Gen11
Product Family: ProLiant
Serial:          DL345G11-002
```

(Continued on next page)



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## Platform Notes (Continued)

### 23. dmidecode

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

#### Memory:

```
5x Hynix HMCG88AEBRA168N 32 GB 2 rank 4800
4x Hynix HMCG88MEBRA113N 32 GB 2 rank 4800
3x Hynix HMCG88MEBRA115N 32 GB 2 rank 4800
```

---

### 24. BIOS

(This section combines info from /sys/devices and dmidecode.)

```
BIOS Vendor: HPE
BIOS Version: 1.12
BIOS Date: 11/24/2022
BIOS Revision: 1.12
Firmware Revision: 1.10
```

## Compiler Version Notes

---

```
=====
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
   | 644.nab_s(base, peak)
=====
```

---

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
 LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
=====
```

---

```
=====
C++, C, Fortran | 607.cactusBSSN_s(base, peak)
=====
```

---

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
 LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
 LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
=====
```

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## Compiler Version Notes (Continued)

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====

Fortran	603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)
	654.roms_s(base, peak)

=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====

Fortran, C	621.wrf_s(base, peak) 627.cam4_s(base, peak)
	628.pop2_s(base, peak)

=====

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

AMD clang version 14.0.6 (CLANG: AOCC\_4.0.0-Build#389 2022\_10\_07) (based on LLVM Mirror.Version.14.0.6)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

## Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

(Continued on next page)



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Test Date: Feb-2023

Hardware Availability: Nov-2022

Software Availability: Nov-2022

## Base Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64  
607.cactubssn\_s: -DSPEC\_LP64  
619.lbm\_s: -DSPEC\_LP64  
621.wrf\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
627.cam4\_s: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
628.pop2\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
638.imagick\_s: -DSPEC\_LP64  
644.nab\_s: -DSPEC\_LP64  
649.fotonik3d\_s: -DSPEC\_LP64  
654.roms\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver4  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=7  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3  
-DSPEC\_OPENMP -zopt -fopenmp=libomp -lomp -lamdlibm -lamdaloc  
-lflang

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC\_OPENMP -O3 -march=znver4  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -Mrecursive  
-funroll-loops -mllvm -lsr-in-nested-loop  
-mllvm -reduce-array-computations=3 -zopt -fopenmp=libomp -lomp  
-lamdlibm -lamdaloc -lflang

Benchmarks using both Fortran and C:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=7  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000

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CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

**Test Date:** Feb-2023

**Hardware Availability:** Nov-2022

**Software Availability:** Nov-2022

## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3  
-DSPEC_OPENMP -zopt -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -fopenmp=libomp -lomp -lamdlibm -lamdaloc  
-flang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4  
-fveclib=AMDLIBM -ffast-math -fopenmp -fno-finite-math-only  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3  
-DSPEC_OPENMP -zopt -mllvm -unroll-threshold=100 -finline-aggressive  
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -fopenmp=libomp -lomp -lamdlibm -lamdaloc  
-flang
```

## Base Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-return-type -Wno-unused-command-line-argument
```

## Peak Compiler Invocation

C benchmarks:

```
clang
```

Fortran benchmarks:

```
flang
```

Benchmarks using both Fortran and C:

```
flang clang
```

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**Software Availability:** Nov-2022

## Peak Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
619.lbm_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

638.imagick\_s: Same as 619.lbm\_s

```
644.nab_s: -m64 -Wl,-mllvm -Wl,-region-vectorize -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Fortran benchmarks:

```
603.bwaves_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver4 -fveclib=AMDLIBM -ffast-math
-fopenmp -Mrecursive -mllvm -reduce-array-computations=3
-fvector-transform -fscalar-transform -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang
```

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## Peak Optimization Flags (Continued)

649.fotonik3d\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC\_OPENMP  
-Ofast -march=znver4 -fveclib=AMDLIBM -ffast-math  
-fopenmp -flto -Mrecursive  
-mllvm -reduce-array-computations=3 -zopt -fopenmp=libomp  
-lomp -lamdlibm -lamdaloc -lflang

654.roms\_s: Same as 603.bwaves\_s

Benchmarks using both Fortran and C:

621.wrf\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast  
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp  
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -DSPEC\_OPENMP -zopt  
-O3 -Mrecursive -funroll-loops -mllvm -lsr-in-nested-loop  
-fopenmp=libomp -lomp -lamdlibm -lamdaloc -lflang

627.cam4\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast  
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp  
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -DSPEC\_OPENMP -zopt  
-Mrecursive -fopenmp=libomp -lomp -lamdlibm -lamdaloc  
-lflang

628.pop2\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast  
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp  
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -DSPEC\_OPENMP -zopt  
-Mrecursive -fvector-transform -fscalar-transform  
-fopenmp=libomp -lomp -lamdlibm -lamdaloc -lflang

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## Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver4  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=9  
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-DSPEC_OPENMP -zopt -finline-aggressive -mllvm -unroll-threshold=100  
-Mrecursive -fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

## Peak Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-return-type -Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Genoa-rev2.2.html>

<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Genoa-rev2.2.xml>

<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>

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Tested with SPEC CPU®2017 v1.1.9 on 2022-06-27 14:31:54-0400.

Report generated on 2023-03-15 10:17:43 by CPU2017 PDF formatter v6442.

Originally published on 2023-03-14.