



# SPEC® MPIL2007 Result

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## Lenovo Global Technology

[SPECmpiL\\_peak2007 = Not Run](#)

ThinkSystem SR665  
(AMD EPYC 7H12, 2.6 GHz)

[SPECmpiL\\_base2007 = 6.02](#)

MPI2007 license: 28

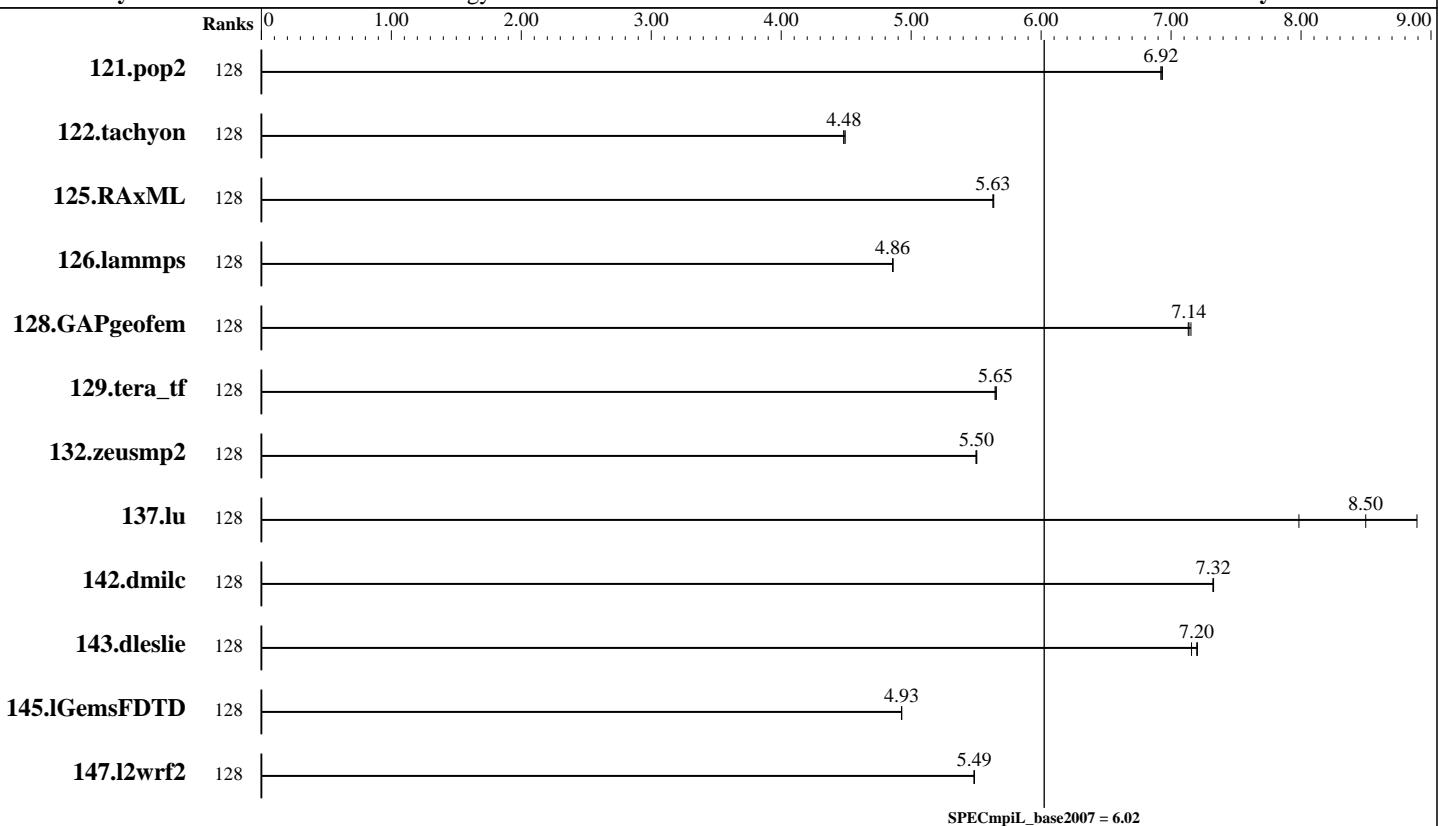
Test date: Jan-2020

Test sponsor: Lenovo Global Technology

Hardware Availability: Jun-2020

Tested by: Lenovo Global Technology

Software Availability: Jun-2020



## Results Table

Benchmark	Base							Peak						
	Ranks	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Ranks	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
121.pop2	128	<b>562</b>	<b>6.92</b>	561	6.93	562	6.92							
122.tachyon	128	434	4.48	<b>434</b>	<b>4.48</b>	433	4.49							
125.RAxML	128	518	5.63	<b>518</b>	<b>5.63</b>	518	5.64							
126.lammps	128	506	4.86	<b>506</b>	<b>4.86</b>	506	4.86							
128.GAPgeofem	128	<b>831</b>	<b>7.14</b>	832	7.13	830	7.15							
129.tera_tf	128	<b>194</b>	<b>5.65</b>	195	5.65	194	5.66							
132.zeusmp2	128	385	5.50	386	5.50	<b>385</b>	<b>5.50</b>							
137.lu	128	<b>494</b>	<b>8.50</b>	526	7.98	473	8.89							
142.dmilc	128	503	7.32	503	7.33	<b>503</b>	<b>7.32</b>							
143.dleslie	128	430	7.20	<b>431</b>	<b>7.20</b>	433	7.16							
145.lGemsFDTD	128	895	4.93	<b>895</b>	<b>4.93</b>	896	4.93							
147.l2wrf2	128	<b>1496</b>	<b>5.49</b>	1496	5.48	1495	5.49							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Standard Performance Evaluation Corporation

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### Hardware Summary

Type of System: Homogeneous  
Compute Node: ThinkSystem SR665  
File Server Node: NFS  
Total Compute Nodes: 1  
Total Chips: 2  
Total Cores: 128  
Total Threads: 128  
Total Memory: 1 TB  
Base Ranks Run: 128  
Minimum Peak Ranks: --  
Maximum Peak Ranks: --

### Software Summary

C Compiler: Intel C++ Compiler 20.0 for Linux Version 19.1.0.166 Build 20191121  
C++ Compiler: Intel C++ Compiler 20.0 for Linux Version 19.1.0.166 Build 20191121  
Fortran Compiler: Intel Fortran Compiler 20.0 for Linux Version 19.1.0.166 Build 20191121  
Base Pointers: 64-bit  
Peak Pointers: Not Applicable  
MPI Library: Open MPI Library Version 4.0.2  
Other MPI Info: None  
Pre-processors: No  
Other Software: None

## Node Description: ThinkSystem SR665

### Hardware

Number of nodes: 1  
Uses of the node: compute  
Vendor: Lenovo Global Technology  
Model: SR665  
CPU Name: AMD EPYC 7H12  
CPU(s) orderable: 1-2 chips  
Chips enabled: 2  
Cores enabled: 128  
Cores per chip: 64  
Threads per core: 1  
CPU Characteristics: None  
CPU MHz: 2600  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 512 KB I+D on chip per core  
L3 Cache: 256 MB I+D on chip per chip  
16 MB shared / 4 cores  
Other Cache: None  
Memory: 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)  
Disk Subsystem: 1 x 480 GB SATA 2.5" SSD  
Other Hardware:  
Adapter: Mellanox ConnectX-6 HDR Infiniband  
Number of Adapters: 1  
Slot Type: PCI-Express 4.0 x16  
Data Rate: 200 Gbs/s  
Ports Used: 1  
Interconnect Type: Mellanox ConnectX-6 HDR Infiniband Adapter

### Software

Adapter: Mellanox ConnectX-6 HDR Infiniband 4.7-1.0.0.1.2  
Adapter Driver: 20.25.2006  
Adapter Firmware: Red Hat Enterprise Linux Server release 8.1, 4.18.0-147.el8.x86\_64  
Operating System: xfs  
Local File System: None  
Shared File System: Multi-user, run level 3  
System State: Other Software: None



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Hardware Availability: Jun-2020

Tested by: Lenovo Global Technology

Software Availability: Jun-2020

### Node Description: NFS

#### Hardware

Number of nodes: 1  
Uses of the node: Fileserver  
Vendor: Lenovo Global Technology  
Model: ThinkSystem SR665  
CPU Name: AMD EPYC 7H12 CPU  
CPU(s) orderable: 1-2 chips  
Chips enabled: 2  
Cores enabled: 128  
Cores per chip: 64  
Threads per core: 1  
CPU Characteristics: None  
CPU MHz: 2600  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 512 KB I+D on chip per core  
L3 Cache: 256 MB I+D on chip per chip  
16 MB shared / 4 cores  
Other Cache: None  
Memory: 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)  
Disk Subsystem: 1 x 480 GB SATA 2.5" SSD  
Other Hardware:  
Adapter: Mellanox ConnectX-6 HDR Infiniband  
Number of Adapters: 1  
Slot Type: PCI-Express 4.0 x16  
Data Rate: 200 Gb/s  
Ports Used: 1  
Interconnect Type: Mellanox ConnectX-6 HDR Infiniband

#### Software

Adapter: Mellanox ConnectX-6 HDR Infiniband  
Adapter Driver: 4.7-1.0.0.1.2  
Adapter Firmware: 20.25.2006  
Operating System: Red Hat Enterprise Linux Server release 8.1  
Local File System: None  
Shared File System: NFS  
System State: Multi-User, run level 3  
Other Software: None

### Submit Notes

The config file option 'submit' was used.

### General Notes

MPI startup command:

mpieexec command was used to start MPI jobs.

RAM configuration:

Compute nodes have 1 x 32 GB RDIMM on each memory channel.

Add "idle=poll" into grub

BIOS settings:

Operating Mode : Maximum Performance Mode

Hyper-Threading Technology (SMT): Enabled

NPS4

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication,

that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication,

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**Software Availability:** Jun-2020

## General Notes (Continued)

that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Base Compiler Invocation

C benchmarks:

/opt/OMPI/O402\_I20\_H47\_R81/bin/mpicc

C++ benchmarks:

126.lammps: /opt/OMPI/O402\_I20\_H47\_R81/bin/mpicxx

Fortran benchmarks:

/opt/OMPI/O402\_I20\_H47\_R81/bin/mpif90

Benchmarks using both Fortran and C:

/opt/OMPI/O402\_I20\_H47\_R81/bin/mpicc

/opt/OMPI/O402\_I20\_H47\_R81/bin/mpif90

## Base Portability Flags

121.pop2: -DSPEC\_MPI\_CASE\_FLAG

126.lammps: -DMPICH\_IGNORE\_CXX\_SEEK

## Base Optimization Flags

C benchmarks:

-O3 -march=core-avx2 -no-prec-div -ipo

C++ benchmarks:

126.lammps: -O3 -march=core-avx2 -no-prec-div -ipo

Fortran benchmarks:

-O3 -march=core-avx2 -no-prec-div -ipo

Benchmarks using both Fortran and C:

-O3 -march=core-avx2 -no-prec-div -ipo

The flags file that was used to format this result can be browsed at

[http://www.spec.org/mpi2007/flags/EM64T\\_Intel121\\_flags.20200506.00.html](http://www.spec.org/mpi2007/flags/EM64T_Intel121_flags.20200506.00.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/mpi2007/flags/EM64T\\_Intel121\\_flags.20200506.00.xml](http://www.spec.org/mpi2007/flags/EM64T_Intel121_flags.20200506.00.xml)



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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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