SPEC CFP95 Results

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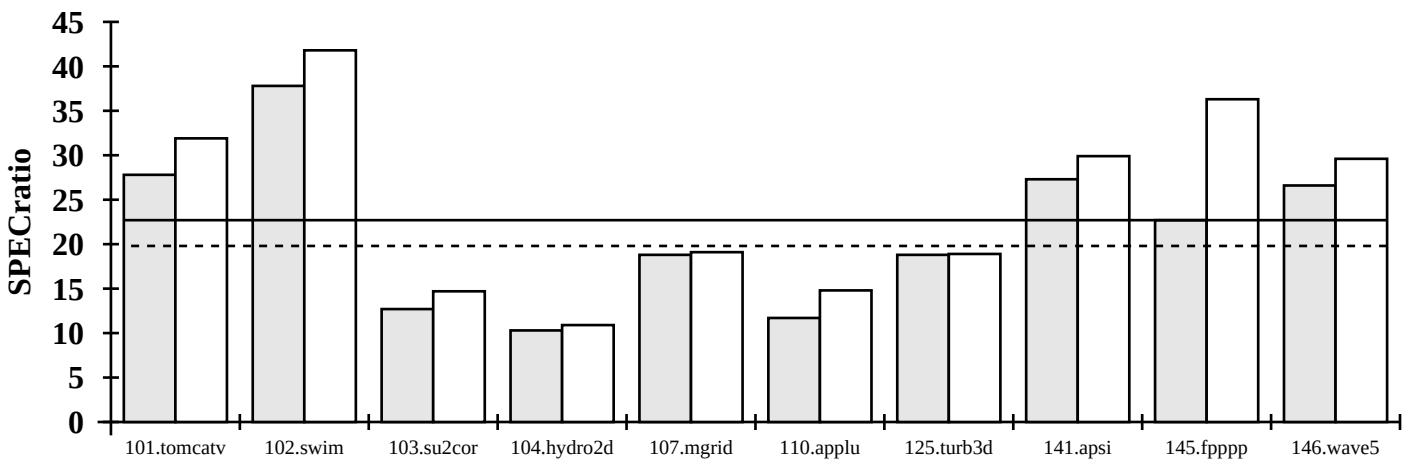
Sun Microsystems
Ultra 10 Model 440

SPECfp95 = 22.7

SPECfp_base95 = 19.8

SPEC license # 6	Tested By: Sun, Palo Alto	Test Date: Apr-99	Hardware Avail: May-99	Software Avail: Feb-99



Hardware/Software Configuration for:
Ultra 10 Model 440

Hardware

Model Name:	Ultra 10 Model 440
CPU:	440 MHz UltraSPARC-IIi
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	16KBI+16KBD on chip
Secondary Cache:	2MB(I+D) off chip
Other Cache:	None
Memory:	512MB
Disk Subsystem:	2 x 9GB (7200 RPM)
Other Hardware:	Fast Ethernet System clock frequency: 110 MHz

Software

Operating System:	Solaris 7 (64bit)
Compiler:	Sun WorkShop FORTRAN 77 5.0
File System:	UFS
System State:	Single User
Kernel Extensions:	consistent_coloring=1

Benchmark # and Name	Reference Time	Base Run Time	Base SPEC Ratio	Run Time	SPEC Ratio
101.tomcatv	3700	133	27.8	116	31.9
102.swim	8600	227	37.8	206	41.8
103.su2cor	1400	110	12.7	95	14.7
104.hydro2d	2400	233	10.3	220	10.9
107.mgrid	2500	133	18.8	131	19.1
110.applu	2200	188	11.7	149	14.8
125.turb3d	4100	218	18.8	217	18.9
141.apsi	2100	77.0	27.3	70	29.9
145.fpppp	9600	423	22.7	265	36.3
146.wave5	3000	113	26.6	101	29.6
SPECfp_base95 (G. Mean)			19.8		
		SPECfp95 (G. Mean)			22.7

Notes/Tuning Information

```
P1= -xprofile=collect:‘pwd‘/../feedback; P2= -xprofile=use:‘pwd‘/../feedback; FOPTION= -fast -xarch=v8plusa -s
BCOPY = -Qoption iropt -O4+bcopy; INV = -Qoption iropt -O4+invccexp; BMR = -Qoption iropt -O4+bmerge
Baseline: -fast -xarch=v8plusa -fsimple=2 -xprefetch
101.tomcatv: -fast -xarch=v8plusa -xpad=local:23 -xprefetch -Qoption iropt -O4+data_access
102.swim:    -fast -xarch=v8plusa -xpad=common:15 -xprefetch -fsimple=2
103.su2cor:  ${FOPTION} -xvector -Qoption cg -Qms_pipe+nfll=5 -fsimple=2 ${BCOPY}
103.su2cor:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
104.hydro2d: ${FOPTION} -xprefetch -Qoption iropt -O4+pde -fsimple=2
104.hydro2d: PASS1= ${P1}; PASS2= ${P2}
107.mgrid:   ${FOPTION} -xprefetch -fsimple=2 ${BCOPY}
107.mgrid:   PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
110.applu:   -fast -xarch=v8plusa -Qoption iropt -O4+scalarrep+ddint+perfect+loop_dist+pde+algassoc,-Mt6000,-Mr20000,-Ma140,-ldstr -fsimple=2
125.turb3d:  ${FOPTION} -stackvar -xpad -fsimple=2 -Qoption iropt -O4+bcopy ${INV} -xvector=yes
125.turb3d:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
141.apsi:    ${FOPTION} -fsimple=2 -stackvar  -Qoption iropt -Si36  ${INV} ${BMR} -Qoption iropt -O4+algassoc
141.apsi:    PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
145.fpppp:   -fast -xarch=v8plusa -xO5 -xsafe=mem -fsimple=2 -nodepend -Qoption iropt -O4+algassoc -Qoption iropt -reroll=1
145.fpppp:   PASS1= ${P1}; PASS2= ${P2}
146.wave5:   ${FOPTION} -stackvar -fsimple=2 -Qoption iropt -O4+unroll ${BCOPY}
Shell limits: # ulimit -n 1024  or % limit descriptors 1024
Config file: ultrabest.cfg
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