SPEC CINT95 Results

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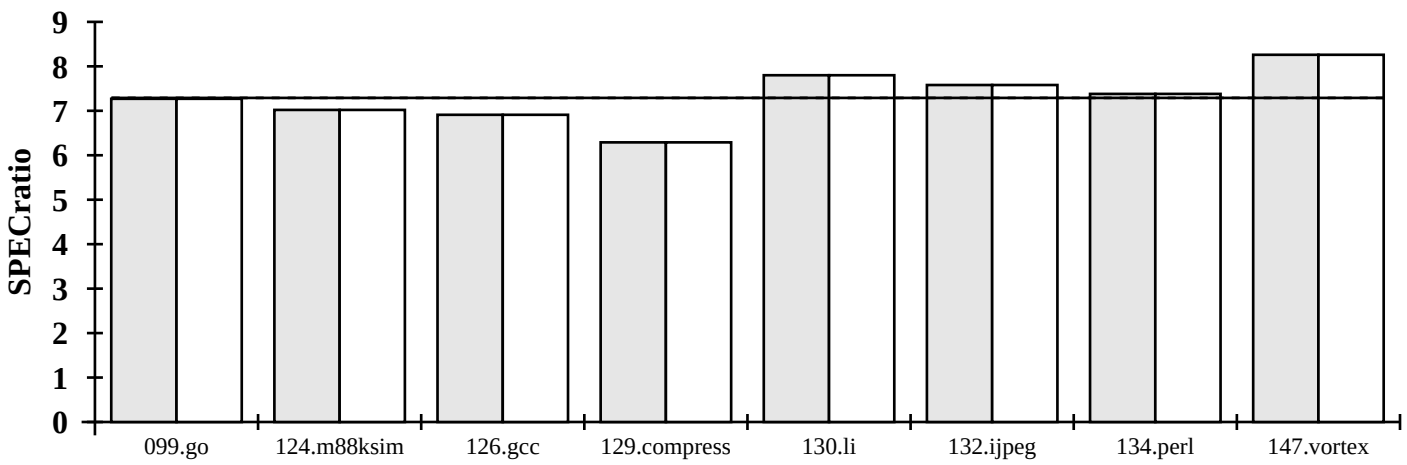
Intel Corporation
Alder System (180MHz, 256KB L2)

SPECint95 = 7.29

SPECint_base95 = 7.29

SPEC license # 14 | Tested By: Intel | Test Date: Oct-95 | Hardware Avail: May-96 | Software Avail: Feb-96



Hardware/Software Configuration for:
Alder System (180MHz, 256KB L2)

Hardware

Model Name:	Alder
CPU:	180MHz Pentium Pro Processor
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	8KBI+8KBD
Secondary Cache:	256KB(I+D)
Other Cache:	None
Memory:	128MB (60ns fast page)
Disk Subsystem:	2GB ST32550W
Other Hardware:	AHA-2940W Controller

Software

Operating System:	UnixWare 2.0, SDK
Compiler:	Intel C Reference Compiler 2.2 Beta
File System:	ufs, vxfs (/tmp as 8MB /tmpfs)
System State:	Single user (root + killall)

Benchmark # and Name	Reference Time	Base Run Time	Base SPEC Ratio	Run Time	SPEC Ratio
099.go	4600	633	7.27	633	7.27
124.m88ksim	1900	271	7.02	271	7.02
126.gcc	1700	246	6.91	246	6.91
129.compress	1800	286	6.29	286	6.29
130.li	1900	244	7.80	244	7.80
132.ijpeg	2400	317	7.58	317	7.58
134.perl	1900	257	7.38	257	7.38
147.vortex	2700	327	8.26	327	8.26
SPECint_base95 (G. Mean)			7.29		
		SPECint95 (G. Mean)			7.29

Notes/Tuning Information

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Base and non-base flags are the same and use Feedback Directed Optimization
Pass1: -tp p6 -ipo -xi -prof_gen -ircdb_dir /tmp/IRCDB
Pass2: -tp p6 -ipo -xi -prof_use -ircdb_dir /tmp/IRCDB
-ircdb_dir is a location flag and not an optimization flag
Portability: 124: -DSYSV -DLEHOST 130, 134, 147: -lm  132: -DSYSV  126: -lm -lc -L/usr/ucblib -lucb -lmalloc
Memory subsystem is four-way interleaved.
```