SPEC CFP95 Results

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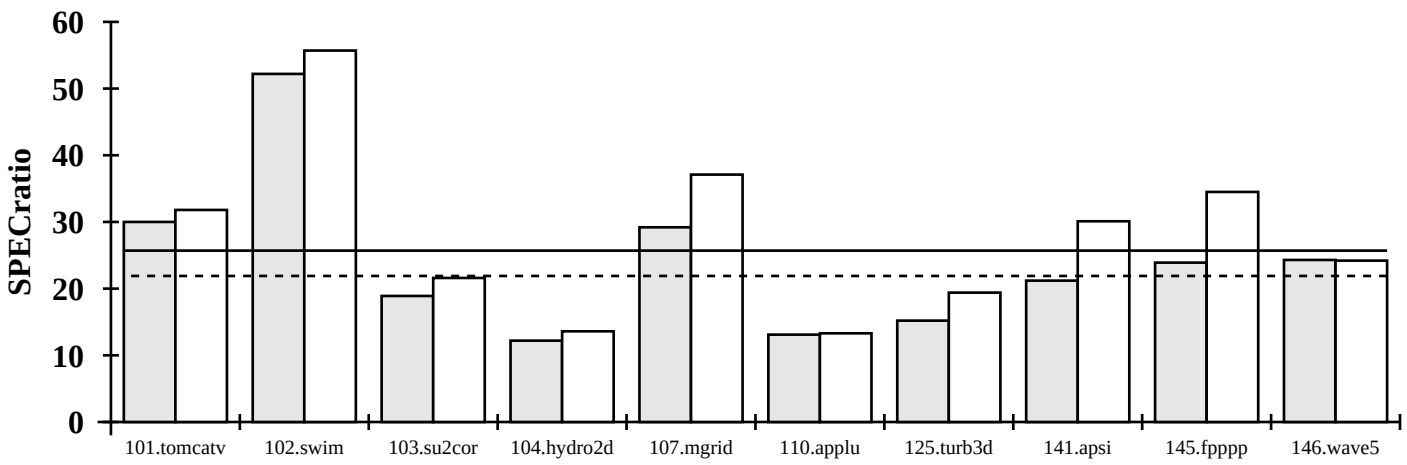
Sun Microsystems
Ultra 2 Model 2400

SPECfp95 = 25.7

SPECfp_base95 = 21.9

SPEC license # 6	Tested By: Sun, Palo Alto	Test Date: Dec-1999	Hardware Avail: May-1999	Software Avail: May-1999



Hardware/Software Configuration for:
Ultra 2 Model 2400

Hardware

Model Name:	Ultra 2 Model 2400
CPU:	400 MHz UltraSPARC-II
FPU:	Integrated
Number of CPU(s):	2
Primary Cache:	16KBI+16KBD on chip
Secondary Cache:	2MB(I+D) off chip
Other Cache:	None
Memory:	512MB
Disk Subsystem:	2 x 9.1GB (7200 RPM)
Other Hardware:	Fast Ethernet System clock frequency: 100 MHz

Software

Operating System:	Solaris 7 SU2 (64-bit)
Compiler:	Sun Workshop FORTRAN 77 5.0
File System:	UFS
System State:	Single User
Kernel Extensions:	

Benchmark # and Name	Reference Time	Base Run Time	Base SPEC Ratio	Run Time	SPEC Ratio
101.tomcatv	3700	123	30.0	116	31.8
102.swim	8600	165	52.2	154	55.7
103.su2cor	1400	74.2	18.9	65	21.6
104.hydro2d	2400	196	12.2	177	13.6
107.mgrid	2500	85.5	29.2	67	37.1
110.applu	2200	168	13.1	165	13.3
125.turb3d	4100	270	15.2	211	19.4
141.apsi	2100	99.3	21.2	70	30.1
145.fpppp	9600	401	23.9	278	34.5
146.wave5	3000	123	24.3	124	24.2
SPECfp_base95 (G. Mean)			21.9		
		SPECfp95 (G. Mean)			25.7

Notes/Tuning Information

```
P1= -xprofile=collect:‘pwd‘/../feedback
P2= -xprofile=use:‘pwd‘/../feedback
BCOPY= -Qoption iropt -O4+bcopy; INV = -Qoption iropt -O4+invccexp; BMR = -Qoption iropt -O4+bmerge
Baseline: -fast -xarch=v8plusa -xparallel
Baseline: PASS1= ${P1}; PASS2= ${P2};
101.tomcatv: -fast -xarch=v8plusa -s -xpad=local:23 -xprefetch -xparallel -xreduction -Qoption iropt -O4+data_access
102.swim:    -fast -xarch=v8plusa -s -xpad=common:15 -xprefetch  -xparallel
103.su2cor:  -fast -xarch=v8plusa -s -xvector -Qoption cg -Qms_pipe+nfll=5 -fsimple=2  ${BCOPY}  -xparallel -xreduction
103.su2cor:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};  EXTRA_LIBS= -lmvec
104.hydro2d: -fast -xarch=v8plusa -s -xprefetch -Qoption iropt -O4+pde -fsimple=2  ${BMR} -xparallel
104.hydro2d:  PASS1= ${P1}; PASS2= ${P2};
107.mgrid: -fast -xarch=v8plusa -fsimple=2 -xprefetch -xparallel
110.applu: -fast -xarch=v8plusa -s -Qoption iropt -O4+scalarrep+ddint+perfect+loop_dist+algassoc,-Mt6000,-Mr20000,-Ma140,-ldstr -xparallel
125.turb3d:  -fast -xarch=v8plusa -s -stackvar -xpad -fsimple=2 ${INV}
125.turb3d:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
141.apsi:    -fast -xarch=v8plusa -s -xchip=ultra2  -fsimple=2 -xvector -stackvar  -Qoption iropt -Si36  ${BMR} ${INV}
141.apsi (cont):    -Qoption iropt -O4+algassoc
141.apsi:    PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
145.fpppp:   -fast -xarch=v8plusa -s -xO5 -xsafe=mem -fsimple=2 -nodepend -Qoption iropt -O4+algassoc -Qoption iropt -reroll=1
145.fpppp:   PASS1= ${P1}; PASS2= ${P2};
146.wave5:   -fast -xarch=v8plusa  -s -stackvar -fsimple=2 -Qoption iropt -O4+unroll ${BCOPY}  -xparallel
Config file: ultra2fpmp.cfg
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