



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL145 Gen11

(2.50 GHz, AMD EPYC 8434P)

**SPECspeed®2017\_int\_base = 11.8**

**SPECspeed®2017\_int\_peak = 11.8**

CPU2017 License: 3

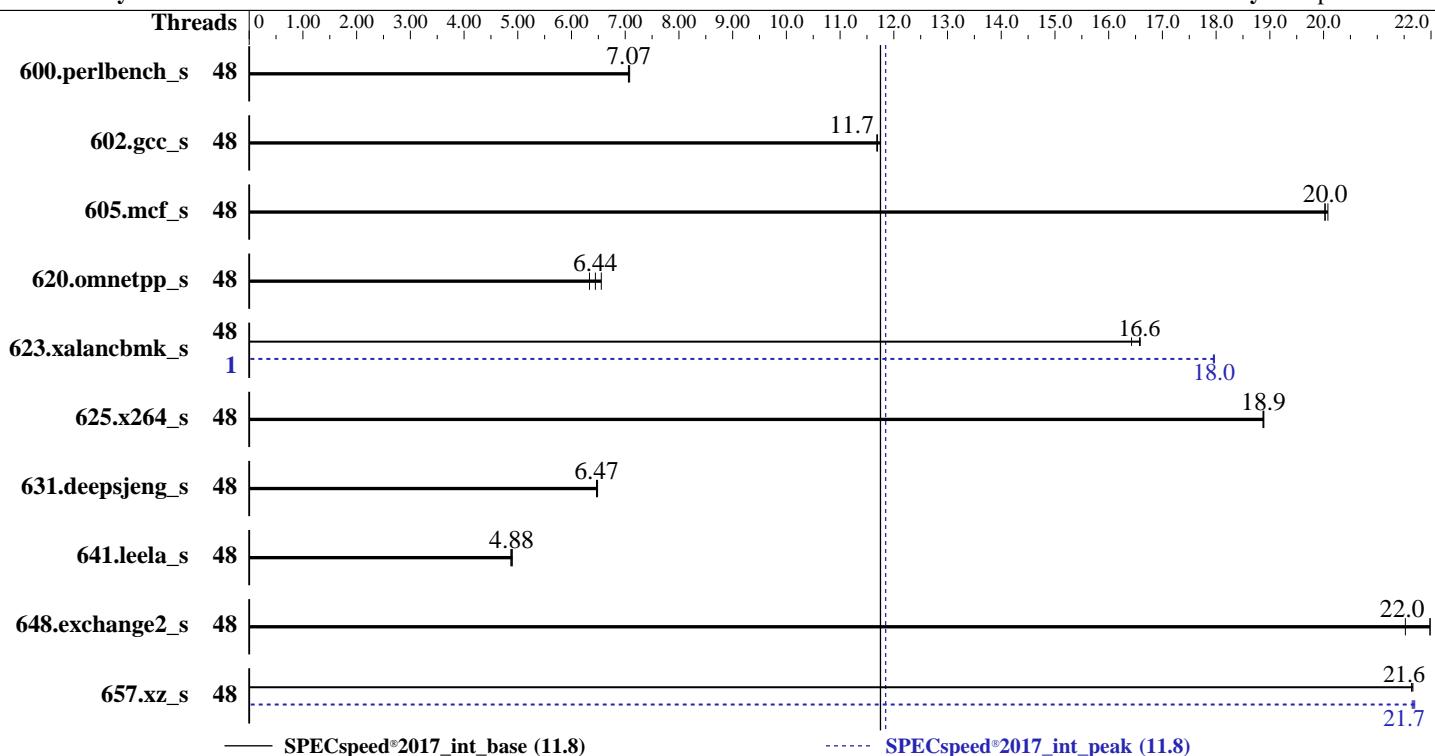
**Test Date:** Oct-2024

**Test Sponsor:** HPE

**Hardware Availability:** Oct-2024

**Tested by:** HPE

**Software Availability:** Sep-2024



## Hardware

CPU Name: AMD EPYC 8434P  
 Max MHz: 3100  
 Nominal: 2500  
 Enabled: 48 cores, 1 chip  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 128 MB I+D on chip per chip,  
 16 MB shared / 6 cores  
 Other: None  
 Memory: 192 GB (6 x 32 GB 2Rx8 PC5-4800B-R)  
 Storage: 1 x 480 GB SATA SSD  
 Other: CPU Cooling: Air

## Software

OS: SUSE Linux Enterprise Server 15 SP6  
 Compiler: Kernel 6.4.0-150600.21-default  
 Parallel: C/C++/Fortran: Version 5.0.0 of AOCC  
 Firmware: Yes  
 HPE BIOS Version v1.20 08/09/2024 released Aug-2024  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None  
 Power Management: BIOS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	48	251	7.08	<b>251</b>	<b>7.07</b>	251	7.06	48	251	7.08	<b>251</b>	<b>7.07</b>	251	7.06
602.gcc_s	48	<b>340</b>	<b>11.7</b>	339	11.8	341	11.7	48	<b>340</b>	<b>11.7</b>	339	11.8	341	11.7
605.mcf_s	48	236	20.0	235	20.1	<b>236</b>	<b>20.0</b>	48	236	20.0	235	20.1	<b>236</b>	<b>20.0</b>
620.omnetpp_s	48	<b>253</b>	<b>6.44</b>	257	6.33	249	6.55	48	<b>253</b>	<b>6.44</b>	257	6.33	249	6.55
623.xalancbmk_s	48	<b>85.5</b>	<b>16.6</b>	86.3	16.4	85.4	16.6	1	78.9	17.9	<b>78.9</b>	<b>18.0</b>	78.9	18.0
625.x264_s	48	93.5	18.9	<b>93.4</b>	<b>18.9</b>	93.4	18.9	48	93.5	18.9	<b>93.4</b>	<b>18.9</b>	93.4	18.9
631.deepsjeng_s	48	<b>221</b>	<b>6.47</b>	221	6.49	222	6.47	48	<b>221</b>	<b>6.47</b>	221	6.49	222	6.47
641.leela_s	48	350	4.87	348	4.90	<b>349</b>	<b>4.88</b>	48	350	4.87	348	4.90	<b>349</b>	<b>4.88</b>
648.exchange2_s	48	<b>134</b>	<b>22.0</b>	137	21.5	134	22.0	48	<b>134</b>	<b>22.0</b>	137	21.5	134	22.0
657.xz_s	48	<b>286</b>	<b>21.6</b>	286	21.6	285	21.7	48	285	21.7	<b>285</b>	<b>21.7</b>	286	21.7

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
 'numactl' was used to bind copies to the cores.  
 See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
 numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
 To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
 To free node-local memory and avoid remote memory usage,  
 'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
 To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
 To disable address space layout randomization (ASLR) to reduce run-to-run  
 variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,  
 'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
 'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.



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## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-47"  
LD_LIBRARY_PATH =  
    "/home/cpu2k17/amd_speed_aocc500_znver5_A/lib:/home/cpu2k17/amd_speed_aocc500_znver5_A/lib/lib32:  
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"  
MALLOC_CONF = "retain:true"  
OMP_DYNAMIC = "false"  
OMP_SCHEDULE = "static"  
OMP_STACKSIZE = "128M"  
OMP_THREAD_LIMIT = "48"
```

Environment variables set by runcpu during the 623.xalancbmk\_s peak run:

```
GOMP_CPU_AFFINITY = "0"
```

Environment variables set by runcpu during the 657.xz\_s peak run:

```
GOMP_CPU_AFFINITY = "0-47"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB Memory using Ubuntu 22.04

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS Configuration

Workload Profile set to General Peak Frequency Compute

AMD SMT Option set to Disabled

Determinism Control set to Manual

Performance Determinism set to Power Deterministic

AMD Periodic Directory Rinse Tuning set to Cache-Bound

Memory Patrol Scrubbing set to Disabled

NUMA memory domains per socket set to Two memory domains per socket

ACPI CST C2 Latency set to 18 microseconds

Thermal Configuration set to Maximum Cooling

The reference code/AGESA version used in this ROM is version Siena-PI 1.0.0

```
Sysinfo program /home/cpu2k17/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Mon Oct 7 10:50:51 2024
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry

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## Platform Notes (Continued)

```
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
-----
-----
1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux
-----
2. w
10:50:51 up 6 min, 3 users, load average: 0.25, 0.11, 0.04
USER      TTY      FROM          LOGIN@    IDLE      JCPU      PCPU WHAT
root      pts/0    172.17.1.13    22Apr24 11.00s  1.22s   0.07s /bin/bash ./amd_speed_aocc500_znver5_A1.sh
-----
3. Username
From environment variable $USER: root
-----
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size            (kbytes, -d) unlimited
scheduling priority      (-e) 0
file size                (blocks, -f) unlimited
pending signals          (-i) 772387
max locked memory        (kbytes, -l) 2097152
max memory size          (kbytes, -m) unlimited
open files               (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues     (bytes, -q) 819200
real-time priority       (-r) 0
stack size               (kbytes, -s) unlimited
cpu time                 (seconds, -t) unlimited
max user processes        (-u) 772387
virtual memory            (kbytes, -v) unlimited
file locks               (-x) unlimited
-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=31
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@pts/0
-bash
```

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## Platform Notes (Continued)

```
python3 ./run_intspeed.py
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 intspeed
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed intspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.010/templogs/preenv.intspeed.010.0.log --lognum 010.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2k17

-----
6. /proc/cpuinfo
model name      : AMD EPYC 8434P 48-Core Processor
vendor_id        : AuthenticAMD
cpu family       : 25
model            : 160
stepping          : 2
microcode         : 0xaa00215
bugs              : sysret_ss_atrs spectre_v1 spectre_v2 spec_store_bypass srso
TLB size          : 3584 4K pages
cpu cores         : 48
siblings           : 48
1 physical ids (chips)
48 processors (hardware threads)
physical id 0: core ids 0-5,8-13,16-21,24-29,32-37,40-45,48-53,56-61
physical id 0: apicids 0-5,8-13,16-21,24-29,32-37,40-45,48-53,56-61
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

-----
7. lscpu
From lscpu from util-linux 2.39.3:
Architecture:                  x86_64
CPU op-mode(s):                32-bit, 64-bit
Address sizes:                 52 bits physical, 57 bits virtual
Byte Order:                    Little Endian
CPU(s):                        48
On-line CPU(s) list:          0-47
Vendor ID:                     AuthenticAMD
BIOS Vendor ID:               Advanced Micro Devices, Inc.
Model name:                    AMD EPYC 8434P 48-Core Processor
BIOS Model name:              AMD EPYC 8434P 48-Core Processor
CPU family:                   107
Model:                         25
Thread(s) per core:           160
Core(s) per socket:           1
Socket(s):                    1
Stepping:                      2
BogoMIPS:                      4992.05
Flags:                          fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                                rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                                extd_apicid aperfmpfperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                                sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand lahf_lm
                                cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                                osw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                                perfctr_llc mwaitx cpb cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2
                                ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase bmi1 avx2 smep bmi2
                                erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma
```

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## Platform Notes (Continued)

```
clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
xgetbvl xsaves cq_m_llc cq_m_occup_llc cq_m_mb_m_total cq_m_mb_m_local
user_shstk avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd
amd_ppin cppc arat npt lbrv svm_lock nrrip_save tsc_scale vmcb_clean
flushbyasid decodeassists pausefilter pfthreshold avic
v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi avx512vbmi umip pku
ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_lid
debug_swap
AMD-V
```

Virtualization:

L1d cache: 1.5 MiB (48 instances)

L1i cache: 1.5 MiB (48 instances)

L2 cache: 48 MiB (48 instances)

L3 cache: 128 MiB (8 instances)

NUMA node(s): 2

NUMA node0 CPU(s): 0-23

NUMA node1 CPU(s): 24-47

Vulnerability Gather data sampling: Not affected

Vulnerability Itlb multihit: Not affected

Vulnerability Llft: Not affected

Vulnerability Mds: Not affected

Vulnerability Meltdown: Not affected

Vulnerability Mmio stale data: Not affected

Vulnerability Reg file data sampling: Not affected

Vulnerability Retbleed: Not affected

Vulnerability Spec rstack overflow: Mitigation; Safe RET

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and \_\_user pointer sanitization

Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP

disabled; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected

Vulnerability Srbds: Not affected

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	1.5M	8	Data	1	64	1	64
L1i	32K	1.5M	8	Instruction	1	64	1	64
L2	1M	48M	8	Unified	2	2048	1	64
L3	16M	128M	16	Unified	3	16384	1	64

-----  
8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0-23

node 0 size: 96450 MB

node 0 free: 95875 MB

node 1 cpus: 24-47

node 1 size: 96671 MB

node 1 free: 95954 MB

node distances:

node 0 1

0: 10 12

1: 12 10

-----  
9. /proc/meminfo

MemTotal: 197756832 kB

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## Platform Notes (Continued)

10. who -r  
run-level 3 Apr 22 17:30

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)  
Default Target Status  
multi-user running

12. Services, from systemctl list-unit-files  
STATE UNIT FILES  
enabled apparmor audtfd cron getty@ irqbalance issue-generator kbdsettings lvm2-monitor postfix  
purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4  
wickedd-dhcp6 wickedd-nanny  
enabled-runtime systemd-remount-fs  
disabled blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell  
grub2-once haveged hwloc-dump-hwdata issue-add-ssh-keys kexec-load lunmask rpmconfigcheck  
serial-getty@ systemd-boot-check-no-failures systemd-context systemd-network-generator  
systemd-sysext systemd-time-wait-sync systemd-timesyncd  
indirect systemd-userdbd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default  
root=UUID=45afdf261-73b0-43a7-a23e-94db089f1753  
splash=silent  
resume=/dev/disk/by-uuid/d6c60601-2238-4347-950d-7321063e97c9  
mitigations=auto  
quiet  
security=apparmor

14. cpupower frequency-info  
analyzing CPU 29:  
Unable to determine current policy  
boost state support:  
Supported: yes  
Active: yes

15. sysctl  
kernel.numa\_balancing 1  
kernel.randomize\_va\_space 0  
vm.compaction\_prolactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000  
vm.dirty\_ratio 8  
vm.dirty\_writeback\_centisecs 500  
vm.dirtytime\_expire\_seconds 43200  
vm.extfrag\_threshold 500  
vm.min\_unmapped\_ratio 1  
vm.nr\_hugepages 0  
vm.nr\_hugepages\_mempolicy 0  
vm.nr\_overcommit\_hugepages 0  
vm.swappiness 1  
vm.watermark\_boost\_factor 15000  
vm.watermark\_scale\_factor 10  
vm.zone\_reclaim\_mode 1

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## Platform Notes (Continued)

16. /sys/kernel/mm/transparent\_hugepage  
defrag [always] defer defer+madvise madvise never  
enabled [always] madvise never  
huge\_pmd\_size 2097152  
shmem\_enabled always within\_size advise [never] deny force

17. /sys/kernel/mm/transparent\_hugepage/khugepaged  
alloc\_sleep\_millisecs 60000  
defrag 1  
max\_ptes\_none 511  
max\_ptes\_shared 256  
max\_ptes\_swap 64  
pages\_to\_scan 4096  
scan\_sleep\_millisecs 10000

18. OS release  
From /etc/\*-release /etc/\*-version  
os-release SUSE Linux Enterprise Server 15 SP6

19. Disk information  
SPEC is set to: /home/cpu2k17  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda3 xfs 476G 94G 383G 20% /home

20. /sys/devices/virtual/dmi/id  
Vendor: HPE  
Product: ProLiant DL145 Gen11  
Product Family: ProLiant  
Serial: DL145GEN11

21. dmidecode  
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section.  
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
6x Hynix HMCG88MEBRA115N 32 GB 2 rank 4800

22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: HPE  
BIOS Version: 1.20  
BIOS Date: 08/09/2024  
BIOS Revision: 1.20  
Firmware Revision: 1.62

## Compiler Version Notes

=====

C | 600.perlbench\_s(base, peak) 602.gcc\_s(base, peak) 605.mcf\_s(base, peak) 625.x264\_s(base, peak)

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## Compiler Version Notes (Continued)

| 657.xz\_s(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
C++ | 620.omnetpp\_s(base, peak) 623.xalancbmk\_s(base, peak) 631.deepsjeng\_s(base, peak)  
| 641.leela\_s(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
Fortran | 648.exchange2\_s(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LINUX\_X64 -DSPEC\_LP64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LINUX -DSPEC\_LP64  
625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64

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# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL145 Gen11

(2.50 GHz, AMD EPYC 8434P)

**SPECspeed®2017\_int\_base = 11.8**

**SPECspeed®2017\_int\_peak = 11.8**

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

**Test Date:** Oct-2024

**Hardware Availability:** Oct-2024

**Software Availability:** Sep-2024

## Base Portability Flags (Continued)

648.exchange2\_s: -DSPEC\_LP64

657.xz\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-allow-multiple-definition -Wl,-mllvm -Wl,-extra-inliner -O3  
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP  
-flto -fremap-arrays -fstrip-mining -fstruct-layout=7  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp -lamdlibm  
-lflang -lamdaloc
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto  
-mllvm -loop-unswitch-threshold=200000  
-mllvm -reduce-array-computations=3 -mllvm -unroll-threshold=100 -zopt  
-fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp  
-lomp -lamdlibm -lflang -lamdaloc-ext
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-iv-split -Wl,-mllvm -Wl,-inline-recursion=4  
-Wl,-mllvm -Wl,-lsr-in-nested-loop -O3 -march=znver5 -fveclib=AMDLIBM  
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost  
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp  
-lomp -lamdlibm -lflang -lamdaloc
```

## Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

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## Base Other Flags (Continued)

Fortran benchmarks:

-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

600.perlbench\_s: basepeak = yes

602.gcc\_s: basepeak = yes

605.mcf\_s: basepeak = yes

625.x264\_s: basepeak = yes

657.xz\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3

-Wl,-allow-multiple-definition

-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5

-fveclib=AMDLIB -ffast-math -fopenmp -flto

-DSPEC\_OPENMP -fremap-arrays -fstrip-mining

-fstruct-layout=9 -mllvm -inline-threshold=1000

-mllvm -reduce-array-computations=3

-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp

-lamdlibm -lamdaloc -lflang

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CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2024

Hardware Availability: Oct-2024

Software Availability: Sep-2024

## Peak Optimization Flags (Continued)

C++ benchmarks:

620.omnetpp\_s: basepeak = yes

623.xalancbmk\_s: -m64 -std=c++14  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-do-block-reorder=advanced -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp  
-flto -DSPEC\_OPENMP -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=100 -zopt  
-fvirtual-function-elimination -fvisibility=hidden  
-mllvm -do-block-reorder=advanced -fopenmp=libomp -lomp  
-lamdlibm -lamdalloc-ext -lflang

631.deepsjeng\_s: basepeak = yes

641.leela\_s: basepeak = yes

Fortran benchmarks:

648.exchange2\_s: basepeak = yes

## Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aoxx500-flags.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Siena-rev1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aoxx500-flags.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Siena-rev1.0.xml>



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