



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen11
(2.20 GHz, Intel Xeon Gold 6554S)

SPECrate®2017_int_base = 338

SPECrate®2017_int_peak = Not Run

CPU2017 License: 3

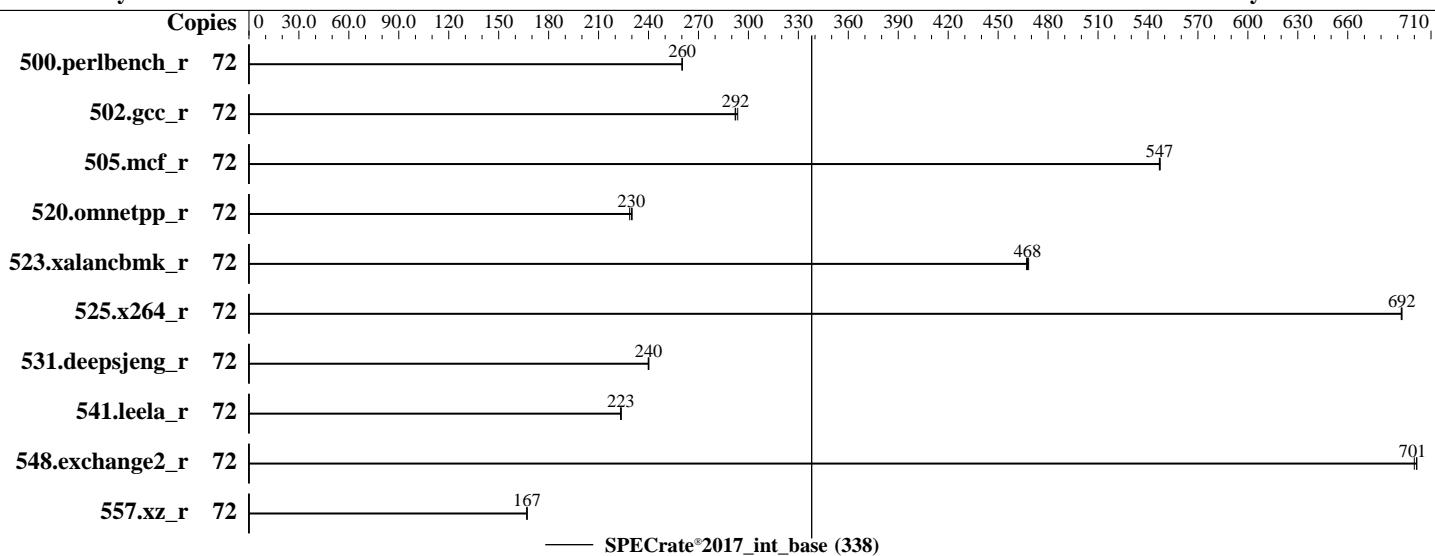
Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2024

Hardware Availability: Dec-2023

Software Availability: Mar-2024



Hardware

CPU Name: Intel Xeon Gold 6554S
Max MHz: 4000
Nominal: 2200
Enabled: 36 cores, 1 chip, 2 threads/core
Orderable: 1, 2 chip(s)
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 180 MB I+D on chip per chip
Other: None
Memory: 512 GB (8 x 64 GB 2Rx8 PC5-5600B-R, running at 5200)
Storage: 2 x 480 GB NVMe SSD, RAID 1
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP5
Compiler: Kernel 5.14.21-150500.53-default
C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel: No
Firmware: HPE BIOS Version v2.30 08/09/2024 released Sep-2024
File System: btrfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: None
Power Management: BIOS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	72	440	260	440	260	441	260									
502.gcc_r	72	349	292	347	294	349	292									
505.mcf_r	72	213	547	213	547	213	547									
520.omnetpp_r	72	413	229	410	230	411	230									
523.xalancbmk_r	72	163	468	163	467	162	468									
525.x264_r	72	182	692	182	692	182	692									
531.deepsjeng_r	72	344	240	344	240	344	240									
541.leela_r	72	534	223	534	223	533	224									
548.exchange2_r	72	269	702	269	701	269	700									
557.xz_r	72	465	167	466	167	465	167									

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
 Transparent Huge Pages enabled by default
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
 sync; echo 3 > /proc/sys/vm/drop_caches
 runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
 LD_LIBRARY_PATH = "/cpu2017/lib/intel64:/cpu2017/lib/ia32:/cpu2017/je5.0.1-32"
 MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
 NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.



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Platform Notes

BIOS Configuration:

Workload Profile set to General Throughput Compute
Memory Patrol Scrubbing set to Disabled
Sub-NUMA Clustering set to Enable SNC2(2-clusters)
Enhanced Processor Performance Profile set to Aggressive
Thermal Configuration set to Maximum Cooling
Workload Profile set to Custom
Minimum Processor Idle Power Package C-State set to Package C6 (non-retention) State

Sysinfo program /cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Tue Oct 15 08:12:47 2024

SUT (System Under Test) info as seen by some common utilities.

Table of contents

- 1. uname -a
- 2. w
- 3. Username
- 4. ulimit -a
- 5. sysinfo process ancestry
- 6. /proc/cpuinfo
- 7. lscpu
- 8. numactl --hardware
- 9. /proc/meminfo
- 10. who -r
- 11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
- 12. Services, from systemctl list-unit-files
- 13. Linux kernel boot-time arguments, from /proc/cmdline
- 14. cpupower frequency-info
- 15. sysctl
- 16. /sys/kernel/mm/transparent_hugepage
- 17. /sys/kernel/mm/transparent_hugepage/khugepaged
- 18. OS release
- 19. Disk information
- 20. /sys/devices/virtual/dmi/id
- 21. dmidecode
- 22. BIOS

1. uname -a
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
x86_64 x86_64 x86_64 GNU/Linux

2. w
08:12:47 up 28 min, 3 users, load average: 0.10, 0.16, 0.10
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root tty1 - 07:52 20:22 0.01s 0.01s -bash
root pts/0 16.1.33.101 07:52 2.00s 0.86s 0.00s -bash
root pts/1 16.1.33.101 08:00 11:43 0.03s 0.03s -bash

3. Username
From environment variable \$USER: root

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Platform Notes (Continued)

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size            (kbytes, -d) unlimited
scheduling priority      (-e) 0
file size                (blocks, -f) unlimited
pending signals          (-i) 2061032
max locked memory        (kbytes, -l) 64
max memory size          (kbytes, -m) unlimited
open files               (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues     (bytes, -q) 819200
real-time priority       (-r) 0
stack size               (kbytes, -s) unlimited
cpu time                 (seconds, -t) unlimited
max user processes        (-u) 2061032
virtual memory            (kbytes, -v) unlimited
file locks               (-x) unlimited
```

```
-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root@pts/0
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=72 -c
  ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=36 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=72 --configfile
  ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=36 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode
  rate --tune base --size refrate intrate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.002/templogs/preenv.intrate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /cpu2017
```

```
-----
6. /proc/cpuinfo
model name      : INTEL(R) XEON(R) GOLD 6554S
vendor_id       : GenuineIntel
cpu family     : 6
model          : 207
stepping        : 2
microcode       : 0x21000283
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrp_brs
cpu cores      : 36
siblings        : 72
1 physical ids (chips)
72 processors (hardware threads)
physical id 0: core ids 0-35
physical id 0: apicids 0-71
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

```
-----
7. lscpu
```

```
From lscpu from util-linux 2.37.4:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
```

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Platform Notes (Continued)

Address sizes:	46 bits physical, 57 bits virtual							
Byte Order:	Little Endian							
CPU(s):	72							
On-line CPU(s) list:	0-71							
Vendor ID:	GenuineIntel							
Model name:	INTEL(R) XEON(R) GOLD 6554S							
CPU family:	6							
Model:	207							
Thread(s) per core:	2							
Core(s) per socket:	36							
Socket(s):	1							
Stepping:	2							
BogoMIPS:	4400.00							
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe sysctl nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtTopology nonstop_tsc cpuid aperfmpf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrandlahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13 invpcid_single cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts hfi avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitlg tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_l1d arch_capabilities							
Virtualization:	VT-x							
L1d cache:	1.7 MiB (36 instances)							
L1i cache:	1.1 MiB (36 instances)							
L2 cache:	72 MiB (36 instances)							
L3 cache:	180 MiB (1 instance)							
NUMA node(s):	2							
NUMA node0 CPU(s):	0-17,36-53							
NUMA node1 CPU(s):	18-35,54-71							
Vulnerability Itlb multihit:	Not affected							
Vulnerability L1tf:	Not affected							
Vulnerability Mds:	Not affected							
Vulnerability Meltdown:	Not affected							
Vulnerability Mmio stale data:	Not affected							
Vulnerability Retbleed:	Not affected							
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl and seccomp							
Vulnerability Spectre v1:	Mitigation; usercopy/swapgs barriers and __user pointer sanitization							
Vulnerability Spectre v2:	Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW sequence							
Vulnerability Srbds:	Not affected							
Vulnerability Tsx async abort:	Not affected							
From lscpu --cache:								
NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.7M	12	Data	1	64	1	64
L1i	32K	1.1M	8	Instruction	1	64	1	64
L2	2M	72M	16	Unified	2	2048	1	64
L3	180M	180M	20	Unified	3	147456	1	64

8. numactl --hardware

(Continued on next page)



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Platform Notes (Continued)

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 2 nodes (0-1)
node 0 cpus: 0-17,36-53
node 0 size: 257660 MB
node 0 free: 256037 MB
node 1 cpus: 18-35,54-71
node 1 size: 257627 MB
node 1 free: 248958 MB
node distances:
node    0    1
  0:  10   20
  1:  20   10
```

```
9. /proc/meminfo
MemTotal:      527654916 kB
```

```
10. who -r
run-level 3 Oct 15 07:45
```

```
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
Default Target  Status
multi-user      running
```

```
12. Services, from systemctl list-unit-files
STATE          UNIT FILES
enabled        YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance
                issue-generator kbdsettings kdump kdump-early klog lvm2-monitor nsqd
                nvmeffc-boot-connections postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore
                wickedd wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime
disabled       autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
                firewalld gpm grub2-once haveged haveged-switch-root ipmi ipmievrd issue-add-ssh-keys
                kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nvmf-autocconnect rpcbind
                rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd
                systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                systemd-time-wait-sync systemd-timesyncd vncserver@
indirect        wickedd
```

```
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
root=UUID=b4cac700-78f9-446b-9ce6-b12658046c8f
splash=silent
mitigations=auto
quiet
security=apparmor
crashkernel=356M,high
crashkernel=72M,low
```

```
14. cpupower frequency-info
analyzing CPU 0:
  Unable to determine current policy
  boost state support:
    Supported: yes
```

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Platform Notes (Continued)

Active: yes

```
15. sysctl
    kernel.numa_balancing          1
    kernel.randomize_va_space      2
    vm.compaction_proactiveness   20
    vm.dirty_background_bytes      0
    vm.dirty_background_ratio     10
    vm.dirty_bytes                 0
    vm.dirty_expire_centisecs    3000
    vm.dirty_ratio                20
    vm.dirty_writeback_centisecs  500
    vm.dirtytime_expire_seconds   43200
    vm.extfrag_threshold          500
    vm.min_unmapped_ratio         1
    vm.nr_hugepages                0
    vm.nr_hugepages_mempolicy      0
    vm.nr_overcommit_hugepages     0
    vm.swappiness                  60
    vm.watermark_boost_factor     15000
    vm.watermark_scale_factor      10
    vm.zone_reclaim_mode           0
```

```
16. /sys/kernel/mm/transparent_hugepage
    defrag           always defer defer+madvise [madvise] never
    enabled          [always] madvise never
    hpage_pmd_size  2097152
    shmem_enabled   always within_size advise [never] deny force
```

```
17. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs  60000
    defrag                 1
    max_ptes_none          511
    max_ptes_shared         256
    max_ptes_swap           64
    pages_to_scan           4096
    scan_sleep_millisecs   10000
```

```
18. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP5
```

```
19. Disk information
  SPEC is set to: /cpu2017
  Filesystem  Type  Size  Used Avail Use% Mounted on
  /dev/nvme0n1p3  btrfs  444G  11G  433G  3% /
```

```
20. /sys/devices/virtual/dmi/id
  Vendor:        HPE
  Product:       ProLiant DL380 Gen11
  Product Family: ProLiant
  Serial:        CZ240702YX
```

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Platform Notes (Continued)

21. dmidecode

Additional information from dmidecode 3.4 follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

8x Hynix HMCG94AGBRA181N 64 GB 2 rank 5600, configured at 5200

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE
BIOS Version: 2.30
BIOS Date: 08/09/2024
BIOS Revision: 2.30
Firmware Revision: 1.63

Compiler Version Notes

```
=====
C      | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base) 557.xz_r(base)
-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
-----

=====
C++     | 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base) 541.leela_r(base)
-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
-----

=====
Fortran | 548.exchange2_r(base)
-----
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
```

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx



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Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc_r: -DSPEC_LP64  
505.mcf_r: -DSPEC_LP64  
520.omnetpp_r: -DSPEC_LP64  
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
525.x264_r: -DSPEC_LP64  
531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64  
548.exchange2_r: -DSPEC_LP64  
557.xz_r: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/opt/intel/oneapi/compiler/2024.1/lib -lgkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/opt/intel/oneapi/compiler/2024.1/lib -lgkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto  
-L/opt/intel/oneapi/compiler/2024.1/lib -lgkmalloc
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-EMR-rev1.0.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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