



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

meta brain NF3290G8 (Intel Xeon 6980P)

SPECrate®2017_int_base = 1220

SPECrate®2017_int_peak = 1260

CPU2017 License: 3358

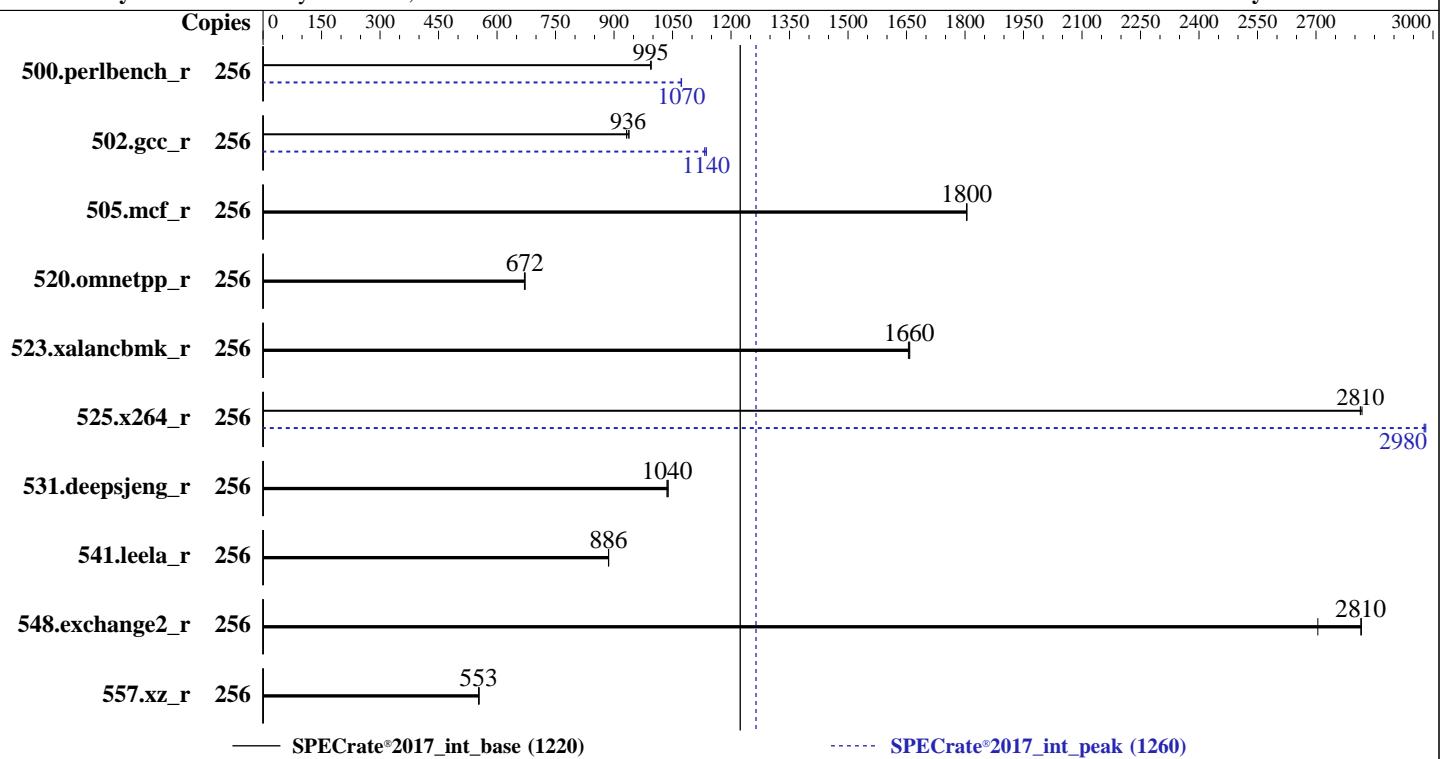
Test Sponsor: IEIT Systems Co., Ltd.

Tested by: IEIT Systems Co., Ltd.

Test Date: Oct-2024

Hardware Availability: Oct-2024

Software Availability: Jun-2024



— SPECrate®2017_int_base (1220)

----- SPECrate®2017_int_peak (1260)

Hardware

CPU Name: Intel Xeon 6980P
 Max MHz: 3900
 Nominal: 2000
 Enabled: 128 cores, 1 chip, 2 threads/core
 Orderable: 1 chip
 Cache L1: 64 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 504 MB I+D on chip per chip
 Other: None
 Memory: 768 GB (12 x 64 GB 2Rx4 PC5-8800B-R)
 Storage: 1 x 960 GB SSD
 Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6
 Compiler: 6.4.0-150600.21-default
 C/C++: Version 2024.1 of Intel oneAPI DPC++/C++
 Compiler for Linux;
 Fortran: Version 2024.1 of Intel Fortran Compiler
 for Linux;
 Parallel: No
 Firmware: Version 00.13.05 released Sep-2024
 File System: btrfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS set to prefer performance at the cost
 of additional power usage.



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

meta brain NF3290G8 (Intel Xeon 6980P)

SPECrate®2017_int_base = 1220

SPECrate®2017_int_peak = 1260

CPU2017 License: 3358

Test Date: Oct-2024

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Oct-2024

Tested by: IEIT Systems Co., Ltd.

Software Availability: Jun-2024

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	256	410	995	410	994	410	995	256	380	1070	380	1070	380	1070		
502.gcc_r	256	389	932	387	936	386	939	256	320	1130	319	1140	319	1140		
505.mcf_r	256	229	1800	229	1800	229	1800	256	229	1800	229	1800	229	1800		
520.omnetpp_r	256	501	670	500	672	500	672	256	501	670	500	672	500	672		
523.xalancbmk_r	256	163	1650	163	1660	163	1660	256	163	1650	163	1660	163	1660		
525.x264_r	256	159	2810	159	2810	159	2820	256	151	2980	150	2980	150	2980		
531.deepsjeng_r	256	283	1040	283	1040	282	1040	256	283	1040	283	1040	282	1040		
541.leela_r	256	478	886	479	886	479	886	256	478	886	479	886	479	886		
548.exchange2_r	256	238	2820	238	2810	248	2700	256	238	2820	238	2810	248	2700		
557.xz_r	256	500	553	500	553	499	554	256	500	553	500	553	499	554		

SPECrate®2017_int_base = 1220

SPECrate®2017_int_peak = 1260

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/lib/ia32:/home/CPU2017/je5.0.1-32"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

meta brain NF3290G8 (Intel Xeon 6980P)

SPECrate®2017_int_base = 1220

SPECrate®2017_int_peak = 1260

CPU2017 License: 3358

Test Date: Oct-2024

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Oct-2024

Tested by: IEIT Systems Co., Ltd.

Software Availability: Jun-2024

General Notes (Continued)

is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS configuration:

ENERGY_PERF_BIAS_CFG mode set to Performance

Hardware Prefetch set to Disable

VT Support set to Disable

Sysinfo program /home/CPU2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Mon Oct 21 04:23:57 2024

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
 14. cpupower frequency-info
 15. tuned-adm active
 16. sysctl
 17. /sys/kernel/mm/transparent_hugepage
 18. /sys/kernel/mm/transparent_hugepage/khugepaged
 19. OS release
 20. Disk information
 21. /sys/devices/virtual/dmi/id
 22. dmidecode
 23. BIOS
-

1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36cle09)
x86_64 x86_64 x86_64 GNU/Linux

2. w
04:23:57 up 30 min, 1 user, load average: 0.00, 0.00, 0.00
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root ttym1 - 04:21 13.00s 1.19s 0.03s sh
reportable-ic2024.1-lin-core-avx512-rate-smt-on-20240308.sh

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

meta brain NF3290G8 (Intel Xeon 6980P)

SPECrate®2017_int_base = 1220

SPECrate®2017_int_peak = 1260

CPU2017 License: 3358

Test Sponsor: IEIT Systems Co., Ltd.

Tested by: IEIT Systems Co., Ltd.

Test Date: Oct-2024

Hardware Availability: Oct-2024

Software Availability: Jun-2024

Platform Notes (Continued)

3. Username

From environment variable \$USER: root

4. ulimit -a

```
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 3092354
max locked memory       (kbytes, -l) 8192
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes       (-u) 3092354
virtual memory           (kbytes, -v) unlimited
file locks              (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize=31
login -- root
-bash
sh reportable-ic2024.1-lin-core-avx512-rate-smt-on-20240308.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=256 -c
  ic2024.1-lin-core-avx512-rate-20240308.cfg --define smt-on --define cores=128 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=256 --configfile
  ic2024.1-lin-core-avx512-rate-20240308.cfg --define smt-on --define cores=128 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
  --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.021/templogs/preenv.intrate.021.0.log --lognum 021.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/CPU2017
```

6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) 6980P
vendor_id       : GenuineIntel
cpu family     : 6
model          : 173
stepping        : 1
microcode       : 0x810002a0
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores       : 128
siblings        : 256
1 physical ids (chips)
256 processors (hardware threads)
physical id 0: core ids 0-42,64-106,128-169
physical id 0: apicids 0-85,128-213,256-339
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECrate®2017_int_base = 1220

meta brain NF3290G8 (Intel Xeon 6980P)

SPECrate®2017_int_peak = 1260

CPU2017 License: 3358

Test Date: Oct-2024

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Oct-2024

Tested by: IEIT Systems Co., Ltd.

Software Availability: Jun-2024

Platform Notes (Continued)

From lscpu from util-linux 2.39.3:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	52 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	256
On-line CPU(s) list:	0-255
Vendor ID:	GenuineIntel
BIOS Vendor ID:	Intel(R) Corporation
Model name:	Intel(R) Xeon(R) 6980P
BIOS Model name:	Intel(R) Xeon(R) 6980P CPU @ 2.0GHz
BIOS CPU family:	179
CPU family:	6
Model:	173
Thread(s) per core:	2
Core(s) per socket:	128
Socket(s):	1
Stepping:	1
Frequency boost:	enabled
CPU(s) scaling MHz:	41%
CPU max MHz:	2001.0000
CPU min MHz:	800.0000
BogoMIPS:	4000.00
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtstopology nonstop_tsc cpuid aperf mperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13 intel_ppin cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmil hle avx2 smep bmi2 erms invpcid rtm cqmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqmq_llc cqmq_occrap_llc cqmq_mbm_total cqmq_mbm_local split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts avx512vbmi umip pku ospek waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpocntdq la57 rdpid bus_lock_detect cldemote movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile amx_int8 flush_lll arch_capabilities
L1d cache:	6 MiB (128 instances)
L1i cache:	8 MiB (128 instances)
L2 cache:	256 MiB (128 instances)
L3 cache:	504 MiB (1 instance)
NUMA node(s):	3
NUMA node0 CPU(s):	0-42,128-170
NUMA node1 CPU(s):	43-85,171-213
NUMA node2 CPU(s):	86-127,214-255
Vulnerability Gather data sampling:	Not affected
Vulnerability Itlb multihit:	Not affected
Vulnerability L1tf:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Mmio stale data:	Not affected
Vulnerability Reg file data sampling:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec rstack overflow:	Not affected

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

meta brain NF3290G8 (Intel Xeon 6980P)

SPECrate®2017_int_base = 1220

SPECrate®2017_int_peak = 1260

CPU2017 License: 3358

Test Date: Oct-2024

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Oct-2024

Tested by: IEIT Systems Co., Ltd.

Software Availability: Jun-2024

Platform Notes (Continued)

Vulnerability Spec store bypass:

Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1:

Mitigation; usercopy/swapgs barriers and __user pointer sanitization

Vulnerability Spectre v2:

Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSB-eIBRS Not affected; BHI BHI_DIS_S

Vulnerability Srbds:

Not affected

Vulnerability Tsx async abort:

Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	6M	12	Data	1	64	1	64
L1i	64K	8M	16	Instruction	1	64	1	64
L2	2M	256M	16	Unified	2	2048	1	64
L3	504M	504M	16	Unified	3	516096	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 3 nodes (0-2)

node 0 cpus: 0-42,128-170

node 0 size: 257562 MB

node 0 free: 256630 MB

node 1 cpus: 43-85,171-213

node 1 size: 257986 MB

node 1 free: 257028 MB

node 2 cpus: 86-127,214-255

node 2 size: 257563 MB

node 2 free: 256203 MB

node distances:

node 0 1 2

0: 10 12 12

1: 12 10 12

2: 12 12 10

9. /proc/meminfo

MemTotal: 791668076 kB

10. who -r

run-level 3 Oct 21 03:54

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

Default Target Status

multi-user running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	apparmor auditd cron firewalld getty@ irqbalance issue-generator kbdsettings kdump kdump-early kdump-notify nvmefc-boot-connections nvmf-autoconnect postfix purge-kernels rollback sshd systemd-pstore wickedd wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell ebttables exchange-bmc-os-info fsidd grub2-once haveged hwloc-dump-hwdata ipmiевd issue-add-ssh-keys kexec-load nfs nfs-blkmap rpcbind rpmconfigcheck serial-getty@ systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd tuned
indirect	systemd-userdbd wickedd

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

meta brain NF3290G8 (Intel Xeon 6980P)

SPECrate®2017_int_base = 1220

SPECrate®2017_int_peak = 1260

CPU2017 License: 3358

Test Date: Oct-2024

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Oct-2024

Tested by: IEIT Systems Co., Ltd.

Software Availability: Jun-2024

Platform Notes (Continued)

```
13. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
    root=UUID=daf71402-9a22-417d-a498-881cf3539e73
    splash=silent
    mitigations=auto
    quiet
    security=apparmor
    crashkernel=384M,high
    crashkernel=72M,low
```

```
14. cpupower frequency-info
    analyzing CPU 1:
        current policy: frequency should be within 800 MHz and 2.00 GHz.
                    The governor "ondemand" may decide which speed to use
                    within this range.
    boost state support:
        Supported: yes
        Active: yes
```

```
15. tuned-adm active
    It seems that tuned daemon is not running, preset profile is not activated.
    Preset profile: throughput-performance
```

```
16. sysctl
    kernel.numa_balancing          1
    kernel.randomize_va_space      2
    vm.compaction_proactiveness   20
    vm.dirty_background_bytes      0
    vm.dirty_background_ratio     10
    vm.dirty_bytes                 0
    vm.dirty_expire_centisecs    3000
    vm.dirty_ratio                 20
    vm.dirty_writeback_centisecs  500
    vm.dirtytime_expire_seconds   43200
    vm.extfrag_threshold          500
    vm.min_unmapped_ratio         1
    vm.nr_hugepages                0
    vm.nr_hugepages_mempolicy      0
    vm.nr_overcommit_hugepages     0
    vm.swappiness                  60
    vm.watermark_boost_factor     15000
    vm.watermark_scale_factor      10
    vm.zone_reclaim_mode           0
```

```
17. /sys/kernel/mm/transparent_hugepage
    defrag      always defer defer+madvise [madvise] never
    enabled     [always] madvise never
    hpage_pmd_size 2097152
    shmem_enabled always within_size advise [never] deny force
```

```
18. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs   60000
    defrag                   1
```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

meta brain NF3290G8 (Intel Xeon 6980P)

SPECrate®2017_int_base = 1220

SPECrate®2017_int_peak = 1260

CPU2017 License: 3358

Test Sponsor: IEIT Systems Co., Ltd.

Tested by: IEIT Systems Co., Ltd.

Test Date: Oct-2024

Hardware Availability: Oct-2024

Software Availability: Jun-2024

Platform Notes (Continued)

```
max_ptes_none      511
max_ptes_shared    256
max_ptes_swap      64
pages_to_scan      4096
scan_sleep_millisecs 10000
```

```
-----  
19. OS release  
From /etc/*-release /etc/*-version  
os-release SUSE Linux Enterprise Server 15 SP6
```

```
-----  
20. Disk information  
SPEC is set to: /home/CPU2017  
Filesystem      Type   Size  Used Avail Use% Mounted on  
/dev/nvme0n1p2  btrfs   892G  217G  673G  25%  /home
```

```
-----  
21. /sys/devices/virtual/dmi/id  
Vendor:          IEIT SYSTEMS  
Product:         NF3290-M8-A0-R0-00  
Product Family: Not specified  
Serial:          000000000
```

```
-----  
22. dmidecode  
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section.  
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the  
"DMTF SMBIOS" standard.  
Memory:  
12x Micron MTC40F2046S1HC88XD1 WFFFG 64 GB 2 rank 8800
```

```
-----  
23. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor:      American Megatrends International, LLC.  
BIOS Version:     00.13.05  
BIOS Date:        09/29/2024
```

Compiler Version Notes

```
=====| 502.gcc_r(peak)
```

```
-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
```

```
=====| 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)  
| 557.xz_r(base, peak)
```

```
-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

SPECrate®2017_int_base = 1220

meta brain NF3290G8 (Intel Xeon 6980P)

SPECrate®2017_int_peak = 1260

CPU2017 License: 3358

Test Date: Oct-2024

Test Sponsor: IEIT Systems Co., Ltd.

Hardware Availability: Oct-2024

Tested by: IEIT Systems Co., Ltd.

Software Availability: Jun-2024

Compiler Version Notes (Continued)

C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
| 541.leela_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Fortran | 548.exchange2_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

meta brain NF3290G8 (Intel Xeon 6980P)

SPECrate®2017_int_base = 1220

SPECrate®2017_int_peak = 1260

CPU2017 License: 3358

Test Sponsor: IEIT Systems Co., Ltd.

Tested by: IEIT Systems Co., Ltd.

Test Date: Oct-2024

Hardware Availability: Oct-2024

Software Availability: Jun-2024

Base Portability Flags (Continued)

541.leela_r: -DSPEC_LP64

548.exchange2_r: -DSPEC_LP64

557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/opt/intel/oneapi/compiler/2024.1/lib -lgkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/opt/intel/oneapi/compiler/2024.1/lib -lgkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto  
-L/opt/intel/oneapi/compiler/2024.1/lib -lgkmalloc
```

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64

502.gcc_r: -D_FILE_OFFSET_BITS=64

505.mcf_r: -DSPEC_LP64

520.omnetpp_r: -DSPEC_LP64

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

meta brain NF3290G8 (Intel Xeon 6980P)

SPECrate®2017_int_base = 1220

SPECrate®2017_int_peak = 1260

CPU2017 License: 3358

Test Sponsor: IEIT Systems Co., Ltd.

Tested by: IEIT Systems Co., Ltd.

Test Date: Oct-2024

Hardware Availability: Oct-2024

Software Availability: Jun-2024

Peak Portability Flags (Continued)

523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX

525.x264_r: -DSPEC_LP64

531.deepsjeng_r: -DSPEC_LP64

541.leela_r: -DSPEC_LP64

548.exchange2_r: -DSPEC_LP64

557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.propdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

502.gcc_r: -m32 -L/opt/intel/oneapi/compiler/2024.1/lib32 -std=gnu89
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.propdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

IEIT Systems Co., Ltd.

meta brain NF3290G8 (Intel Xeon 6980P)

SPECrate®2017_int_base = 1220

SPECrate®2017_int_peak = 1260

CPU2017 License: 3358

Test Sponsor: IEIT Systems Co., Ltd.

Tested by: IEIT Systems Co., Ltd.

Test Date: Oct-2024

Hardware Availability: Oct-2024

Software Availability: Jun-2024

Peak Optimization Flags (Continued)

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/IEIT-Platform-Settings-intel-V1.1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/IEIT-Platform-Settings-intel-V1.1.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2024-10-21 04:23:56-0400.

Report generated on 2024-11-06 12:22:58 by CPU2017 PDF formatter v6716.

Originally published on 2024-11-05.