



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V3  
(3.60 GHz, Intel Xeon Gold 6544Y)

SPECspeed®2017\_int\_base = 14.6

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

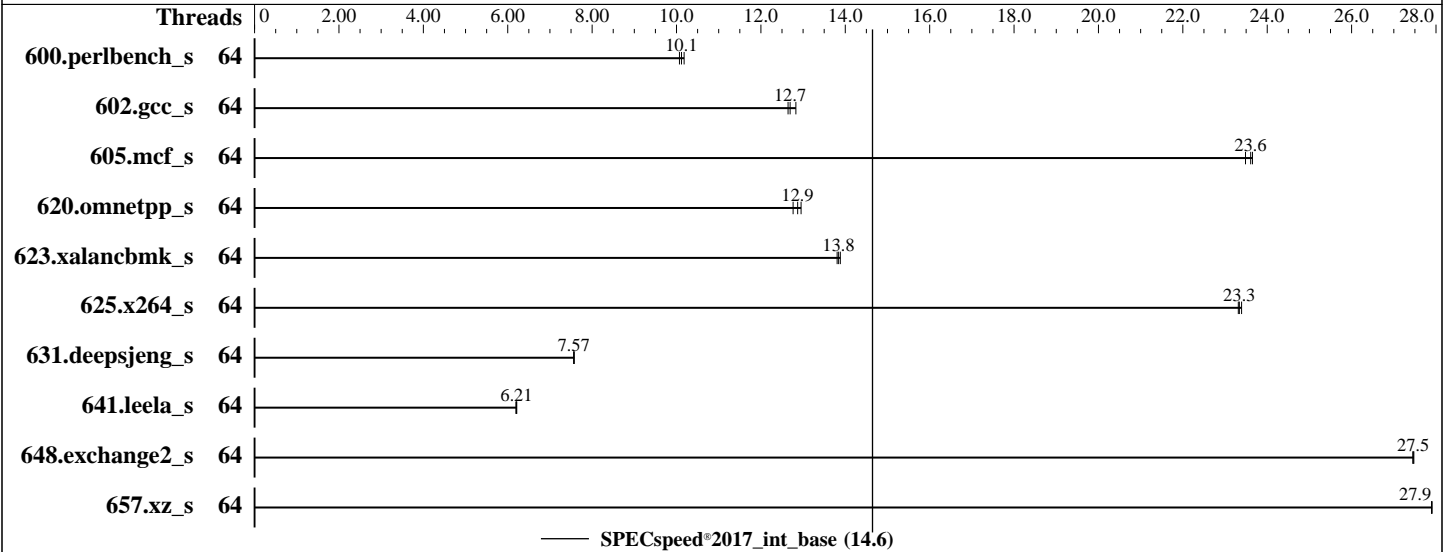
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Feb-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023



### Hardware

CPU Name: Intel Xeon Gold 6544Y  
 Max MHz: 4100  
 Nominal: 3600  
 Enabled: 32 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 45 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 5200)  
 Storage: 1 x 960 GB SATA SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux 9.2 (Plow)  
 Kernel 5.14.0-284.11.1.el9\_2.x86\_64  
 Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;  
 Parallel: Yes  
 Firmware: Lenovo BIOS Version ESE121V 3.10 released Jan-2024  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V3  
(3.60 GHz, Intel Xeon Gold 6544Y)

SPECspeed®2017\_int\_base = 14.6

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017  
Test Sponsor: Lenovo Global Technology  
Tested by: Lenovo Global Technology

Test Date: Feb-2024  
Hardware Availability: Feb-2024  
Software Availability: Dec-2023

## Results Table

| Benchmark       | Base    |             |             |            |             |            |             | Peak    |         |       |         |       |         |       |
|-----------------|---------|-------------|-------------|------------|-------------|------------|-------------|---------|---------|-------|---------|-------|---------|-------|
|                 | Threads | Seconds     | Ratio       | Seconds    | Ratio       | Seconds    | Ratio       | Threads | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 600.perlbench_s | 64      | <b>175</b>  | <b>10.1</b> | 176        | 10.1        | 174        | 10.2        |         |         |       |         |       |         |       |
| 602.gcc_s       | 64      | 310         | 12.8        | 315        | 12.6        | <b>314</b> | <b>12.7</b> |         |         |       |         |       |         |       |
| 605.mcf_s       | 64      | 200         | 23.7        | 201        | 23.5        | <b>200</b> | <b>23.6</b> |         |         |       |         |       |         |       |
| 620.omnetpp_s   | 64      | 128         | 12.8        | 126        | 13.0        | <b>127</b> | <b>12.9</b> |         |         |       |         |       |         |       |
| 623.xalancbmk_s | 64      | 102         | 13.9        | <b>102</b> | <b>13.8</b> | 103        | 13.8        |         |         |       |         |       |         |       |
| 625.x264_s      | 64      | <b>75.6</b> | <b>23.3</b> | 75.7       | 23.3        | 75.4       | 23.4        |         |         |       |         |       |         |       |
| 631.deepsjeng_s | 64      | 189         | 7.56        | <b>189</b> | <b>7.57</b> | 189        | 7.58        |         |         |       |         |       |         |       |
| 641.leela_s     | 64      | <b>275</b>  | <b>6.21</b> | 275        | 6.21        | 275        | 6.19        |         |         |       |         |       |         |       |
| 648.exchange2_s | 64      | 107         | 27.5        | 107        | 27.5        | <b>107</b> | <b>27.5</b> |         |         |       |         |       |         |       |
| 657.xz_s        | 64      | 222         | 27.9        | 222        | 27.9        | <b>222</b> | <b>27.9</b> |         |         |       |         |       |         |       |

SPECspeed®2017\_int\_base = 14.6

SPECspeed®2017\_int\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH =

"/home/cpu2017-1.1.9-ic2023.2.3/lib/intel64:/home/cpu2017-1.1.9-ic2023.2.3/lib/ia32:/home/cpu2017-1.1.9-ic2023.2.3/je5.0.1-64"

MALLOC\_CONF = "retain:true"

OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM

memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECspeed®2017\_int\_base = 14.6

ThinkSystem SR650 V3  
(3.60 GHz, Intel Xeon Gold 6544Y)

SPECspeed®2017\_int\_peak = Not Run

**CPU2017 License:** 9017

**Test Date:** Feb-2024

**Test Sponsor:** Lenovo Global Technology

**Hardware Availability:** Feb-2024

**Tested by:** Lenovo Global Technology

**Software Availability:** Dec-2023

## Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance and then set it to Custom Mode  
C-States set to Legacy

Sysinfo program /home/cpu2017-1.1.9-ic2023.2.3/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost.localdomain Sun Feb 18 21:27:56 2024

SUT (System Under Test) info as seen by some common utilities.

### Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-13.el9\_2)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

```
1. uname -a
Linux localhost.localdomain 5.14.0-284.11.1.el9_2.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 12 10:45:03 EDT
2023 x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
21:27:56 up 32 min, 1 user, load average: 1.31, 4.34, 2.80
USER      TTY      LOGIN@  IDLE   JCPU   PCPU   WHAT
root      tty1     20:55   44.00s 0.75s  0.00s  -bash
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECspeed®2017\_int\_base = 14.6

ThinkSystem SR650 V3  
(3.60 GHz, Intel Xeon Gold 6544Y)

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Feb-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

### Platform Notes (Continued)

```

pending signals                (-i) 4127019
max locked memory              (kbytes, -l) 64
max memory size                (kbytes, -m) unlimited
open files                     (-n) 1024
pipe size                      (512 bytes, -p) 8
POSIX message queues          (bytes, -q) 819200
real-time priority            (-r) 0
stack size                    (kbytes, -s) unlimited
cpu time                      (seconds, -t) unlimited
max user processes            (-u) 4127019
virtual memory                 (kbytes, -v) unlimited
file locks                    (-x) unlimited

```

#### 5. sysinfo process ancestry

```

/usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags -c
  ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg --define cores=32 --tune base -o all --define
  intspeedaffinity --define smt-on --define drop_caches intspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
  ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg --define cores=32 --tune base --output_format all
  --define intspeedaffinity --define smt-on --define drop_caches --nopower --runmode speed --tune base
  --size refspeed intspeed --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.203/templogs/preenv.intspeed.203.0.log --lognum 203.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-ic2023.2.3

```

#### 6. /proc/cpuinfo

```

model name      : INTEL(R) XEON(R) GOLD 6544Y
vendor_id      : GenuineIntel
cpu family     : 6
model          : 207
stepping      : 2
microcode     : 0x21000200
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores     : 16
siblings      : 32
2 physical ids (chips)
64 processors (hardware threads)
physical id 0: core ids 0-15
physical id 1: core ids 0-15
physical id 0: apicids 0-31
physical id 1: apicids 128-159
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

```

#### 7. lscpu

```

From lscpu from util-linux 2.37.4:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63

```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECspeed®2017\_int\_base = 14.6

ThinkSystem SR650 V3  
(3.60 GHz, Intel Xeon Gold 6544Y)

SPECspeed®2017\_int\_peak = Not Run

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Feb-2024  
**Hardware Availability:** Feb-2024  
**Software Availability:** Dec-2023

### Platform Notes (Continued)

```

Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
Model name: INTEL(R) XEON(R) GOLD 6544Y
BIOS Model name: INTEL(R) XEON(R) GOLD 6544Y
CPU family: 6
Model: 207
Thread(s) per core: 2
Core(s) per socket: 16
Socket(s): 2
Stepping: 2
BogoMIPS: 7200.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good noopl xtopology
nonstop_tsc cpuid aperfperf tsc_known_freq pni pclmulqdq dtes64 monitor
ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
vmni flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2
erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local avx_vnni
avx512_bf16 wbnoinvd dtherm ida arat pln pts hfi avx512vbmi umip pku ospke
waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16
avx512_fp16 amx_tile amx_int8 flush_lld arch_capabilities

Virtualization: VT-x
L1d cache: 1.5 MiB (32 instances)
L1i cache: 1 MiB (32 instances)
L2 cache: 64 MiB (32 instances)
L3 cache: 90 MiB (2 instances)
NUMA node(s): 2
NUMA node0 CPU(s): 0-15,32-47
NUMA node1 CPU(s): 16-31,48-63
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSE-eIBRS SW
sequence
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE        | LEVEL | SETS  | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|-------|----------|----------------|
| L1d  | 48K      | 1.5M     | 12   | Data        | 1     | 64    | 1        | 64             |
| L1i  | 32K      | 1M       | 8    | Instruction | 1     | 64    | 1        | 64             |
| L2   | 2M       | 64M      | 16   | Unified     | 2     | 2048  | 1        | 64             |
| L3   | 45M      | 90M      | 15   | Unified     | 3     | 49152 | 1        | 64             |

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.  
available: 2 nodes (0-1)

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECspeed®2017\_int\_base = 14.6

ThinkSystem SR650 V3  
(3.60 GHz, Intel Xeon Gold 6544Y)

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Feb-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

### Platform Notes (Continued)

```
node 0 cpus: 0-15,32-47
node 0 size: 515713 MB
node 0 free: 514687 MB
node 1 cpus: 16-31,48-63
node 1 size: 516083 MB
node 1 free: 514674 MB
node distances:
node  0  1
  0:  10  21
  1:  21  10
```

```
9. /proc/meminfo
MemTotal:      1056560192 kB
```

```
10. who -r
run-level 3 Feb 18 20:55
```

```
11. Systemd service manager version: systemd 252 (252-13.e19_2)
Default Target Status
multi-user      running
```

```
12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online atd auditd bluetooth
chronyd crond dbus-broker firewalld getty@ insights-client-boot irqbalance iscsi
iscsi-onboot kdump libstoragemgmt low-memory-monitor lvm2-monitor mcelog mdmonitor
microcode multipathd nis-domainname nvme-fc-boot-connections rhsmcertd rsyslog rtkit-daemon
selinux-autorelabel-mark smartd sshd sssd systemd-boot-update systemd-network-generator
tuned udisks2 upower
enabled-runtime systemd-remount-fs
disabled arp-ethers blk-availability canberra-system-bootup canberra-system-shutdown
canberra-system-shutdown-reboot chrony-wait console-getty cpupower debug-shell
dnf-system-upgrade iprdump iprinit iprupdate iscsid iscsiui0 kpatch kvm_stat ledmon
man-db-restart-cache-update nftables nvme-fc-autoconnect pesign psacct rdisc rhcd rhsm
rhsm-facts rpmd-b-rebuild selinux-check-proper-disable serial-getty@ sshd-keygen@
systemd-boot-check-no-failures systemd-pstore systemd-sysex
indirect sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate
systemd-sysupdate-reboot
```

```
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd3,gpt2)/vmlinuz-5.14.0-284.11.1.e19_2.x86_64
root=/dev/mapper/rhel-root
ro
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap
rhgb
quiet
```

```
14. cpupower frequency-info
analyzing CPU 0:
Unable to determine current policy
boost state support:
Supported: yes
```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V3  
(3.60 GHz, Intel Xeon Gold 6544Y)

SPECspeed®2017\_int\_base = 14.6

SPECspeed®2017\_int\_peak = Not Run

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Feb-2024

**Hardware Availability:** Feb-2024

**Software Availability:** Dec-2023

### Platform Notes (Continued)

Active: yes

-----  
15. tuned-adm active  
Current active profile: throughput-performance

-----  
16. sysctl  
kernel.numa\_balancing 1  
kernel.randomize\_va\_space 2  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000  
vm.dirty\_ratio 40  
vm.dirty\_writeback\_centisecs 500  
vm.dirtytime\_expire\_seconds 43200  
vm.extfrag\_threshold 500  
vm.min\_unmapped\_ratio 1  
vm.nr\_hugepages 0  
vm.nr\_hugepages\_mempolicy 0  
vm.nr\_overcommit\_hugepages 0  
vm.swappiness 10  
vm.watermark\_boost\_factor 15000  
vm.watermark\_scale\_factor 10  
vm.zone\_reclaim\_mode 0

-----  
17. /sys/kernel/mm/transparent\_hugepage  
defrag always defer defer+madvice [madvice] never  
enabled [always] madvice never  
hpage\_pmd\_size 2097152  
shmem\_enabled always within\_size advise [never] deny force

-----  
18. /sys/kernel/mm/transparent\_hugepage/khugepaged  
alloc\_sleep\_millisecs 60000  
defrag 1  
max\_ptes\_none 511  
max\_ptes\_shared 256  
max\_ptes\_swap 64  
pages\_to\_scan 4096  
scan\_sleep\_millisecs 10000

-----  
19. OS release  
From /etc/\*-release /etc/\*-version  
os-release Red Hat Enterprise Linux 9.2 (Plow)  
redhat-release Red Hat Enterprise Linux release 9.2 (Plow)  
system-release Red Hat Enterprise Linux release 9.2 (Plow)

-----  
20. Disk information  
SPEC is set to: /home/cpu2017-1.1.9-ic2023.2.3  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/mapper/rhel-home xfs 819G 54G 765G 7% /home

-----  
21. /sys/devices/virtual/dmi/id

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECspeed®2017\_int\_base = 14.6

ThinkSystem SR650 V3  
(3.60 GHz, Intel Xeon Gold 6544Y)

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Feb-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

### Platform Notes (Continued)

Vendor: Lenovo  
Product: ThinkSystem SR650 V3 MB,EGS,DDR5,SH,2U  
Product Family: ThinkSystem  
Serial: 1234567890

#### 22. dmidecode

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

#### Memory:

5x Samsung M321R8GA0PB0-CWMKH 64 GB 2 rank 5600, configured at 5200  
11x Samsung M321R8GA0PB0-CWMXH 64 GB 2 rank 5600, configured at 5200

#### 23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Lenovo  
BIOS Version: ESE121V-3.10  
BIOS Date: 01/09/2024  
BIOS Revision: 3.10  
Firmware Revision: 3.90

### Compiler Version Notes

C | 600.perlbench\_s(base) 602.gcc\_s(base) 605.mcf\_s(base) 625.x264\_s(base) 657.xz\_s(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C++ | 620.omnetpp\_s(base) 623.xalancbmk\_s(base) 631.deepsjeng\_s(base) 641.leela\_s(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Fortran | 648.exchange2\_s(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

### Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

(Continued on next page)





# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

SPECspeed®2017\_int\_base = 14.6

ThinkSystem SR650 V3  
(3.60 GHz, Intel Xeon Gold 6544Y)

SPECspeed®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Feb-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifx

## Base Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V3  
(3.60 GHz, Intel Xeon Gold 6544Y)

SPECspeed®2017\_int\_base = 14.6

SPECspeed®2017\_int\_peak = Not Run

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Feb-2024

**Hardware Availability:** Feb-2024

**Software Availability:** Dec-2023

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2024-02-18 21:27:56-0500.

Report generated on 2024-03-14 10:59:48 by CPU2017 PDF formatter v6716.

Originally published on 2024-03-13.