



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR950 V3
(2.10 GHz, Intel Xeon Platinum 8468H)

SPECspeed®2017_int_base = 14.0

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017

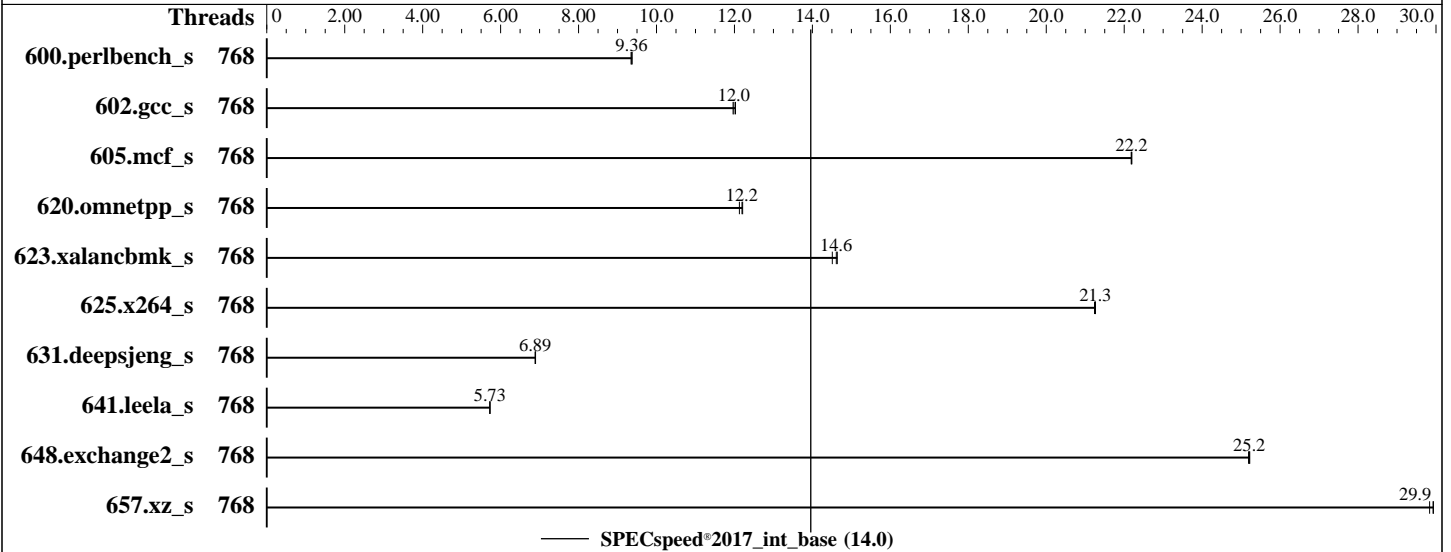
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Dec-2023

Hardware Availability: Oct-2023

Software Availability: Dec-2023



Hardware

CPU Name: Intel Xeon Platinum 8468H
 Max MHz: 3800
 Nominal: 2100
 Enabled: 384 cores, 8 chips, 2 threads/core
 Orderable: 8 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 105 MB I+D on chip per chip
 Other: None
 Memory: 4 TB (64 x 64 GB 2Rx4 PC5-4800B-R)
 Storage: 1 x 480 GB SATA SSD
 Other: None

Software

OS: Red Hat Enterprise Linux 9.2 (Plow)
 Kernel 5.14.0-284.11.1.el9_2.x86_64
 Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;
 Parallel: Yes
 Firmware: Lenovo BIOS Version EBE103M 1.10 released Oct-2023
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR950 V3
(2.10 GHz, Intel Xeon Platinum 8468H)

SPECspeed®2017_int_base = 14.0

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Dec-2023
Hardware Availability: Oct-2023
Software Availability: Dec-2023

Results Table

Benchmark	Base								Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
600.perlbench_s	768	189	9.37	<u>190</u>	<u>9.36</u>	190	9.35									
602.gcc_s	768	331	12.0	<u>332</u>	<u>12.0</u>	333	12.0									
605.mcf_s	768	213	22.2	<u>213</u>	<u>22.2</u>	213	22.2									
620.omnetpp_s	768	134	12.2	134	12.1	<u>134</u>	<u>12.2</u>									
623.xalancbmk_s	768	<u>97.0</u>	<u>14.6</u>	96.8	14.6	97.7	14.5									
625.x264_s	768	83.1	21.2	<u>83.0</u>	<u>21.3</u>	83.0	21.3									
631.deepsjeng_s	768	208	6.89	<u>208</u>	<u>6.89</u>	208	6.90									
641.leela_s	768	298	5.72	298	5.73	<u>298</u>	<u>5.73</u>									
648.exchange2_s	768	<u>117</u>	<u>25.2</u>	117	25.2	117	25.2									
657.xz_s	768	207	29.9	<u>207</u>	<u>29.9</u>	207	29.8									

SPECspeed®2017_int_base = 14.0

SPECspeed®2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH =

"/home/cpu2017-1.1.9-ic2023.2.3/lib/intel64:/home/cpu2017-1.1.9-ic2023.2.3/lib/ia32:/home/cpu2017-1.1.9-ic2023.2.3/je5.0.1-64"

MALLOC_CONF = "retain:true"

OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM

memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017_int_base = 14.0

ThinkSystem SR950 V3
(2.10 GHz, Intel Xeon Platinum 8468H)

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017

Test Date: Dec-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Oct-2023

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance and then set it to Custom Mode

C-States set to Legacy

SNC set to SNC2

```
Sysinfo program /home/cpu2017-1.1.9-ic2023.2.3/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost.localdomain Thu Dec 7 04:53:45 2023
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-13.e19_2)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

```
1. uname -a
Linux localhost.localdomain 5.14.0-284.11.1.e19_2.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 12 10:45:03 EDT
2023 x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
04:53:46 up 4:05, 1 user, load average: 0.38, 4.21, 3.52
USER TTY LOGIN@ IDLE JCPU PCPU WHAT
root tty1 00:49 3:27m 0.90s 0.00s /bin/bash ./speccpu_rock.sh
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017_int_base = 14.0

ThinkSystem SR950 V3
(2.10 GHz, Intel Xeon Platinum 8468H)

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017

Test Date: Dec-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Oct-2023

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

Platform Notes (Continued)

```

pending signals                (-i) 16511320
max locked memory              (kbytes, -l) 64
max memory size                (kbytes, -m) unlimited
open files                     (-n) 1024
pipe size                      (512 bytes, -p) 8
POSIX message queues          (bytes, -q) 819200
real-time priority             (-r) 0
stack size                    (kbytes, -s) unlimited
cpu time                      (seconds, -t) unlimited
max user processes            (-u) 16511320
virtual memory                 (kbytes, -v) unlimited
file locks                    (-x) unlimited

```

5. sysinfo process ancestry

```

/usr/lib/systemd/systemd --switched-root --system --deserialize 31
login -- root
-bash
/bin/bash ./speccpu_rock.sh
/bin/bash ./speccpu_rock.sh
runcpu --nobuild --action validate --define default-platform-flags -c
  ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg --define cores=384 --tune base -o all --define
  intspeedaffinity --define smt-on --define drop_caches intspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
  ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg --define cores=384 --tune base --output_format all
  --define intspeedaffinity --define smt-on --define drop_caches --nopower --runmode speed --tune base
  --size refspeed intspeed --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.054/templogs/preenv.intspeed.054.0.log --lognum 054.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-ic2023.2.3

```

6. /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) Platinum 8468H
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping      : 8
microcode      : 0x2b0004b1
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores      : 48
siblings       : 96
8 physical ids (chips)
768 processors (hardware threads)
physical id 0: core ids 0-47
physical id 1: core ids 0-47
physical id 2: core ids 0-47
physical id 3: core ids 0-47
physical id 4: core ids 0-47
physical id 5: core ids 0-47
physical id 6: core ids 0-47
physical id 7: core ids 0-47
physical id 0: apicids 0-95
physical id 1: apicids 128-223
physical id 2: apicids 256-351
physical id 3: apicids 384-479
physical id 4: apicids 512-607
physical id 5: apicids 640-735
physical id 6: apicids 768-863
physical id 7: apicids 896-991

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017_int_base = 14.0

ThinkSystem SR950 V3
(2.10 GHz, Intel Xeon Platinum 8468H)

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Dec-2023
Hardware Availability: Oct-2023
Software Availability: Dec-2023

Platform Notes (Continued)

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          46 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 768
On-line CPU(s) list:   0-767
Vendor ID:              GenuineIntel
BIOS Vendor ID:        Intel(R) Corporation
Model name:             Intel(R) Xeon(R) Platinum 8468H
BIOS Model name:       Intel(R) Xeon(R) Platinum 8468H
CPU family:             6
Model:                  143
Thread(s) per core:    2
Core(s) per socket:    48
Socket(s):              8
Stepping:               8
CPU max MHz:           3800.0000
CPU min MHz:           800.0000
BogoMIPS:               4200.00
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                        ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                        sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                        lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                        invpcid_single intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced
                        tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bml avx2
                        smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                        xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                        cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                        arat pln pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
                        vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpocntdq la57 rdpid
                        bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
                        tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile amx_int8
                        flush_lld arch_capabilities
Virtualization:         VT-x
L1d cache:             18 MiB (384 instances)
L1i cache:             12 MiB (384 instances)
L2 cache:              768 MiB (384 instances)
L3 cache:              840 MiB (8 instances)
NUMA node(s):          16
NUMA node0 CPU(s):     0-23,384-407
NUMA node1 CPU(s):     24-47,408-431
NUMA node2 CPU(s):     48-71,432-455
NUMA node3 CPU(s):     72-95,456-479
NUMA node4 CPU(s):     96-119,480-503
NUMA node5 CPU(s):     120-143,504-527
NUMA node6 CPU(s):     144-167,528-551
NUMA node7 CPU(s):     168-191,552-575
NUMA node8 CPU(s):     192-215,576-599
NUMA node9 CPU(s):     216-239,600-623

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017_int_base = 14.0

ThinkSystem SR950 V3
(2.10 GHz, Intel Xeon Platinum 8468H)

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017

Test Date: Dec-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Oct-2023

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

Platform Notes (Continued)

```

NUMA node10 CPU(s):          240-263,624-647
NUMA node11 CPU(s):          264-287,648-671
NUMA node12 CPU(s):          288-311,672-695
NUMA node13 CPU(s):          312-335,696-719
NUMA node14 CPU(s):          336-359,720-743
NUMA node15 CPU(s):          360-383,744-767
Vulnerability Itlb multihit: Not affected
Vulnerability Lltf:          Not affected
Vulnerability Mds:           Not affected
Vulnerability Meltdown:     Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed:     Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:    Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:    Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBR SB-eIBRS SW
                               sequence
Vulnerability Srbds:         Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	18M	12	Data	1	64	1	64
L1i	32K	12M	8	Instruction	1	64	1	64
L2	2M	768M	16	Unified	2	2048	1	64
L3	105M	840M	15	Unified	3	114688	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 16 nodes (0-15)
node 0 cpus: 0-23,384-407
node 0 size: 257374 MB
node 0 free: 256717 MB
node 1 cpus: 24-47,408-431
node 1 size: 258037 MB
node 1 free: 257369 MB
node 2 cpus: 48-71,432-455
node 2 size: 258037 MB
node 2 free: 256615 MB
node 3 cpus: 72-95,456-479
node 3 size: 258037 MB
node 3 free: 257544 MB
node 4 cpus: 96-119,480-503
node 4 size: 257998 MB
node 4 free: 257467 MB
node 5 cpus: 120-143,504-527
node 5 size: 258037 MB
node 5 free: 257600 MB
node 6 cpus: 144-167,528-551
node 6 size: 258037 MB
node 6 free: 256461 MB
node 7 cpus: 168-191,552-575
node 7 size: 258037 MB
node 7 free: 257648 MB
node 8 cpus: 192-215,576-599
node 8 size: 258037 MB
node 8 free: 256400 MB
node 9 cpus: 216-239,600-623
node 9 size: 258037 MB
node 9 free: 257593 MB

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017_int_base = 14.0

ThinkSystem SR950 V3
(2.10 GHz, Intel Xeon Platinum 8468H)

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017

Test Date: Dec-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Oct-2023

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

Platform Notes (Continued)

```

node 10 cpus: 240-263,624-647
node 10 size: 258037 MB
node 10 free: 257622 MB
node 11 cpus: 264-287,648-671
node 11 size: 258037 MB
node 11 free: 257659 MB
node 12 cpus: 288-311,672-695
node 12 size: 258037 MB
node 12 free: 257404 MB
node 13 cpus: 312-335,696-719
node 13 size: 258037 MB
node 13 free: 257005 MB
node 14 cpus: 336-359,720-743
node 14 size: 258037 MB
node 14 free: 257684 MB
node 15 cpus: 360-383,744-767
node 15 size: 258011 MB
node 15 free: 257639 MB
node distances:
node 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
0: 10 12 21 21 21 21 21 31 31 31 21 21 21 21 31 31
1: 12 10 21 21 21 21 21 31 31 31 21 21 21 21 31 31
2: 21 21 10 12 31 31 21 21 21 21 31 31 31 31 21 21
3: 21 21 12 10 31 31 21 21 21 21 31 31 31 31 21 21
4: 21 21 31 31 10 12 21 21 21 21 31 31 31 31 21 21
5: 21 21 31 31 12 10 21 21 21 21 31 31 31 31 21 21
6: 31 31 21 21 21 21 10 12 31 31 21 21 21 21 31 31
7: 31 31 21 21 21 21 12 10 31 31 21 21 21 21 31 31
8: 31 31 21 21 21 21 31 31 10 12 21 21 21 21 31 31
9: 31 31 21 21 21 21 31 31 12 10 21 21 21 21 31 31
10: 21 21 31 31 31 31 21 21 21 21 10 12 31 31 21 21
11: 21 21 31 31 31 31 21 21 21 21 12 10 31 31 21 21
12: 21 21 31 31 31 31 21 21 21 21 31 31 10 12 21 21
13: 21 21 31 31 31 31 21 21 21 21 31 31 12 10 21 21
14: 31 31 21 21 21 21 31 31 31 31 21 21 21 21 10 12
15: 31 31 21 21 21 21 31 31 31 31 21 21 21 21 12 10

```

```

-----
9. /proc/meminfo
MemTotal: 4226938960 kB

```

```

-----
10. who -r
run-level 3 Dec 7 00:49

```

```

-----
11. Systemd service manager version: systemd 252 (252-13.e19_2)
Default Target Status
multi-user running

```

```

-----
12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd chronyd crond
dbus-broker firewalld getty@ insights-client-boot irqbalance kdump low-memory-monitor
mdmonitor microcode nis-domainname rhsmcertd rsyslog rtkit-daemon selinux-autorelabel-mark
sshd sssd systemd-boot-update systemd-network-generator udisks2 upower
enabled-runtime systemd-remount-fs
disabled canberra-system-bootup canberra-system-shutdown canberra-system-shutdown-reboot
chrony-wait console-getty cpupower debug-shell dnf-system-upgrade kvm_stat

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017_int_base = 14.0

ThinkSystem SR950 V3
(2.10 GHz, Intel Xeon Platinum 8468H)

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017

Test Date: Dec-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Oct-2023

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

Platform Notes (Continued)

```

indirect
man-db-restart-cache-update nftables pesign rdisc rhod rhsm rhsm-facts rpmdb-rebuild
selinux-check-proper-disable serial-getty@ sshd-keygen@ systemd-boot-check-no-failures
systemd-pstore systemd-sysex
sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate
systemd-sysupdate-reboot

```

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd1,gpt2)/boot/vmlinuz-5.14.0-284.11.1.el9_2.x86_64
root=UUID=116409c2-57ac-4857-ace6-bb315b1769ff
ro
resume=UUID=075e4fda-52f2-4584-8323-c813820fb1bd

```

```

-----
14. cpupower frequency-info
analyzing CPU 0:
  current policy: frequency should be within 800 MHz and 3.80 GHz.
                   The governor "performance" may decide which speed to use
                   within this range.

  boost state support:
    Supported: yes
    Active: yes

```

```

-----
15. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space      2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                  20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                    60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0

```

```

-----
16. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force

```

```

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017_int_base = 14.0

ThinkSystem SR950 V3
(2.10 GHz, Intel Xeon Platinum 8468H)

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Dec-2023
Hardware Availability: Oct-2023
Software Availability: Dec-2023

Platform Notes (Continued)

scan_sleep_millisecs 10000

18. OS release

From /etc/*-release /etc/*-version
os-release Red Hat Enterprise Linux 9.2 (Plow)
redhat-release Red Hat Enterprise Linux release 9.2 (Plow)
system-release Red Hat Enterprise Linux release 9.2 (Plow)

19. Disk information

SPEC is set to: /home/cpu2017-1.1.9-ic2023.2.3
Filesystem Type Size Used Avail Use% Mounted on
/dev/sdc4 xfs 371G 235G 136G 64% /home

20. /sys/devices/virtual/dmi/id

Vendor: Lenovo
Product: ThinkSystem SR950 V3
Product Family: ThinkSystem
Serial: BLRSDV044

21. dmidecode

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
41x SK Hynix HMC94AEBRA102N 64 GB 2 rank 4800
14x SK Hynix HMC94AEBRA109N 64 GB 2 rank 4800
9x SK Hynix HMC94AEBRA123N 64 GB 2 rank 4800

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Lenovo
BIOS Version: EBE103M-1.10
BIOS Date: 10/10/2023
BIOS Revision: 1.10
Firmware Revision: 1.10

Compiler Version Notes

C | 600.perlbenc_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base) 657.xz_s(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C++ | 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base) 641.leela_s(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR950 V3
(2.10 GHz, Intel Xeon Platinum 8468H)

SPECspeed®2017_int_base = 14.0

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Dec-2023

Hardware Availability: Oct-2023

Software Availability: Dec-2023

Compiler Version Notes (Continued)

Fortran | 648.exchange2_s(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR950 V3
(2.10 GHz, Intel Xeon Platinum 8468H)

SPECspeed®2017_int_base = 14.0

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Dec-2023

Hardware Availability: Oct-2023

Software Availability: Dec-2023

Base Optimization Flags (Continued)

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2023-12-06 15:53:45-0500.

Report generated on 2024-01-03 17:39:56 by CPU2017 PDF formatter v6716.

Originally published on 2024-01-02.