SPEC CPU®2017 Integer Speed Result

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Dell Inc.

$SPECspeed^®2017\_int\_base = 11.0$

PowerEdge R760 (Intel Xeon Platinum 8452Y)

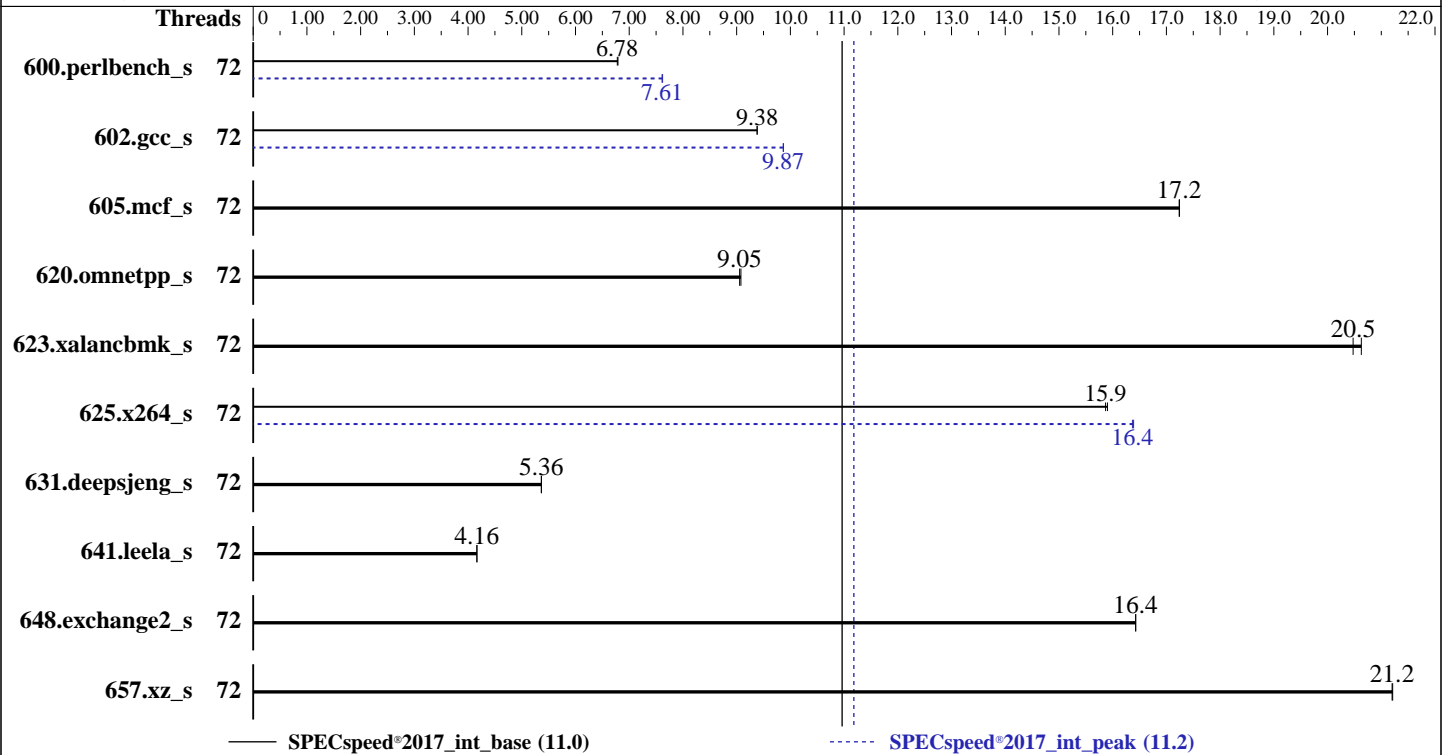$SPECspeed^®2017\_int\_peak = 11.2$

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Dec-2022
Hardware Availability: Feb-2023
Software Availability: Jun-2022

Threads

Benchmark	Threads	base	peak
600.perlbench_s	72	6.78	7.61
602.gcc_s	72	9.38	9.87
605.mcf_s	72	17.2	
620.omnetpp_s	72	9.05	
623.xalancbmk_s	72	20.5	
625.x264_s	72	15.9	16.4
631.deepsjeng_s	72	5.36	
641.leela_s	72	4.16	
648.exchange2_s	72	16.4	
657.xz_s	72	21.2	

SPECspeed®2017_int_base (11.0)   SPECspeed®2017_int_peak (11.2)

Hardware

CPU Name: Intel Xeon Platinum 8452Y
 Max MHz: 3200
 Nominal: 2000
Enabled: 72 cores, 2 chips
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 67.5 MB I+D on chip per chip
 Other: None
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)
Storage: 125 GB on tmpfs
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP4
 5.14.21-150400.22-default
Compiler: C/C++: Version 2022.1 of Intel oneAPI DPC++/C++
 Compiler for Linux;
 Fortran: Version 2022.1 of Intel Fortran Compiler
 for Linux;
Parallel: Yes
Firmware: Version 0.3.1 released Nov-2022
File System: tmpfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance
 at the cost of additional power usage.

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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	72	261	6.79	**262**	**6.78**			72	233	7.62	**233**	**7.61**		
602.gcc_s	72	**425**	**9.38**	424	9.38			72	403	9.87	**404**	**9.87**		
605.mcf_s	72	274	17.2	**274**	**17.2**			72	274	17.2	**274**	**17.2**		
620.omnetpp_s	72	**180**	**9.05**	180	9.08			72	**180**	**9.05**	180	9.08		
623.xalancbmk_s	72	**69.2**	**20.5**	68.7	20.6			72	**69.2**	**20.5**	68.7	20.6		
625.x264_s	72	**111**	**15.9**	111	15.9			72	**108**	**16.4**	108	16.4		
631.deepsjeng_s	72	267	5.36	**267**	**5.36**			72	267	5.36	**267**	**5.36**		
641.leela_s	72	**410**	**4.16**	410	4.17			72	**410**	**4.16**	410	4.17		
648.exchange2_s	72	**179**	**16.4**	179	16.4			72	**179**	**16.4**	179	16.4		
657.xz_s	72	291	21.2	**292**	**21.2**			72	291	21.2	**292**	**21.2**		
SPECspeed®2017_int_base =		**11.0**												
SPECspeed®2017_int_peak =		**11.2**												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
SPEC has ruled that the compiler used for this result was performing a compilation
that specifically improves the performance of the 523.xalancbmk_r / 623.xalancbmk_s
benchmarks using a priori knowledge of the SPEC code and dataset to perform a
transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4
https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer
publish results using this optimization.

This result is left in the SPEC results database for historical reference.
```

Operating System Notes

```
 Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH =
     "/mnt/ramdisk/cpu2017-1.1.8-ic2022.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.8-ic2022.1/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

```
 Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
 memory using Redhat Enterprise Linux 8.0
 Transparent Huge Pages enabled by default
```

Standard Performance Evaluation Corporation (info@spec.org) https://www.spec.org/

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SPECspeed®2017_int_peak = 11.2

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General Notes (Continued)

```
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>          /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"
```

Platform Notes

```
BIOS settings:
                   ADDDC Setting : Disabled
          DIMM Self Healing on
  Uncorrectable Memory Error : Disabled
   Virtualization Technology : Disabled
           Logical Processor : Disabled
             Sub NUMA Cluster : 2-way Clustering
     DCU Streamer Prefetcher : Disabled
                 LLC Prefetch : Disabled
          Dead Line LLC Alloc : Disabled
               Optimizer Mode : Enabled

               System Profile : Custom
        CPU Power Management : Maximum Performance
                          C1E : Disabled
                     C States : Autonomous
         Memory Patrol Scrub : Disabled
      Energy Efficiency Policy : Performance
            PCI ASPM L1 Link
             Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-ic2022.1/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
running on localhost Thu Dec  8 10:10:57 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Platinum 8452Y
      2  "physical id"s (chips)
      72 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
     cpu cores : 36
     siblings  : 36
     physical 0: cores 0 1 2 3 4 5 6 7 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
     26 27 28 29 30 31 32 33 34 35
     physical 1: cores 0 1 2 3 4 5 6 7 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
```

(Continued on next page)

Dell Inc.

PowerEdge R760 (Intel Xeon Platinum 8452Y)

SPECspeed®2017_int_base = 11.0

SPECspeed®2017_int_peak = 11.2

CPU2017 License: 6573　　　　　　　　　　　　　　　**Test Date:** Dec-2022
Test Sponsor: Dell Inc.　　　　　　　　　　　　　**Hardware Availability:** Feb-2023
Tested by: Dell Inc.　　　　　　　　　　　　　　**Software Availability:** Jun-2022

Platform Notes (Continued)

```
      26 27 28 29 30 31 32 33 34 35

From lscpu from util-linux 2.37.2:
      Architecture:                 x86_64
      CPU op-mode(s):               32-bit, 64-bit
      Address sizes:                46 bits physical, 57 bits virtual
      Byte Order:                   Little Endian
      CPU(s):                       72
      On-line CPU(s) list:          0-71
      Vendor ID:                    GenuineIntel
      Model name:                   Intel(R) Xeon(R) Platinum 8452Y
      CPU family:                   6
      Model:                        143
      Thread(s) per core:           1
      Core(s) per socket:           36
      Socket(s):                    2
      Stepping:                     8
      BogoMIPS:                     4000.00
      Flags:                        fpu vme de pse tsc msr pae mce cx8 apic sep mtrr
      pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
      pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
      nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl smx
      est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
      tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault
      epb cat_l3 cat_l2 cdp_l3 invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp
      ibrs_enhanced fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a
      avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni
      avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
      cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln
      pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
      avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri
      movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr avx512_fp16
      amx_tile flush_l1d arch_capabilities
      L1d cache:                    3.4 MiB (72 instances)
      L1i cache:                    2.3 MiB (72 instances)
      L2 cache:                     144 MiB (72 instances)
      L3 cache:                     135 MiB (2 instances)
      NUMA node(s):                 4
      NUMA node0 CPU(s):            0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68
      NUMA node1 CPU(s):            2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70
      NUMA node2 CPU(s):            1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69
      NUMA node3 CPU(s):            3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,63,67,71
      Vulnerability Itlb multihit:  Not affected
      Vulnerability L1tf:           Not affected
      Vulnerability Mds:            Not affected
      Vulnerability Meltdown:       Not affected
      Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via
      prctl and seccomp
      Vulnerability Spectre v1:     Mitigation; usercopy/swapgs barriers and __user
      pointer sanitization
      Vulnerability Spectre v2:     Mitigation; Enhanced IBRS, IBPB conditional, RSB
      filling
      Vulnerability Srbds:          Not affected
      Vulnerability Tsx async abort: Not affected

From lscpu --cache:
      NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
      L1d       48K     3.4M   12 Data            1    64        1             64
      L1i       32K     2.3M    8 Instruction     1    64        1             64
      L2         2M     144M   16 Unified         2  2048        1             64
```

(Continued on next page)

Dell Inc.

PowerEdge R760 (Intel Xeon Platinum 8452Y)

SPECspeed®2017_int_base = 11.0

SPECspeed®2017_int_peak = 11.2

CPU2017 License: 6573
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Software Availability: Jun-2022

Platform Notes (Continued)

```
      L3       67.5M     135M   15 Unified        3 73728        1              64


   /proc/cpuinfo cache data
     cache size : 69120 KB

   From numactl --hardware
   WARNING: a numactl 'node' might or might not correspond to a physical chip.
     available: 4 nodes (0-3)
     node 0 cpus: 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60 64 68
     node 0 size: 257495 MB
     node 0 free: 249240 MB
     node 1 cpus: 2 6 10 14 18 22 26 30 34 38 42 46 50 54 58 62 66 70
     node 1 size: 258008 MB
     node 1 free: 257710 MB
     node 2 cpus: 1 5 9 13 17 21 25 29 33 37 41 45 49 53 57 61 65 69
     node 2 size: 258043 MB
     node 2 free: 257671 MB
     node 3 cpus: 3 7 11 15 19 23 27 31 35 39 43 47 51 55 59 63 67 71
     node 3 size: 258014 MB
     node 3 free: 257694 MB
     node distances:
     node   0   1   2   3
       0:  10  12  21  21
       1:  12  10  21  21
       2:  21  21  10  12
       3:  21  21  12  10


   From /proc/meminfo
     MemTotal:       1056319776 kB
     HugePages_Total:        0
     Hugepagesize:        2048 kB

   From /etc/*release* /etc/*version*
     os-release:
       NAME="SLES"
       VERSION="15-SP4"
       VERSION_ID="15.4"
       PRETTY_NAME="SUSE Linux Enterprise Server 15 SP4"
       ID="sles"
       ID_LIKE="suse"
       ANSI_COLOR="0;32"
       CPE_NAME="cpe:/o:suse:sles:15:sp4"

   uname -a:
     Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18
     UTC 2022 (49db222) x86_64 x86_64 x86_64 GNU/Linux

   Kernel self-reported vulnerability status:

   CVE-2018-12207 (iTLB Multihit):              Not affected
   CVE-2018-3620 (L1 Terminal Fault):           Not affected
   Microarchitectural Data Sampling:            Not affected
   CVE-2017-5754 (Meltdown):                    Not affected
   CVE-2018-3639 (Speculative Store Bypass):    Mitigation: Speculative Store
                                                Bypass disabled via prctl and
                                                seccomp
   CVE-2017-5753 (Spectre variant 1):           Mitigation: usercopy/swapgs
                                                barriers and __user pointer
                                                sanitization
   CVE-2017-5715 (Spectre variant 2):           Mitigation: Enhanced IBRS, IBPB:
```

(Continued on next page)

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Platform Notes (Continued)

```
                                                    conditional, RSB filling
    CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
    CVE-2019-11135 (TSX Asynchronous Abort):              Not affected

    run-level 3 Dec 8 10:08

    SPEC is set to: /mnt/ramdisk/cpu2017-1.1.8-ic2022.1
       Filesystem      Type   Size  Used Avail Use% Mounted on
       tmpfs           tmpfs 125G  3.6G  122G   3% /mnt/ramdisk

    From /sys/devices/virtual/dmi/id
        Vendor:         Dell Inc.
        Product:        PowerEdge R760
        Product Family: PowerEdge

    Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you
    interpret this section. The 'dmidecode' program reads system data which is "intended to
    allow hardware to be accurately determined", but the intent may not be met, as there are
    frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
      Memory:
        16x 00AD00B300AD HMCG94MEBRA123N 64 GB 2 rank 4800

    BIOS:
       BIOS Vendor:      Dell Inc.
       BIOS Version:     0.3.1
       BIOS Date:        11/24/2022
       BIOS Revision:    0.3

    (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================================
C         | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
          | 657.xz_s(base, peak)
----------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------------------------


==============================================================================================
C++       | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
          | 641.leela_s(base, peak)
----------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------------------------


==============================================================================================
Fortran   | 648.exchange2_s(base, peak)
----------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
----------------------------------------------------------------------------------------------
```

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CPU2017 License: 6573		**Test Date:**	Dec-2022
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Base Compiler Invocation

C benchmarks:
`icx`

C++ benchmarks:
`icpx`

Fortran benchmarks:
`ifx`

Base Portability Flags

600.perlbench_s: `-DSPEC_LP64 -DSPEC_LINUX_X64`
602.gcc_s: `-DSPEC_LP64`
605.mcf_s: `-DSPEC_LP64`
620.omnetpp_s: `-DSPEC_LP64`
623.xalancbmk_s: `-DSPEC_LP64 -DSPEC_LINUX`
625.x264_s: `-DSPEC_LP64`
631.deepsjeng_s: `-DSPEC_LP64`
641.leela_s: `-DSPEC_LP64`
648.exchange2_s: `-DSPEC_LP64`
657.xz_s: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
`-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto`
`-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp`
`-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

C++ benchmarks:
`-m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto`
`-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`
`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

Fortran benchmarks:
`-m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto`
`-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`
`-nostandard-realloc-lhs -align array32byte`
`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

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SPECspeed®2017_int_base = 11.0

SPECspeed®2017_int_peak = 11.2

Peak Compiler Invocation

C benchmarks:
`icx`

C++ benchmarks:
`icpx`

Fortran benchmarks:
`ifx`

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

600.perlbench_s: `-m64 -std=c11 -Wl,-z,muldefs -fprofile-generate`(pass 1)
`-fprofile-use=default.profdata`(pass 2) `-xCORE-AVX512 -O3`
`-ffast-math -flto -mfpmath=sse -funroll-loops`
`-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP`
`-fno-strict-overflow -L/usr/local/jemalloc64-5.0.1/lib`
`-ljemalloc`

602.gcc_s: `-m64 -std=c11 -Wl,-z,muldefs -fprofile-generate`(pass 1)
`-fprofile-use=default.profdata`(pass 2) `-xCORE-AVX512 -O3`
`-ffast-math -flto -mfpmath=sse -funroll-loops`
`-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP`
`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

605.mcf_s: `basepeak = yes`

625.x264_s: `-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -O3`
`-ffast-math -flto -mfpmath=sse -funroll-loops`
`-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP`
`-fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

657.xz_s: `basepeak = yes`

C++ benchmarks:

(Continued on next page)

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https://www.spec.org/

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Peak Optimization Flags (Continued)

620.omnetpp_s: `basepeak = yes`

623.xalancbmk_s: `basepeak = yes`

631.deepsjeng_s: `basepeak = yes`

641.leela_s: `basepeak = yes`

Fortran benchmarks:

648.exchange2_s: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64-revB.html`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.2.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64-revB.xml`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.2.xml`

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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