SPEC CPU®2017 Floating Point Rate Result

Cisco Systems

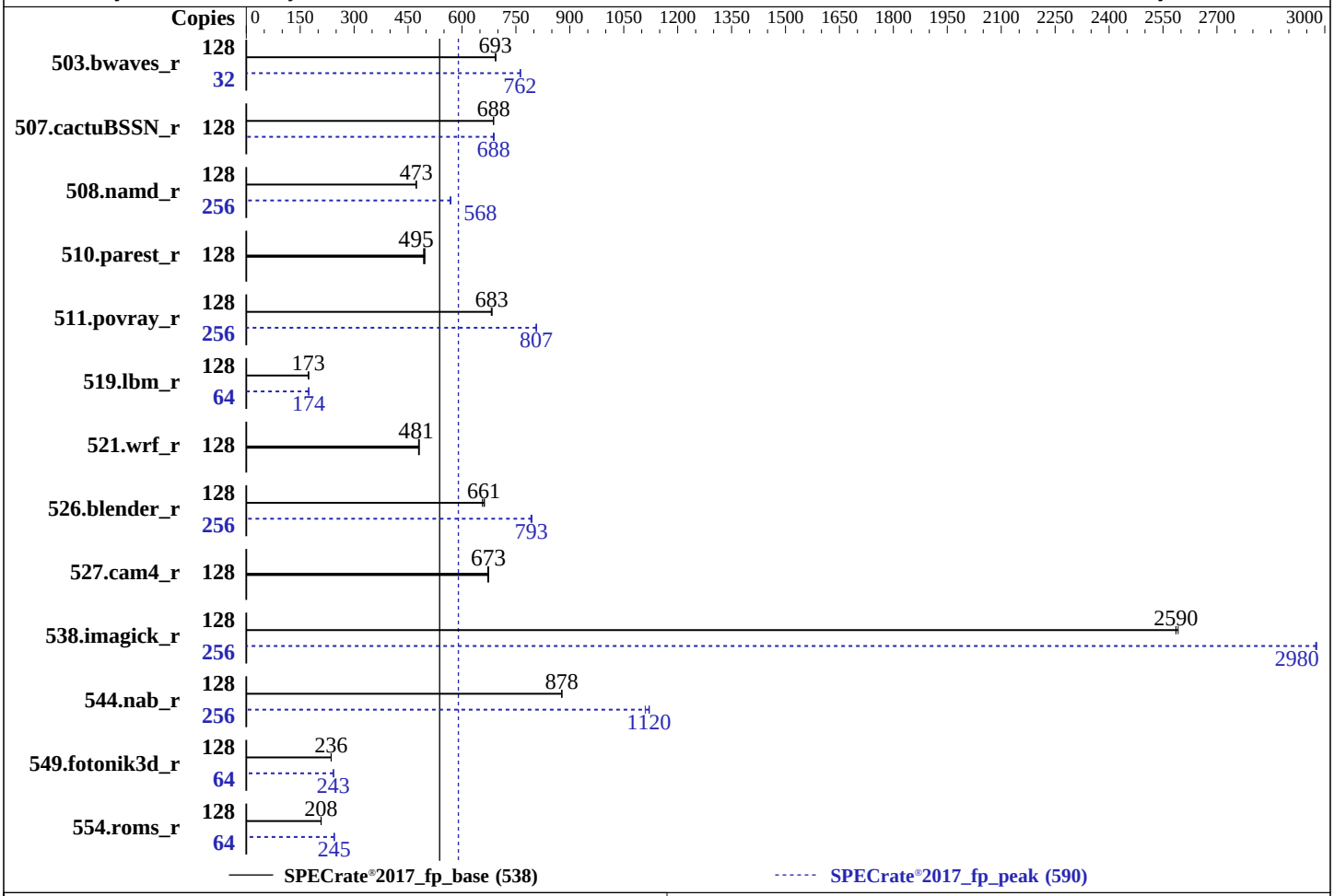
Cisco UCS C225 M6 (AMD EPYC 7662)

SPECrate®2017_fp_base = 538

SPECrate®2017_fp_peak = 590

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Oct-2022
Hardware Availability: Aug-2021
Software Availability: Dec-2021



Hardware

CPU Name:	AMD EPYC 7662
Max MHz:	3300
Nominal:	2000
Enabled:	128 cores, 2 chips, 2 threads/core
Orderable:	1,2 chips
Cache L1:	32 KB I + 32 KB D on chip per core
L2:	512 KB I+D on chip per core
L3:	256 MB I+D on chip per chip, 16 MB shared / 4 cores
Other:	None
Memory:	2 TB (16 x 128 GB 4Rx4 PC4-3200AA-L)
Storage:	1 x 960 GB M.2 SSD SATA
Other:	None

Software

OS:	SUSE Linux Enterprise Server 15 SP2 (x86_64) kernel version 5.3.18-22-default
Compiler:	C/C++/Fortran: Version 3.2.0 of AOCC
Parallel:	No
Firmware:	Version 4.2.2b released May-2022
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	jemalloc: jemalloc memory allocator library v5.1.0
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	128	1849	694	1852	693	**1852**	**693**	32	421	762	**421**	**762**	420	763
507.cactuBSSN_r	128	**236**	**688**	236	688	236	688	128	235	689	**235**	**688**	236	687
508.namd_r	128	**257**	**473**	257	473	257	473	256	429	568	**428**	**568**	427	569
510.parest_r	128	674	497	**676**	**495**	679	493	128	674	497	**676**	**495**	679	493
511.povray_r	128	437	683	439	681	**438**	**683**	256	741	807	**741**	**807**	741	806
519.lbm_r	128	779	173	779	173	**779**	**173**	64	389	174	388	174	**388**	**174**
521.wrf_r	128	**596**	**481**	598	479	596	481	128	**596**	**481**	598	479	596	481
526.blender_r	128	**295**	**661**	297	657	294	663	256	**491**	**793**	491	794	492	792
527.cam4_r	128	332	674	333	672	**332**	**673**	128	332	674	333	672	**332**	**673**
538.imagick_r	128	123	2590	123	2590	**123**	**2590**	256	**214**	**2980**	214	2980	214	2970
544.nab_r	128	245	878	**245**	**878**	246	877	256	384	1120	388	1110	**385**	**1120**
549.fotonik3d_r	128	2112	236	**2113**	**236**	2113	236	64	**1028**	**243**	1029	242	1028	243
554.roms_r	128	978	208	**977**	**208**	977	208	64	**416**	**245**	416	244	415	245

SPECrate®2017_fp_base = 538

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
```

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Operating System Notes (Continued)

```
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/home/cpu2017/amd_rate_aocc320_milanx_A_lib/lib;/home/cpu2017/amd_rate_
     aocc320_milanx_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using OpenSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)
jemalloc 5.1.0 is available here:
https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2
```

Platform Notes

```
BIOS Configuration
 SMT Mode set to Enabled
 NUMA nodes per socket set to NPS4
 ACPI SRAT L3 Cache As NUMA Domain set to Enabled
 DRAM Scrub Time set to Disabled
 Determinism Slider set to Power
 Memory Interleaving set to Disabledd
 APBDIS set to 1
```

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Platform Notes (Continued)

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
running on localhost Mon Oct  3 07:18:21 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : AMD EPYC 7662 64-Core Processor
      2  "physical id"s (chips)
      256 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 64
      siblings  : 128
      physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
      25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
      53 54 55 56 57 58 59 60 61 62 63
      physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
      25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
      53 54 55 56 57 58 59 60 61 62 63

From lscpu from util-linux 2.33.1:
     Architecture:        x86_64
     CPU op-mode(s):      32-bit, 64-bit
     Byte Order:          Little Endian
     Address sizes:       43 bits physical, 48 bits virtual
     CPU(s):              256
     On-line CPU(s) list: 0-255
     Thread(s) per core:  2
     Core(s) per socket:  64
     Socket(s):           2
     NUMA node(s):        32
     Vendor ID:           AuthenticAMD
     CPU family:          23
     Model:               49
     Model name:          AMD EPYC 7662 64-Core Processor
     Stepping:            0
     CPU MHz:             1719.588
     CPU max MHz:         2000.0000
     CPU min MHz:         1500.0000
     BogoMIPS:            3992.74
     Virtualization:      AMD-V
     L1d cache:           32K
     L1i cache:           32K
     L2 cache:            512K
```

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Platform Notes (Continued)

```
      L3 cache:              16384K
      NUMA node0 CPU(s):     0-3,128-131
      NUMA node1 CPU(s):     4-7,132-135
      NUMA node2 CPU(s):     8-11,136-139
      NUMA node3 CPU(s):     12-15,140-143
      NUMA node4 CPU(s):     16-19,144-147
      NUMA node5 CPU(s):     20-23,148-151
      NUMA node6 CPU(s):     24-27,152-155
      NUMA node7 CPU(s):     28-31,156-159
      NUMA node8 CPU(s):     32-35,160-163
      NUMA node9 CPU(s):     36-39,164-167
      NUMA node10 CPU(s):    40-43,168-171
      NUMA node11 CPU(s):    44-47,172-175
      NUMA node12 CPU(s):    48-51,176-179
      NUMA node13 CPU(s):    52-55,180-183
      NUMA node14 CPU(s):    56-59,184-187
      NUMA node15 CPU(s):    60-63,188-191
      NUMA node16 CPU(s):    64-67,192-195
      NUMA node17 CPU(s):    68-71,196-199
      NUMA node18 CPU(s):    72-75,200-203
      NUMA node19 CPU(s):    76-79,204-207
      NUMA node20 CPU(s):    80-83,208-211
      NUMA node21 CPU(s):    84-87,212-215
      NUMA node22 CPU(s):    88-91,216-219
      NUMA node23 CPU(s):    92-95,220-223
      NUMA node24 CPU(s):    96-99,224-227
      NUMA node25 CPU(s):    100-103,228-231
      NUMA node26 CPU(s):    104-107,232-235
      NUMA node27 CPU(s):    108-111,236-239
      NUMA node28 CPU(s):    112-115,240-243
      NUMA node29 CPU(s):    116-119,244-247
      NUMA node30 CPU(s):    120-123,248-251
      NUMA node31 CPU(s):    124-127,252-255
      Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
      pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
      constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf pni pclmulqdq
      monitor ssse3 fma cx16 sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand
      lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw
      ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
      cat_l3 cdp_l3 hw_pstate sme ssbd mba sev ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2
      smep bmi2 cqm rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1
      xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr
      wbnoinvd arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid
      decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif umip rdpid
      overflow_recov succor smca

  /proc/cpuinfo cache data
```


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Platform Notes (Continued)

```
    cache size : 512 KB

 From numactl --hardware
 WARNING: a numactl 'node' might or might not correspond to a physical chip.
   available: 32 nodes (0-31)
   node 0 cpus: 0 1 2 3 128 129 130 131
   node 0 size: 64324 MB
   node 0 free: 63875 MB
   node 1 cpus: 4 5 6 7 132 133 134 135
   node 1 size: 64509 MB
   node 1 free: 64165 MB
   node 2 cpus: 8 9 10 11 136 137 138 139
   node 2 size: 64509 MB
   node 2 free: 64165 MB
   node 3 cpus: 12 13 14 15 140 141 142 143
   node 3 size: 64507 MB
   node 3 free: 64170 MB
   node 4 cpus: 16 17 18 19 144 145 146 147
   node 4 size: 64509 MB
   node 4 free: 64116 MB
   node 5 cpus: 20 21 22 23 148 149 150 151
   node 5 size: 64476 MB
   node 5 free: 64118 MB
   node 6 cpus: 24 25 26 27 152 153 154 155
   node 6 size: 64509 MB
   node 6 free: 64141 MB
   node 7 cpus: 28 29 30 31 156 157 158 159
   node 7 size: 64507 MB
   node 7 free: 64157 MB
   node 8 cpus: 32 33 34 35 160 161 162 163
   node 8 size: 64509 MB
   node 8 free: 64169 MB
   node 9 cpus: 36 37 38 39 164 165 166 167
   node 9 size: 64509 MB
   node 9 free: 64175 MB
   node 10 cpus: 40 41 42 43 168 169 170 171
   node 10 size: 64509 MB
   node 10 free: 64173 MB
   node 11 cpus: 44 45 46 47 172 173 174 175
   node 11 size: 64507 MB
   node 11 free: 64155 MB
   node 12 cpus: 48 49 50 51 176 177 178 179
   node 12 size: 64509 MB
   node 12 free: 64168 MB
   node 13 cpus: 52 53 54 55 180 181 182 183
   node 13 size: 64509 MB
   node 13 free: 64170 MB
```

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Platform Notes (Continued)

```
node 14 cpus: 56 57 58 59 184 185 186 187
node 14 size: 64509 MB
node 14 free: 64176 MB
node 15 cpus: 60 61 62 63 188 189 190 191
node 15 size: 64495 MB
node 15 free: 64159 MB
node 16 cpus: 64 65 66 67 192 193 194 195
node 16 size: 64509 MB
node 16 free: 64167 MB
node 17 cpus: 68 69 70 71 196 197 198 199
node 17 size: 64509 MB
node 17 free: 64165 MB
node 18 cpus: 72 73 74 75 200 201 202 203
node 18 size: 64509 MB
node 18 free: 64167 MB
node 19 cpus: 76 77 78 79 204 205 206 207
node 19 size: 64507 MB
node 19 free: 64138 MB
node 20 cpus: 80 81 82 83 208 209 210 211
node 20 size: 64509 MB
node 20 free: 64165 MB
node 21 cpus: 84 85 86 87 212 213 214 215
node 21 size: 64509 MB
node 21 free: 64173 MB
node 22 cpus: 88 89 90 91 216 217 218 219
node 22 size: 64509 MB
node 22 free: 64176 MB
node 23 cpus: 92 93 94 95 220 221 222 223
node 23 size: 64507 MB
node 23 free: 64169 MB
node 24 cpus: 96 97 98 99 224 225 226 227
node 24 size: 64509 MB
node 24 free: 64176 MB
node 25 cpus: 100 101 102 103 228 229 230 231
node 25 size: 64509 MB
node 25 free: 64173 MB
node 26 cpus: 104 105 106 107 232 233 234 235
node 26 size: 64509 MB
node 26 free: 64167 MB
node 27 cpus: 108 109 110 111 236 237 238 239
node 27 size: 64507 MB
node 27 free: 64161 MB
node 28 cpus: 112 113 114 115 240 241 242 243
node 28 size: 64509 MB
node 28 free: 64175 MB
node 29 cpus: 116 117 118 119 244 245 246 247
node 29 size: 64509 MB
```

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Platform Notes (Continued)

```
node 29 free: 64166 MB
node 30 cpus: 120 121 122 123 248 249 250 251
node 30 size: 64509 MB
node 30 free: 64168 MB
node 31 cpus: 124 125 126 127 252 253 254 255
node 31 size: 64506 MB
node 31 free: 64171 MB
node distances:
node   0   1   2   3   4   5   6   7   8   9  10  11  12  13  14  15  16  17  18  19
20  21  22  23  24  25  26  27  28  29  30  31
  0:  10  11  11  11  12  12  12  12  12  12  12  12  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  1:  11  10  11  11  12  12  12  12  12  12  12  12  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  2:  11  11  10  11  12  12  12  12  12  12  12  12  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  3:  11  11  11  10  12  12  12  12  12  12  12  12  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  4:  12  12  12  12  10  11  11  11  12  12  12  12  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  5:  12  12  12  12  11  10  11  11  12  12  12  12  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  6:  12  12  12  12  11  11  10  11  12  12  12  12  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  7:  12  12  12  12  11  11  11  10  12  12  12  12  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  8:  12  12  12  12  12  12  12  12  10  11  11  11  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  9:  12  12  12  12  12  12  12  12  11  10  11  11  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
 10:  12  12  12  12  12  12  12  12  11  11  10  11  12  12  12  12  32  32  32  32
 32  32  32  32  32  32  32  32  32  32  32  32
 11:  12  12  12  12  12  12  12  12  11  11  11  10  12  12  12  12  32  32  32  32
 32  32  32  32  32  32  32  32  32  32  32  32
 12:  12  12  12  12  12  12  12  12  12  12  12  12  10  11  11  11  32  32  32  32
 32  32  32  32  32  32  32  32  32  32  32  32
 13:  12  12  12  12  12  12  12  12  12  12  12  12  11  10  11  11  32  32  32  32
 32  32  32  32  32  32  32  32  32  32  32  32
 14:  12  12  12  12  12  12  12  12  12  12  12  12  11  11  10  11  32  32  32  32
 32  32  32  32  32  32  32  32  32  32  32  32
 15:  12  12  12  12  12  12  12  12  12  12  12  12  11  11  11  10  32  32  32  32
 32  32  32  32  32  32  32  32  32  32  32  32
 16:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  10  11  11  11
 12  12  12  12  12  12  12  12  12  12  12  12
 17:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  11  10  11  11
 12  12  12  12  12  12  12  12  12  12  12  12
 18:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  11  11  10  11
```

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Platform Notes (Continued)

```
    12  12  12  12  12  12  12  12  12  12  12  12
    19:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  11  11  11  10
    12  12  12  12  12  12  12  12  12  12  12  12
    20:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    10  11  11  11  12  12  12  12  12  12  12  12
    21:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    11  10  11  11  12  12  12  12  12  12  12  12
    22:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    11  11  10  11  12  12  12  12  12  12  12  12
    23:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    11  11  11  10  12  12  12  12  12  12  12  12
    24:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    12  12  12  12  10  11  11  11  12  12  12  12
    25:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    12  12  12  12  11  10  11  11  12  12  12  12
    26:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    12  12  12  12  11  11  10  11  12  12  12  12
    27:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    12  12  12  12  11  11  11  10  12  12  12  12
    28:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    12  12  12  12  12  12  12  12  10  11  11  11
    29:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    12  12  12  12  12  12  12  12  11  10  11  11
    30:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    12  12  12  12  12  12  12  12  11  11  10  11
    31:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    12  12  12  12  12  12  12  12  11  11  11  10

From /proc/meminfo
   MemTotal:       2113603944 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
   performance

From /etc/*release* /etc/*version*
   os-release:
      NAME="SLES"
      VERSION="15-SP2"
      VERSION_ID="15.2"
      PRETTY_NAME="SUSE Linux Enterprise Server 15 SP2"
      ID="sles"
      ID_LIKE="suse"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:15:sp2"
```


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Platform Notes (Continued)

```
uname -a:
   Linux localhost 5.3.18-22-default #1 SMP Wed Jun 3 12:16:43 UTC 2020 (720aeba) x86_64
   x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):                          Not affected
CVE-2018-3620 (L1 Terminal Fault):                       Not affected
Microarchitectural Data Sampling:                        Not affected
CVE-2017-5754 (Meltdown):                                Not affected
CVE-2018-3639 (Speculative Store Bypass):                Mitigation: Speculative Store
                                                         Bypass disabled via prctl and
                                                         seccomp
CVE-2017-5753 (Spectre variant 1):                       Mitigation: usercopy/swapgs
                                                         barriers and __user pointer
                                                         sanitization
CVE-2017-5715 (Spectre variant 2):                       Mitigation: Full AMD retpoline,
                                                       IBPB: conditional, IBRS_FW, STIBP:
                                                         conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort):                 Not affected

run-level 3 Apr 17 06:13

SPEC is set to: /home/cpu2017
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sdb2      xfs   223G   11G  213G   5% /

From /sys/devices/virtual/dmi/id
    Vendor:         Cisco Systems Inc
    Product:        UCSC-C225-M6S
    Serial:         WZP252408JE

Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you
interpret this section. The 'dmidecode' program reads system data which is "intended to
allow hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  Memory:
    16x 0xCE00 M386AAG40AM3-CWE 128 GB 4 rank 3200
    16x Unknown Unknown

BIOS:
   BIOS Vendor:       Cisco Systems, Inc.
   BIOS Version:      C225M6.4.2.2b.0.0509222122
   BIOS Date:         05/09/2022
   BIOS Revision:     5.14
```

(Continued on next page)

Platform Notes (Continued)

```
 (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C               | 519.lbm_r(base, peak) 538.imagick_r(base, peak)
                | 544.nab_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------

==============================================================================
C++             | 508.namd_r(base, peak) 510.parest_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------

==============================================================================
C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------

==============================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
```


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Compiler Version Notes (Continued)

```
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------

==============================================================================
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak)
                | 554.roms_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------

==============================================================================
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
clang

(Continued on next page)

Base Compiler Invocation (Continued)

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

Benchmarks using both C and C++:
clang++ clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -D__BOOL_DEFINED -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization

(Continued on next page)

Base Optimization Flags (Continued)

C benchmarks (continued):

```
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -z muldefs -lamdlibm -ljemalloc -lflang
```

C++ benchmarks:

```
-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -flto
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -extra-vectorizer-passes -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -convert-pow-exp-to-int=false
-mllvm -enable-loop-fusion -z muldefs -lamdlibm -ljemalloc -lflang
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -Hz,1,0x1 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -Kieee -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-loop-fusion
-mllvm -enable-loopinterchange -mllvm -compute-interchange-order
-z muldefs -lamdlibm -ljemalloc -lflang
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
```

(Continued on next page)

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -Hz,1,0x1 -Kieee -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-loopinterchange -mllvm -compute-interchange-order
-z muldefs -lamdlibm -ljemalloc -lflang
```

Benchmarks using both C and C++:

```
-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -flto
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -mllvm -extra-vectorizer-passes
-mllvm -convert-pow-exp-to-int=false -z muldefs -lamdlibm -ljemalloc
-lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -flto
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -mllvm -extra-vectorizer-passes
-mllvm -convert-pow-exp-to-int=false -Hz,1,0x1 -Kieee -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops -mllvm -lsr-in-nested-loop
-mllvm -enable-loopinterchange -mllvm -compute-interchange-order
```

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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):
`-z muldefs -lamdlibm -ljemalloc -lflang`

Base Other Flags

C benchmarks:
`-Wno-unused-command-line-argument`

C++ benchmarks:
`-Wno-unused-command-line-argument`

Fortran benchmarks:
`-Wno-unused-command-line-argument`

Benchmarks using both Fortran and C:
`-Wno-unused-command-line-argument`

Benchmarks using both C and C++:
`-Wno-unused-command-line-argument`

Benchmarks using Fortran, C, and C++:
`-Wno-unused-command-line-argument`

Peak Compiler Invocation

C benchmarks:
`clang`

C++ benchmarks:
`clang++`

Fortran benchmarks:
`flang`

Benchmarks using both Fortran and C:
`flang clang`

Benchmarks using both C and C++:
`clang++ clang`

Benchmarks using Fortran, C, and C++:
`clang++ clang flang`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: -m64 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -lamdlibm -ljemalloc

538.imagick_r: Same as 519.lbm_r

544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize -Ofast -march=znver3
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays
-flv-function-specialization -mllvm -inline-threshold=1000
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -lamdlibm -ljemalloc

C++ benchmarks:

508.namd_r: -m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-flv-function-specialization -mllvm -enable-licm-vrp
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -lamdlibm -ljemalloc

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Peak Optimization Flags (Continued)

510.parest_r: basepeak = yes

Fortran benchmarks:

503.bwaves_r: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -Mrecursive
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp
-lamdlibm -ljemalloc -lflang

549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -Kieee
-Mrecursive -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp
-lamdlibm -ljemalloc -lflang

554.roms_r: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -Mrecursive
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp
-Hz,1,0x1 -mllvm -fuse-tile-inner-loop -lamdlibm
-ljemalloc -lflang

Benchmarks using both Fortran and C:

521.wrf_r: basepeak = yes

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:
-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-enable-licm-vrp
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

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Peak Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver3
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true -mllvm -function-specialize
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-finline-aggressive -mllvm -unroll-threshold=100 -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -lamdlibm -ljemalloc

Benchmarks using Fortran, C, and C++:
-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-enable-licm-vrp
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver3
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true -mllvm -function-specialize
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-finline-aggressive -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -mllvm -extra-vectorizer-passes
-mllvm -convert-pow-exp-to-int=false -Mrecursive -lamdlibm -ljemalloc
-lflang

Peak Other Flags

C benchmarks:
-Wno-unused-command-line-argument

C++ benchmarks:
-Wno-unused-command-line-argument

Fortran benchmarks:
-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:
-Wno-unused-command-line-argument

Benchmarks using both C and C++:
-Wno-unused-command-line-argument

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Peak Other Flags (Continued)

Benchmarks using Fortran, C, and C++:
`-Wno-unused-command-line-argument`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/aocc320-flags-A1.html`
`http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-v2-revD.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/aocc320-flags-A1.xml`
`http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-v2-revD.xml`

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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