



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## Inspur Corporation

SPECrate®2017\_int\_base = 639

### Inspur NF8260M6 (Intel Xeon Gold 6348H)

SPECrate®2017\_int\_peak = 663

CPU2017 License: 3358

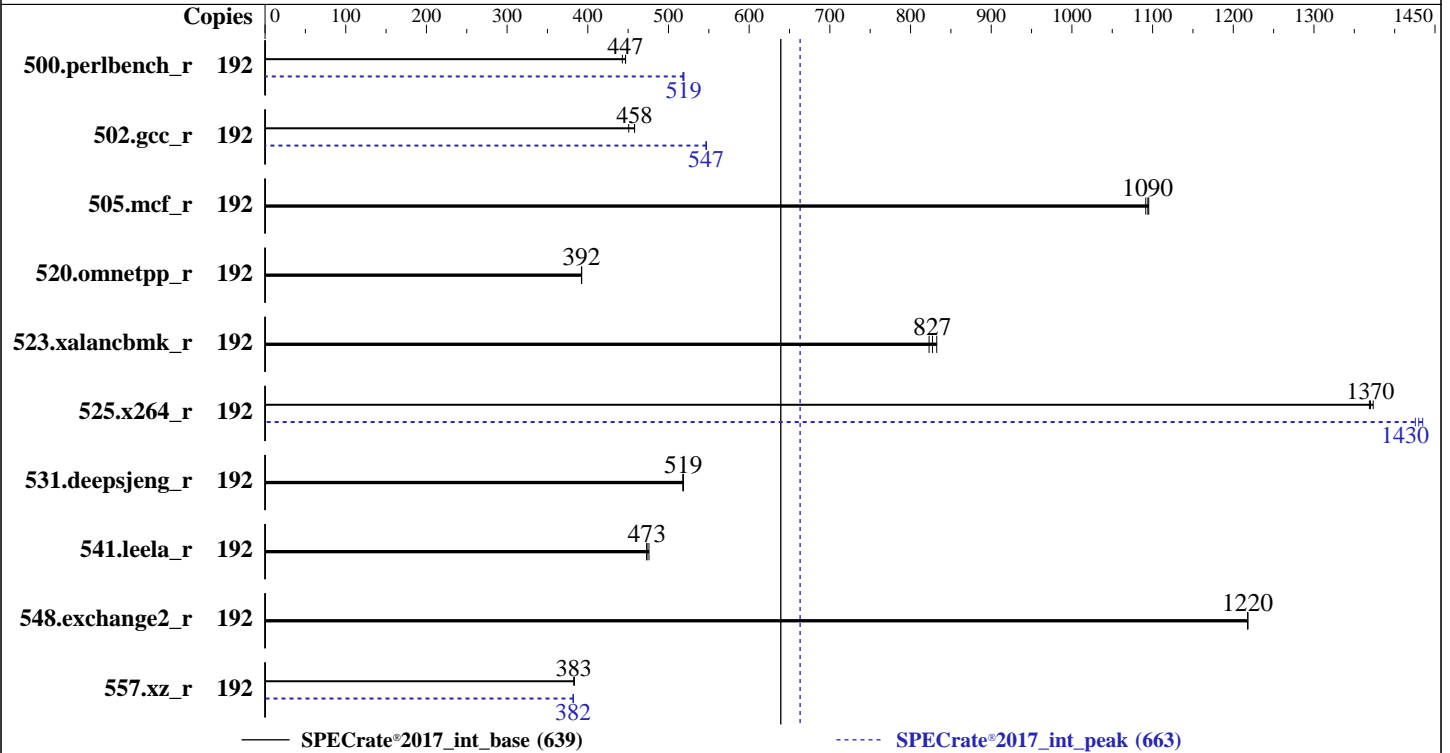
Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Jun-2022

Hardware Availability: Sep-2020

Software Availability: Sep-2021



### Hardware

CPU Name: Intel Xeon Gold 6348H  
 Max MHz: 4200  
 Nominal: 2300  
 Enabled: 96 cores, 4 chips, 2 threads/core  
 Orderable: 2,4 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 33 MB I+D on chip per chip  
 Other: None  
 Memory: 1536 GB (48 x 32 GB 2Rx8 PC4-3200AA-R, running at 2933)  
 Storage: 1 x 2 TB NVME SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux release 8.3 (Ootpa) 4.18.0-240.el8.x86\_64  
 Compiler: C/C++: Version 2021.4.0 of Intel oneAPI DPC++/C++ Compiler Build 20210924 for Linux;  
 Fortran: Version 2021.4.0 of Intel Fortran Compiler Classic Build 20210910 for Linux;  
 C/C++: Version 2021.4.0 of Intel C/C++ Compiler Classic Build 20210910 for Linux;  
 Parallel: No  
 Firmware: Version 04.10.06 released Apr-2021  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	192	<b>684</b>	<b>447</b>	690	443	684	447	192	<b>589</b>	<b>519</b>	590	518	589	519
502.gcc_r	192	<b>594</b>	<b>458</b>	594	458	603	451	192	497	547	<b>497</b>	<b>547</b>	497	547
505.mcf_r	192	284	1090	<b>284</b>	<b>1090</b>	283	1100	192	284	1090	<b>284</b>	<b>1090</b>	283	1100
520.omnetpp_r	192	642	392	642	392	<b>642</b>	<b>392</b>	192	642	392	642	392	<b>642</b>	<b>392</b>
523.xalancbmk_r	192	<b>245</b>	<b>827</b>	246	823	244	832	192	<b>245</b>	<b>827</b>	246	823	244	832
525.x264_r	192	246	1370	<b>245</b>	<b>1370</b>	245	1370	192	<b>235</b>	<b>1430</b>	236	1430	234	1430
531.deepsjeng_r	192	425	518	424	519	<b>424</b>	<b>519</b>	192	425	518	424	519	<b>424</b>	<b>519</b>
541.leela_r	192	668	476	<b>672</b>	<b>473</b>	672	473	192	668	476	<b>672</b>	<b>473</b>	672	473
548.exchange2_r	192	<b>413</b>	<b>1220</b>	413	1220	413	1220	192	<b>413</b>	<b>1220</b>	413	1220	413	1220
557.xz_r	192	<b>542</b>	<b>383</b>	542	383	541	383	192	<b>543</b>	<b>382</b>	543	382	542	382

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH =

"/home/CPU2017/lib/intel64:/home/CPU2017/lib/ia32:/home/CPU2017/je5.0.1-32"

MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop\_caches

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### General Notes (Continued)

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5; sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

### Platform Notes

BIOS and OS configuration:

ENERGY\_PERF\_BIAS\_CFG mode set to Performance

Hardware Prefetch set to Disable

VT Support set to Disable

Sub NUMA Cluster (SNC) set to Enable

Scaling\_Governor set to Performance

Sysinfo program /home/CPU2017/bin/sysinfo

Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d

running on localhost.localdomain Mon Jun 27 10:38:04 2022

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6348H CPU @ 2.30GHz

4 "physical id"s (chips)

192 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 24

siblings : 48

physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

From lscpu from util-linux 2.32.1:

Architecture: x86\_64

CPU op-mode(s): 32-bit, 64-bit

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### Platform Notes (Continued)

```

Byte Order:                Little Endian
CPU(s):                    192
On-line CPU(s) list:      0-191
Thread(s) per core:       2
Core(s) per socket:       24
Socket(s):                 4
NUMA node(s):             8
Vendor ID:                 GenuineIntel
CPU family:               6
Model:                    85
Model name:               Intel(R) Xeon(R) Gold 6348H CPU @ 2.30GHz
Stepping:                 11
CPU MHz:                  3100.047
CPU max MHz:              4200.0000
CPU min MHz:              1000.0000
BogoMIPS:                 4600.00
Virtualization:          VT-x
L1d cache:               32K
L1i cache:               32K
L2 cache:                1024K
L3 cache:                33792K
NUMA node0 CPU(s):       0-2,6-8,12-14,18-20,96-98,102-104,108-110,114-116
NUMA node1 CPU(s):       3-5,9-11,15-17,21-23,99-101,105-107,111-113,117-119
NUMA node2 CPU(s):       24-26,30-32,36-38,42-44,120-122,126-128,132-134,138-140
NUMA node3 CPU(s):       27-29,33-35,39-41,45-47,123-125,129-131,135-137,141-143
NUMA node4 CPU(s):       48-50,54-56,60-62,66-68,144-146,150-152,156-158,162-164
NUMA node5 CPU(s):       51-53,57-59,63-65,69-71,147-149,153-155,159-161,165-167
NUMA node6 CPU(s):       72-74,78-80,84-86,90-92,168-170,174-176,180-182,186-188
NUMA node7 CPU(s):       75-77,81-83,87-89,93-95,171-173,177-179,183-185,189-191
Flags:                    fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single
intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle avx2
smep bmi2 erms invpcid cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt
clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc
cqm_occup_llc cqm_mbm_total cqm_mbm_local avx512_bf16 dtherm ida arat pln pts pku
ospke avx512_vnni md_clear flush_l1d arch_capabilities

```

```
/proc/cpuinfo cache data
cache size : 33792 KB
```

```
From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 8 nodes (0-7)
```

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### Platform Notes (Continued)

```

node 0 cpus: 0 1 2 6 7 8 12 13 14 18 19 20 96 97 98 102 103 104 108 109 110 114 115 116
node 0 size: 185523 MB
node 0 free: 191841 MB
node 1 cpus: 3 4 5 9 10 11 15 16 17 21 22 23 99 100 101 105 106 107 111 112 113 117 118
119
node 1 size: 187486 MB
node 1 free: 193258 MB
node 2 cpus: 24 25 26 30 31 32 36 37 38 42 43 44 120 121 122 126 127 128 132 133 134
138 139 140
node 2 size: 186452 MB
node 2 free: 193283 MB
node 3 cpus: 27 28 29 33 34 35 39 40 41 45 46 47 123 124 125 129 130 131 135 136 137
141 142 143
node 3 size: 187402 MB
node 3 free: 193370 MB
node 4 cpus: 48 49 50 54 55 56 60 61 62 66 67 68 144 145 146 150 151 152 156 157 158
162 163 164
node 4 size: 187846 MB
node 4 free: 193375 MB
node 5 cpus: 51 52 53 57 58 59 63 64 65 69 70 71 147 148 149 153 154 155 159 160 161
165 166 167
node 5 size: 187548 MB
node 5 free: 193355 MB
node 6 cpus: 72 73 74 78 79 80 84 85 86 90 91 92 168 169 170 174 175 176 180 181 182
186 187 188
node 6 size: 188390 MB
node 6 free: 193277 MB
node 7 cpus: 75 76 77 81 82 83 87 88 89 93 94 95 171 172 173 177 178 179 183 184 185
189 190 191
node 7 size: 188245 MB
node 7 free: 193280 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10 11 20 20 20 20 20 20
1:  11 10 20 20 20 20 20 20
2:  20 20 10 11 20 20 20 20
3:  20 20 11 10 20 20 20 20
4:  20 20 20 20 10 11 20 20
5:  20 20 20 20 11 10 20 20
6:  20 20 20 20 20 20 10 11
7:  20 20 20 20 20 20 11 10

```

```

From /proc/meminfo
MemTotal:      1583858300 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

(Continued on next page)



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### Platform Notes (Continued)

```

/sbin/tuned-adm active
  Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
performance

From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux"
  VERSION="8.3 (Ootpa)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="8.3"
  PLATFORM_ID="platform:el8"
  PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
  ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):           Not affected
CVE-2018-3620 (L1 Terminal Fault):       Not affected
Microarchitectural Data Sampling:       Not affected
CVE-2017-5754 (Meltdown):               Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store
Bypass disabled via prctl and
seccomp
CVE-2017-5753 (Spectre variant 1):       Mitigation: usercopy/swapgs
barriers and __user pointer
sanitization
CVE-2017-5715 (Spectre variant 2):       Mitigation: Enhanced IBRS, IBPB:
conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Jun 27 10:18

SPEC is set to: /home/CPU2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel00-home xfs   1.7T  270G  1.5T  16% /home

```

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### Platform Notes (Continued)

```
From /sys/devices/virtual/dmi/id
Vendor:      Inspur
Product:     NF8260M6
Product Family: Family
Serial:      380152314
```

Additional information from dmidecode 3.2 follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
48x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200, configured at 2933

BIOS:  
BIOS Vendor: American Megatrends Inc.  
BIOS Version: 04.10.06  
BIOS Date: 04/22/2021  
BIOS Revision: 5.19

(End of data from sysinfo program)

### Compiler Version Notes

```
=====
C      | 500.perlbench_r(peak) 557.xz_r(peak)
-----
```

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.4.0 Build 20210910\_000000  
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

```
=====
C      | 502.gcc_r(peak)
-----
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.4.0 Build 20210924  
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

```
=====
C      | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
      | 525.x264_r(base, peak) 557.xz_r(base)
-----
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.4.0 Build 20210924  
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## Compiler Version Notes (Continued)

=====  
C | 500.perlbench\_r(peak) 557.xz\_r(peak)  
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=====  
C | 502.gcc\_r(peak)  
=====

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=====  
C | 500.perlbench\_r(base) 502.gcc\_r(base) 505.mcf\_r(base, peak)  
| 525.x264\_r(base, peak) 557.xz\_r(base)  
=====

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=====  
C | 500.perlbench\_r(base) 502.gcc\_r(base) 505.mcf\_r(base, peak)  
| 525.x264\_r(base, peak) 557.xz\_r(base)  
=====

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### Compiler Version Notes (Continued)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.4.0 Build 20210924  
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

```
=====  
C++      | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak)  
         | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)  
=====
```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.4.0 Build 20210924  
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

```
=====  
Fortran | 548.exchange2_r(base, peak)  
=====
```

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.4.0 Build 20210910\_000000  
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

### Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifort

### Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc_r: -DSPEC_LP64  
505.mcf_r: -DSPEC_LP64  
520.omnetpp_r: -DSPEC_LP64  
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
525.x264_r: -DSPEC_LP64  
531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64
```

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## Base Portability Flags (Continued)

548.exchange2\_r: -DSPEC\_LP64

557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/intel/compiler/2021.4.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

C++ benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/intel/compiler/2021.4.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-auto -mbranches-within-32B-boundaries
-L/usr/local/intel/compiler/2021.4.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icx

500.perlbench\_r: icc

557.xz\_r: icc

C++ benchmarks:

icpx

Fortran benchmarks:

ifort



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Software Availability: Sep-2021

## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/intel/compiler/2021.4.0/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/usr/local/intel/compiler/2021.4.0/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -flto
-O3 -ffast-math -qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/intel/compiler/2021.4.0/linux/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/intel/compiler/2021.4.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

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Inspur Corporation

SPECrate®2017\_int\_base = 639

Inspur NF8260M6 (Intel Xeon Gold 6348H)

SPECrate®2017\_int\_peak = 663

CPU2017 License: 3358

Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Jun-2022

Hardware Availability: Sep-2020

Software Availability: Sep-2021

## Peak Optimization Flags (Continued)

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html)

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.5.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml)

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.5.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

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