



# SPEC CPU®2017 Integer Speed Result

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## Supermicro

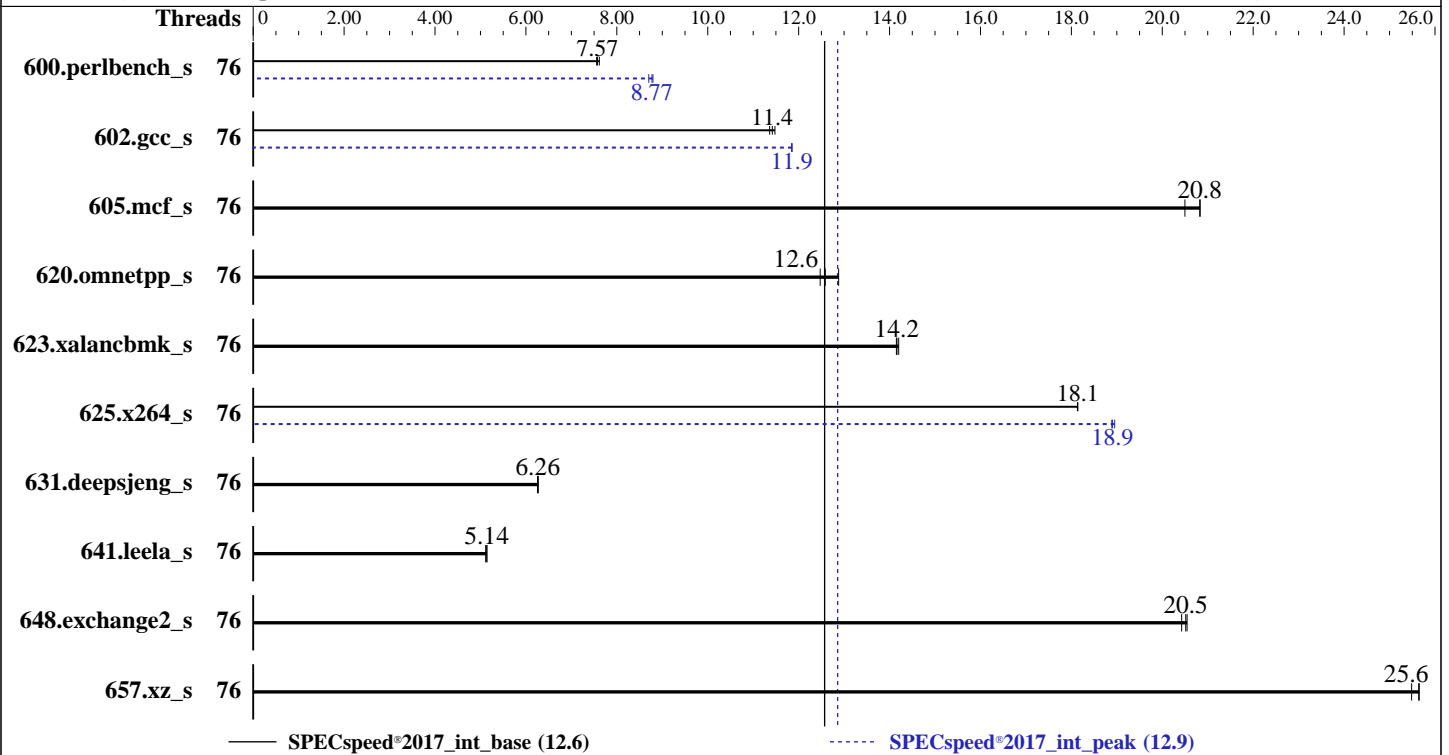
SuperServer SYS-620U-TNR  
(X12DPU-6 , Intel Xeon Platinum 8368Q)

SPECspeed®2017\_int\_base = 12.6

SPECspeed®2017\_int\_peak = 12.9

CPU2017 License: 001176  
Test Sponsor: Supermicro  
Tested by: Supermicro

Test Date: May-2021  
Hardware Availability: Apr-2021  
Software Availability: Dec-2020



### Hardware

CPU Name: Intel Xeon Platinum 8368Q  
Max MHz: 3700  
Nominal: 2600  
Enabled: 76 cores, 2 chips  
Orderable: 1,2 Chips  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 1.25 MB I+D on chip per core  
L3: 57 MB I+D on chip per chip  
Other: None  
Memory: 1 TB  
(32 x 32 GB 2Rx4 PC4-3200AA-R)  
Storage: 1 x 800 GB SATA III SSD  
Other: None

### Software

OS: Red Hat Enterprise Linux release 8.3 (Ootpa)  
4.18.0-240.el8.x86\_64  
Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++  
Compiler Build 20201113 for Linux;  
Fortran: Version 2021.1 of Intel Fortran Compiler  
Classic Build 20201112 for Linux;  
C/C++: Version 2021.1 of Intel C/C++ Compiler  
Classic Build 20201112 for Linux  
Parallel: Yes  
Firmware: Version 1.1 released Apr-2021  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS and OS set to prefer performance at  
the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	76	<b>234</b>	<b>7.57</b>	235	7.56	233	7.62	76	<b>202</b>	<b>8.77</b>	204	8.70	202	8.79
602.gcc_s	76	347	11.5	<b>349</b>	<b>11.4</b>	350	11.4	76	<b>336</b>	<b>11.9</b>	336	11.8	336	11.9
605.mcf_s	76	230	20.5	<b>227</b>	<b>20.8</b>	227	20.8	76	230	20.5	<b>227</b>	<b>20.8</b>	227	20.8
620.omnetpp_s	76	<b>130</b>	<b>12.6</b>	127	12.9	131	12.5	76	<b>130</b>	<b>12.6</b>	127	12.9	131	12.5
623.xalancbmk_s	76	100	14.2	99.8	14.2	<b>100</b>	<b>14.2</b>	76	100	14.2	99.8	14.2	<b>100</b>	<b>14.2</b>
625.x264_s	76	<b>97.2</b>	<b>18.1</b>	97.2	18.1	97.3	18.1	76	93.4	18.9	<b>93.3</b>	<b>18.9</b>	93.1	19.0
631.deepsjeng_s	76	<b>229</b>	<b>6.26</b>	229	6.27	229	6.26	76	<b>229</b>	<b>6.26</b>	229	6.27	229	6.26
641.leela_s	76	334	5.11	<b>332</b>	<b>5.14</b>	332	5.14	76	334	5.11	<b>332</b>	<b>5.14</b>	332	5.14
648.exchange2_s	76	143	20.5	144	20.4	<b>143</b>	<b>20.5</b>	76	143	20.5	144	20.4	<b>143</b>	<b>20.5</b>
657.xz_s	76	<b>241</b>	<b>25.6</b>	243	25.5	241	25.7	76	<b>241</b>	<b>25.6</b>	243	25.5	241	25.7

SPECspeed®2017\_int\_base = **12.6**

SPECspeed®2017\_int\_peak = **12.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH =  
"/root/cpu2017-1.1.7/lib/intel64:/root/cpu2017-1.1.7/je5.0.1-64"  
MALLOC\_CONF = "retain:true"  
OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM  
memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
jemalloc, a general purpose malloc implementation

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### General Notes (Continued)

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

### Platform Notes

BIOS Settings:

Power Technology = Custom  
Power Performance Tuning = BIOS Controls EPB  
ENERGY\_PERF\_BIAS\_CFG mode = Extreme Performance  
SNC (Sub NUMA)= Enable  
KTI Prefetch= Enable  
LLC Dead Line Alloc = Disable  
Hyper-Threading = Disable

Sysinfo program /root/cpu2017-1.1.7/bin/sysinfo  
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c  
running on 161-241.pnet Sun May 2 14:28:08 2021

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) Platinum 8368Q CPU @ 2.60GHz
 2 "physical id"s (chips)
 76 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores      : 38
siblings       : 38
physical 0:    : cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
                25 26 27 28 29 30 31 32 33 34 35 36 37
physical 1:    : cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
                25 26 27 28 29 30 31 32 33 34 35 36 37
```

From lscpu:

```
Architecture:    x86_64
CPU op-mode(s):  32-bit, 64-bit
Byte Order:      Little Endian
CPU(s):          76
On-line CPU(s) list: 0-75
Thread(s) per core: 1
Core(s) per socket: 38
Socket(s):       2
NUMA node(s):   2
Vendor ID:       GenuineIntel
```

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### Platform Notes (Continued)

```

CPU family:          6
Model:              106
Model name:         Intel(R) Xeon(R) Platinum 8368Q CPU @ 2.60GHz
Stepping:           6
CPU MHz:            3498.476
CPU max MHz:        3700.0000
CPU min MHz:        800.0000
BogoMIPS:           5200.00
Virtualization:     VT-x
L1d cache:          48K
L1i cache:          32K
L2 cache:           1280K
L3 cache:           58368K
NUMA node0 CPU(s): 0-37
NUMA node1 CPU(s): 38-75
Flags:              fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single
intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept
vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a
avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local split_lock_detect wbnoinvd dtherm ida arat pln pts avx512vbmi umip pku
ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 58368 KB

```

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
28 29 30 31 32 33 34 35 36 37
node 0 size: 488369 MB
node 0 free: 514815 MB
node 1 cpus: 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62
63 64 65 66 67 68 69 70 71 72 73 74 75
node 1 size: 486376 MB
node 1 free: 515182 MB
node distances:
node  0  1
  0:  10  20
  1:  20  10

```

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### Platform Notes (Continued)

From /proc/meminfo

MemTotal: 1056443096 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/sbin/tuned-adm active  
Current active profile: throughput-performance

/sys/devices/system/cpu/cpu\*/cpufreq/scaling\_governor has  
performance

From /etc/\*release\* /etc/\*version\*

os-release:  
NAME="Red Hat Enterprise Linux"  
VERSION="8.3 (Ootpa)"  
ID="rhel"  
ID\_LIKE="fedora"  
VERSION\_ID="8.3"  
PLATFORM\_ID="platform:el8"  
PRETTY\_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"  
ANSI\_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)  
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)  
system-release-cpe: cpe:/o:redhat:enterprise\_linux:8.3:ga

uname -a:

Linux 161-241.pnet 4.18.0-240.el8.x86\_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020 x86\_64  
x86\_64 x86\_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):	Not affected
CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):	Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling):	Not affected
CVE-2019-11135 (TSX Asynchronous Abort):	Not affected

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### Platform Notes (Continued)

run-level 3 May 2 13:12

SPEC is set to: /root/cpu2017-1.1.7

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfst	739G	194G	545G	27%	/

From /sys/devices/virtual/dmi/id

```
Vendor: Supermicro
Product: SYS-620U-TNR
Product Family: Ultra
Serial: 0123456789
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

32x SK Hynix HMA84GR7DJR4N-XN 32 GB 2 rank 3200

BIOS:

```
BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 1.1
BIOS Date: 04/21/2021
BIOS Revision: 5.22
```

(End of data from sysinfo program)

### Compiler Version Notes

```
=====  
C | 600.perlbench_s(peak)  
-----
```

```
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----
```

```
=====  
C | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
| 625.x264_s(base, peak) 657.xz_s(base, peak)  
-----
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----
```

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### Compiler Version Notes (Continued)

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-----

=====  
C | 600.perlbench\_s(base) 602.gcc\_s(base, peak) 605.mcf\_s(base, peak)  
625.x264\_s(base, peak) 657.xz\_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C++ | 620.omnetpp\_s(base, peak) 623.xalancbmk\_s(base, peak)  
631.deepsjeng\_s(base, peak) 641.leela\_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
Fortran | 648.exchange2\_s(base, peak)  
-----

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112\_000000  
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-----

### Base Compiler Invocation

C benchmarks:  
icx

C++ benchmarks:  
icpx

Fortran benchmarks:  
ifort



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## Base Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

## Base Optimization Flags

### C benchmarks:

```
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

### C++ benchmarks:

```
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-lqkmalloc
```

### Fortran benchmarks:

```
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries
```

## Peak Compiler Invocation

### C benchmarks (except as noted below):

icx

600.perlbench\_s: icc

### C++ benchmarks:

icpx

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## Peak Compiler Invocation (Continued)

Fortran benchmarks:  
ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
605.mcf_s: basepeak = yes
```

```
625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
657.xz_s: basepeak = yes
```

C++ benchmarks:

```
620.omnetpp_s: basepeak = yes
```

```
623.xalancbmk_s: basepeak = yes
```

```
631.deepsjeng_s: basepeak = yes
```

(Continued on next page)



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## Peak Optimization Flags (Continued)

641.leela\_s:basepeak = yes

Fortran benchmarks:

648.exchange2\_s:basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-CLX-revH.2021-04-14.html>

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html)

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-CLX-revH.2021-04-14.xml>

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml)

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