



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

GIGA-BYTE TECHNOLOGY CO., LTD.

R282-Z90  
(AMD EPYC 7763 , 2.45GHz)

SPECspeed®2017\_fp\_base = 249

SPECspeed®2017\_fp\_peak = 256

CPU2017 License: 9082

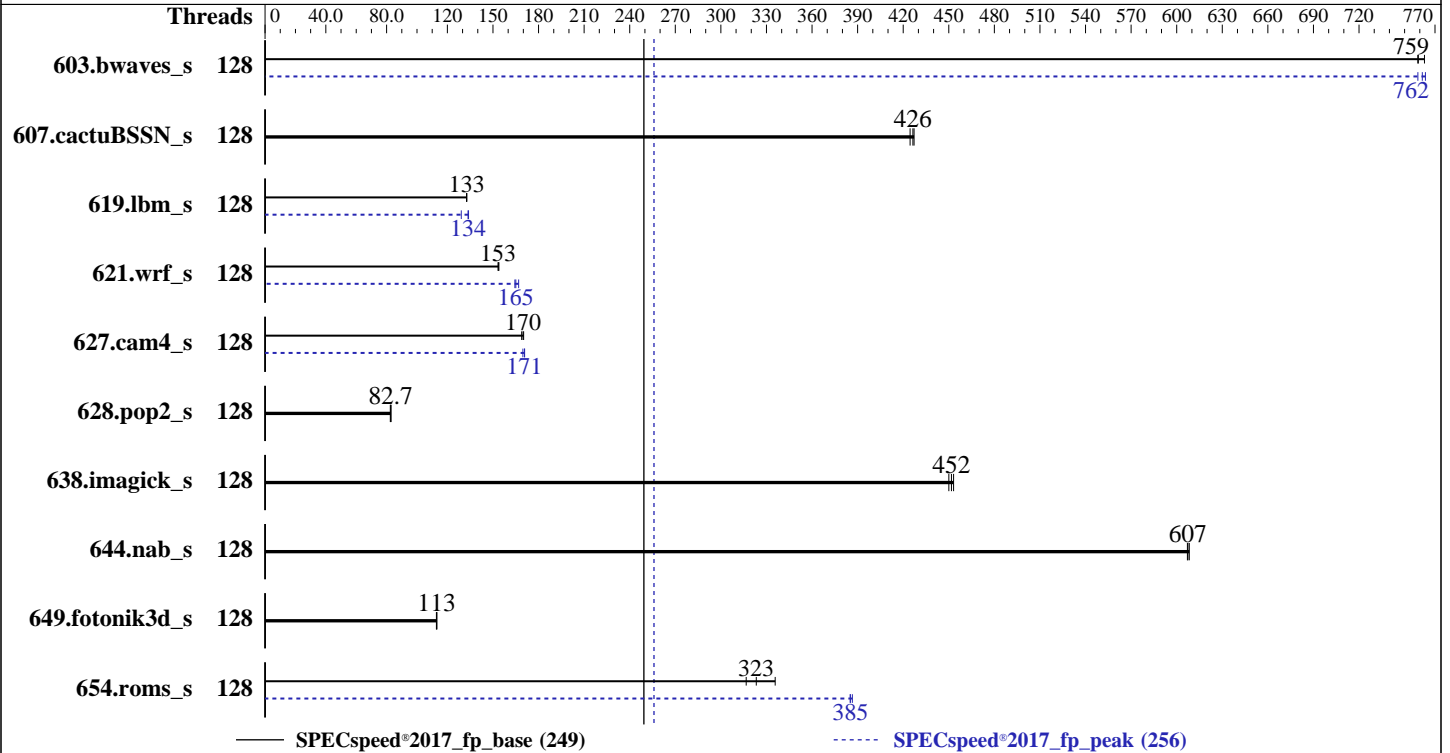
Test Sponsor: GIGA-BYTE TECHNOLOGY CO., LTD.

Tested by: GIGA-BYTE TECHNOLOGY CO., LTD.

Test Date: Feb-2021

Hardware Availability: Mar-2021

Software Availability: Mar-2021



### Hardware

CPU Name: AMD EPYC 7763  
 Max MHz: 3500  
 Nominal: 2450  
 Enabled: 128 cores, 2 chips  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 512 KB I+D on chip per core  
 L3: 256 MB I+D on chip per chip, 32 MB shared / 8 cores  
 Other: None  
 Memory: 1 TB (16 x 64 GB 4DRx16 PC4-3200AA-L)  
 Storage: 1 x 1.92 TB SATA SSD  
 Other: None

### Software

OS: Ubuntu 20.04.1 LTS (x86\_64) Kernel 5.4.0-65-generic  
 Compiler: C/C++/Fortran: Version 3.0.0 of AOCC  
 Parallel: Yes  
 Firmware: Version M02 released Feb-2021  
 File System: ext4  
 System State: Run level 5 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc: jemalloc memory allocator library v5.1.0;  
 Power Management: BIOS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

GIGA-BYTE TECHNOLOGY CO., LTD.  
R282-Z90  
(AMD EPYC 7763 , 2.45GHz)

SPECspeed®2017\_fp\_base = 249  
SPECspeed®2017\_fp\_peak = 256

CPU2017 License: 9082

Test Sponsor: GIGA-BYTE TECHNOLOGY CO., LTD.

Tested by: GIGA-BYTE TECHNOLOGY CO., LTD.

Test Date: Feb-2021

Hardware Availability: Mar-2021

Software Availability: Mar-2021

## Results Table

Benchmark	Base						Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	128	77.8	759	<b><u>77.7</u></b>	<b><u>759</u></b>	77.3	763	128	77.3	764	77.8	759	<b><u>77.5</u></b>	<b><u>762</u></b>
607.cactuBSSN_s	128	39.0	427	<b><u>39.1</u></b>	<b><u>426</u></b>	39.3	425	128	39.0	427	<b><u>39.1</u></b>	<b><u>426</u></b>	39.3	425
619.lbm_s	128	<b><u>39.5</u></b>	<b><u>133</u></b>	39.5	133	39.5	133	128	40.6	129	<b><u>39.2</u></b>	<b><u>134</u></b>	39.1	134
621.wrf_s	128	85.9	154	<b><u>86.2</u></b>	<b><u>153</u></b>	86.2	153	128	<b><u>80.1</u></b>	<b><u>165</u></b>	80.4	164	79.2	167
627.cam4_s	128	52.5	169	<b><u>52.2</u></b>	<b><u>170</u></b>	52.1	170	128	<b><u>51.9</u></b>	<b><u>171</u></b>	52.2	170	51.9	171
628.pop2_s	128	<b><u>144</u></b>	<b><u>82.7</u></b>	143	82.9	144	82.4	128	<b><u>144</u></b>	<b><u>82.7</u></b>	143	82.9	144	82.4
638.imagick_s	128	32.1	450	31.8	453	<b><u>31.9</u></b>	<b><u>452</u></b>	128	32.1	450	31.8	453	<b><u>31.9</u></b>	<b><u>452</u></b>
644.nab_s	128	28.7	608	<b><u>28.8</u></b>	<b><u>607</u></b>	28.8	607	128	28.7	608	<b><u>28.8</u></b>	<b><u>607</u></b>	28.8	607
649.fotonik3d_s	128	80.8	113	<b><u>80.7</u></b>	<b><u>113</u></b>	80.6	113	128	80.8	113	<b><u>80.7</u></b>	<b><u>113</u></b>	80.6	113
654.roms_s	128	<b><u>48.7</u></b>	<b><u>323</u></b>	49.7	317	46.9	336	128	40.9	385	40.7	387	<b><u>40.9</u></b>	<b><u>385</u></b>

SPECspeed®2017\_fp\_base = 249

SPECspeed®2017\_fp\_peak = 256

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
'echo 8 > /proc/sys/vm/dirty_ratio' run as root to limit dirty cache to 8% of
memory.
'echo 1 > /proc/sys/vm/swappiness' run as root to limit swap usage to minimum
necessary.
'echo 1 > /proc/sys/vm/zone_reclaim_mode' run as root to free node-local memory
and avoid remote memory usage.
'sync; echo 3 > /proc/sys/vm/drop_caches' run as root to reset filesystem caches.
'sysctl -w kernel.randomize_va_space=0' run as root to disable address space layout
randomization (ASLR) to reduce run-to-run variability.
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root to enable
Transparent Hugepages (THP) for this run.
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled' run as root for peak
runs of 628.pop2_s and 638.imagick_s to enable THP only on request.
```



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

GIGA-BYTE TECHNOLOGY CO., LTD.  
R282-Z90  
(AMD EPYC 7763 , 2.45GHz)

SPECspeed®2017\_fp\_base = 249

SPECspeed®2017\_fp\_peak = 256

CPU2017 License: 9082

Test Sponsor: GIGA-BYTE TECHNOLOGY CO., LTD.

Tested by: GIGA-BYTE TECHNOLOGY CO., LTD.

Test Date: Feb-2021

Hardware Availability: Mar-2021

Software Availability: Mar-2021

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP\_CPU\_AFFINITY = "0-127"

LD\_LIBRARY\_PATH =

"/cpu2017/amd\_speed\_aocc300\_milan\_A\_lib/64;/cpu2017/amd\_speed\_aocc300\_milan\_A\_lib/32:"

MALLOC\_CONF = "retain:true"

OMP\_DYNAMIC = "false"

OMP\_SCHEDULE = "static"

OMP\_STACKSIZE = "128M"

OMP\_THREAD\_LIMIT = "128"

Environment variables set by runcpu during the 603.bwaves\_s peak run:

GOMP\_CPU\_AFFINITY = "0-127"

Environment variables set by runcpu during the 619.lbm\_s peak run:

GOMP\_CPU\_AFFINITY = "0-127"

Environment variables set by runcpu during the 621.wrf\_s peak run:

GOMP\_CPU\_AFFINITY = "0-127"

Environment variables set by runcpu during the 627.cam4\_s peak run:

GOMP\_CPU\_AFFINITY = "0-127"

Environment variables set by runcpu during the 654.roms\_s peak run:

GOMP\_CPU\_AFFINITY = "0-127"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using openSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

The AMD64 AOCC Compiler Suite is available at

<http://developer.amd.com/amd-aocc/>

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)

jemalloc 5.1.0 is available here:

<https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2>



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**GIGA-BYTE TECHNOLOGY CO., LTD.**  
R282-Z90  
(AMD EPYC 7763 , 2.45GHz)

**SPECspeed®2017\_fp\_base = 249**  
**SPECspeed®2017\_fp\_peak = 256**

**CPU2017 License:** 9082  
**Test Sponsor:** GIGA-BYTE TECHNOLOGY CO., LTD.  
**Tested by:** GIGA-BYTE TECHNOLOGY CO., LTD.

**Test Date:** Feb-2021  
**Hardware Availability:** Mar-2021  
**Software Availability:** Mar-2021

## Platform Notes

BIOS settings:  
cTDP = 280  
Determinism Slider set to Power  
SMT set to disable  
IOMMU set to enable  
Package Power Limit set to 280  
NUMA nodes per socket set to NPS1

Sysinfo program /cpu2017/bin/sysinfo  
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c  
running on test Thu Feb 18 11:47:13 2021

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : AMD EPYC 7763 64-Core Processor  
2 "physical id"s (chips)  
128 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 64  
siblings : 64  
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63  
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63

From lscpu:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
Address sizes: 48 bits physical, 48 bits virtual  
CPU(s): 128  
On-line CPU(s) list: 0-127  
Thread(s) per core: 1  
Core(s) per socket: 64  
Socket(s): 2  
NUMA node(s): 2  
Vendor ID: AuthenticAMD  
CPU family: 25  
Model: 1  
Model name: AMD EPYC 7763 64-Core Processor  
Stepping: 1

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

GIGA-BYTE TECHNOLOGY CO., LTD.  
R282-Z90  
(AMD EPYC 7763 , 2.45GHz)

SPECspeed®2017\_fp\_base = 249  
SPECspeed®2017\_fp\_peak = 256

CPU2017 License: 9082  
Test Sponsor: GIGA-BYTE TECHNOLOGY CO., LTD.  
Tested by: GIGA-BYTE TECHNOLOGY CO., LTD.

Test Date: Feb-2021  
Hardware Availability: Mar-2021  
Software Availability: Mar-2021

## Platform Notes (Continued)

```

Frequency boost:          enabled
CPU MHz:                  1806.044
CPU max MHz:              2450.0000
CPU min MHz:              1500.0000
BogoMIPS:                 4900.00
Virtualization:          AMD-V
L1d cache:                4 MiB
L1i cache:                4 MiB
L2 cache:                 64 MiB
L3 cache:                 512 MiB
NUMA node0 CPU(s):       0-63
NUMA node1 CPU(s):       64-127
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:       Not affected
Vulnerability Mds:        Not affected
Vulnerability Meltdown:   Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:  Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:  Mitigation; Full AMD retpoline, IBPB conditional, IBRS_FW, STIBP disabled, RSB filling
Vulnerability Srbds:       Not affected
Vulnerability Tsx async abort: Not affected
Flags:                    fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good noopl nonstop_tsc cpuid extd_apicid aperfmperf pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr wbnoinvd arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold v_omsave_vmload vgif umip pku ospke vaes vpclmulqdq rdpid overflow_recov succor smca

```

```
/proc/cpuinfo cache data
cache size : 512 KB
```

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56
57 58 59 60 61 62 63
node 0 size: 515918 MB

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

GIGA-BYTE TECHNOLOGY CO., LTD.  
R282-Z90  
(AMD EPYC 7763 , 2.45GHz)

SPECspeed®2017\_fp\_base = 249  
SPECspeed®2017\_fp\_peak = 256

CPU2017 License: 9082  
Test Sponsor: GIGA-BYTE TECHNOLOGY CO., LTD.  
Tested by: GIGA-BYTE TECHNOLOGY CO., LTD.

Test Date: Feb-2021  
Hardware Availability: Mar-2021  
Software Availability: Mar-2021

## Platform Notes (Continued)

```

node 0 free: 514958 MB
node 1 cpus: 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88
89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105 106 107 108 109 110 111 112
113 114 115 116 117 118 119 120 121 122 123 124 125 126 127
node 1 size: 516076 MB
node 1 free: 515035 MB
node distances:
node 0 1
  0: 10 32
  1: 32 10

From /proc/meminfo
MemTotal:      1056762984 kB
HugePages_Total:      0
Hugepagesize:      2048 kB

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
performance

/usr/bin/lsb_release -d
Ubuntu 20.04.1 LTS

From /etc/*release* /etc/*version*
debian_version: bullseye/sid
os-release:
  NAME="Ubuntu"
  VERSION="20.04.1 LTS (Focal Fossa)"
  ID=ubuntu
  ID_LIKE=debian
  PRETTY_NAME="Ubuntu 20.04.1 LTS"
  VERSION_ID="20.04"
  HOME_URL="https://www.ubuntu.com/"
  SUPPORT_URL="https://help.ubuntu.com/"

uname -a:
Linux test 5.4.0-65-generic #73-Ubuntu SMP Mon Jan 18 17:25:17 UTC 2021 x86_64 x86_64
x86_64 GNU/Linux

```

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):	Not affected
CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

GIGA-BYTE TECHNOLOGY CO., LTD.  
R282-Z90  
(AMD EPYC 7763 , 2.45GHz)

SPECspeed®2017\_fp\_base = 249  
SPECspeed®2017\_fp\_peak = 256

CPU2017 License: 9082  
Test Sponsor: GIGA-BYTE TECHNOLOGY CO., LTD.  
Tested by: GIGA-BYTE TECHNOLOGY CO., LTD.

Test Date: Feb-2021  
Hardware Availability: Mar-2021  
Software Availability: Mar-2021

## Platform Notes (Continued)

CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):	Mitigation: Full AMD retpoline, IBPB: conditional, IBRS_FW, STIBP: disabled, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling):	Not affected
CVE-2019-11135 (TSX Asynchronous Abort):	Not affected

run-level 5 Feb 18 07:42

SPEC is set to: /cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	ext4	1.8T	17G	1.7T	1%	/

```

From /sys/devices/virtual/dmi/id
Vendor:          GIGABYTE
Product:         R282-Z90-00
Product Family: Server
Serial:          1234567890abcdefghijkl

```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```

Memory:
  16x Samsung M386A8K40DM2-CWE 64 GB 4 rank 3200
  16x Unknown Unknown

```

```

BIOS:
  BIOS Vendor:      GIGABYTE
  BIOS Version:     M02
  BIOS Date:        02/01/2021
  BIOS Revision:    5.21

```

(End of data from sysinfo program)

## Compiler Version Notes

```

=====
C          | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
          | 644.nab_s(base, peak)
-----

```

```

AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**GIGA-BYTE TECHNOLOGY CO., LTD.**  
R282-Z90  
(AMD EPYC 7763 , 2.45GHz)

**SPECspeed®2017\_fp\_base = 249**  
**SPECspeed®2017\_fp\_peak = 256**

**CPU2017 License:** 9082  
**Test Sponsor:** GIGA-BYTE TECHNOLOGY CO., LTD.  
**Tested by:** GIGA-BYTE TECHNOLOGY CO., LTD.

**Test Date:** Feb-2021  
**Hardware Availability:** Mar-2021  
**Software Availability:** Mar-2021

## Compiler Version Notes (Continued)

Thread model: posix  
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin

=====  
C++, C, Fortran | 607.cactuBSSN\_s(base, peak)

AMD clang version 12.0.0 (CLANG: AOCC\_3.0.0-Build#78 2020\_12\_10) (based on LLVM Mirror.Version.12.0.0)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin

AMD clang version 12.0.0 (CLANG: AOCC\_3.0.0-Build#78 2020\_12\_10) (based on LLVM Mirror.Version.12.0.0)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin

AMD clang version 12.0.0 (CLANG: AOCC\_3.0.0-Build#78 2020\_12\_10) (based on LLVM Mirror.Version.12.0.0)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin

=====  
Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak)  
| 654.roms\_s(base, peak)

AMD clang version 12.0.0 (CLANG: AOCC\_3.0.0-Build#78 2020\_12\_10) (based on LLVM Mirror.Version.12.0.0)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin

=====  
Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak)  
| 628.pop2\_s(base, peak)

AMD clang version 12.0.0 (CLANG: AOCC\_3.0.0-Build#78 2020\_12\_10) (based on LLVM Mirror.Version.12.0.0)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin

AMD clang version 12.0.0 (CLANG: AOCC\_3.0.0-Build#78 2020\_12\_10) (based on LLVM Mirror.Version.12.0.0)

Target: x86\_64-unknown-linux-gnu

(Continued on next page)





# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

GIGA-BYTE TECHNOLOGY CO., LTD.  
R282-Z90  
(AMD EPYC 7763 , 2.45GHz)

SPECspeed®2017\_fp\_base = 249  
SPECspeed®2017\_fp\_peak = 256

CPU2017 License: 9082

Test Sponsor: GIGA-BYTE TECHNOLOGY CO., LTD.

Tested by: GIGA-BYTE TECHNOLOGY CO., LTD.

Test Date: Feb-2021

Hardware Availability: Mar-2021

Software Availability: Mar-2021

## Compiler Version Notes (Continued)

Thread model: posix  
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin  
-----

## Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64  
607.cactuBSSN\_s: -DSPEC\_LP64  
619.lbm\_s: -DSPEC\_LP64  
621.wrf\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
627.cam4\_s: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
628.pop2\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
638.imagick\_s: -DSPEC\_LP64  
644.nab\_s: -DSPEC\_LP64  
649.fotonik3d\_s: -DSPEC\_LP64  
654.roms\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -mno-adx -mno-sse4a -Wl,-mllvm -Wl,-region-vectorize  
-Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3  
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=5  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -mllvm -function-specialize -flv-function-specialization

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

GIGA-BYTE TECHNOLOGY CO., LTD.  
R282-Z90  
(AMD EPYC 7763 , 2.45GHz)

SPECspeed®2017\_fp\_base = 249

SPECspeed®2017\_fp\_peak = 256

CPU2017 License: 9082

Test Sponsor: GIGA-BYTE TECHNOLOGY CO., LTD.

Tested by: GIGA-BYTE TECHNOLOGY CO., LTD.

Test Date: Feb-2021

Hardware Availability: Mar-2021

Software Availability: Mar-2021

## Base Optimization Flags (Continued)

C benchmarks (continued):

```
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -z muldefs
-DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -lamdlibm -ljemalloc
-lflang -lflangrti
```

Fortran benchmarks:

```
-m64 -mno-adx -mno-sse4a -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Hz,1,0x1 -O3
-march=znver3 -fveclib=AMDLIBM -ffast-math -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -z muldefs -DSPEC_OPENMP -fopenmp
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang -lflangrti
```

Benchmarks using both Fortran and C:

```
-m64 -mno-adx -mno-sse4a -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -Hz,1,0x1
-Mrecursive -mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop -z muldefs
-DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -lamdlibm -ljemalloc
-lflang -lflangrti
```

Benchmarks using Fortran, C, and C++:

```
-m64 -mno-adx -mno-sse4a -std=c++98
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

GIGA-BYTE TECHNOLOGY CO., LTD.  
R282-Z90  
(AMD EPYC 7763 , 2.45GHz)

SPECspeed®2017\_fp\_base = 249

SPECspeed®2017\_fp\_peak = 256

CPU2017 License: 9082

Test Sponsor: GIGA-BYTE TECHNOLOGY CO., LTD.

Tested by: GIGA-BYTE TECHNOLOGY CO., LTD.

Test Date: Feb-2021

Hardware Availability: Mar-2021

Software Availability: Mar-2021

## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-mllvm -enable-partial-unswitch -mllvm -unroll-threshold=100  
-finline-aggressive -mllvm -loop-unswitch-threshold=200000  
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch  
-mllvm -extra-vectorizer-passes -mllvm -convert-pow-exp-to-int=false  
-Hz,1,0x1 -Mrecursive -mllvm -fuse-tile-inner-loop -funroll-loops  
-mllvm -lsr-in-nested-loop -z muldefs -DSPEC_OPENMP -fopenmp  
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang -lflangrti
```

## Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument -Wno-return-type
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument -Wno-return-type
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument -Wno-return-type
```

Benchmarks using Fortran, C, and C++:

```
-Wno-unused-command-line-argument -Wno-return-type
```

## Peak Compiler Invocation

C benchmarks:

```
clang
```

Fortran benchmarks:

```
flang
```

Benchmarks using both Fortran and C:

```
flang clang
```

Benchmarks using Fortran, C, and C++:

```
clang++ clang flang
```



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

GIGA-BYTE TECHNOLOGY CO., LTD.  
R282-Z90  
(AMD EPYC 7763 , 2.45GHz)

SPECspeed®2017\_fp\_base = 249

SPECspeed®2017\_fp\_peak = 256

CPU2017 License: 9082

Test Sponsor: GIGA-BYTE TECHNOLOGY CO., LTD.

Tested by: GIGA-BYTE TECHNOLOGY CO., LTD.

Test Date: Feb-2021

Hardware Availability: Mar-2021

Software Availability: Mar-2021

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
619.lbm_s: -m64 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=5 -mllvm -unroll-threshold=50
-freemap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -fopenmp
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

638.imagick\_s: basepeak = yes

644.nab\_s: basepeak = yes

Fortran benchmarks:

```
603.bwaves_s: -m64 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -Mrecursive
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp
-DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -lamdlibm
-ljemalloc -lflang
```

649.fotonik3d\_s: basepeak = yes

654.roms\_s: Same as 603.bwaves\_s

Benchmarks using both Fortran and C:

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

GIGA-BYTE TECHNOLOGY CO., LTD.  
R282-Z90  
(AMD EPYC 7763 , 2.45GHz)

SPECspeed®2017\_fp\_base = 249  
SPECspeed®2017\_fp\_peak = 256

CPU2017 License: 9082

Test Sponsor: GIGA-BYTE TECHNOLOGY CO., LTD.

Tested by: GIGA-BYTE TECHNOLOGY CO., LTD.

Test Date: Feb-2021

Hardware Availability: Mar-2021

Software Availability: Mar-2021

## Peak Optimization Flags (Continued)

```
621.wrf_s: -m64 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=5 -mllvm -unroll-threshold=50
-freemap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -Hz,1,0x1 -O3
-Mrecursive -mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -lamdlibm
-ljemalloc -lflang
```

```
627.cam4_s: -m64 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=5 -mllvm -unroll-threshold=50
-freemap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -Mrecursive
-DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -lamdlibm
-ljemalloc -lflang
```

628.pop2\_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN\_s: basepeak = yes

## Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

GIGA-BYTE TECHNOLOGY CO., LTD.  
R282-Z90  
(AMD EPYC 7763 , 2.45GHz)

SPECspeed®2017\_fp\_base = 249  
SPECspeed®2017\_fp\_peak = 256

CPU2017 License: 9082

Test Sponsor: GIGA-BYTE TECHNOLOGY CO., LTD.

Tested by: GIGA-BYTE TECHNOLOGY CO., LTD.

Test Date: Feb-2021

Hardware Availability: Mar-2021

Software Availability: Mar-2021

## Peak Other Flags (Continued)

Fortran benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument -Wno-return-type

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument -Wno-return-type

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc300-flags-A1.html>

<http://www.spec.org/cpu2017/flags/GIGA-BYTE-Platform-SPECcpu2017-Flags-V1.2-Milan.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc300-flags-A1.xml>

<http://www.spec.org/cpu2017/flags/GIGA-BYTE-Platform-SPECcpu2017-Flags-V1.2-Milan.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.5 on 2021-02-18 06:47:13-0500.

Report generated on 2021-03-16 15:24:01 by CPU2017 PDF formatter v6255.

Originally published on 2021-03-16.