



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## Dell Inc.

SPECspeed®2017\_fp\_base = 135

PowerEdge C6420 (Intel Xeon Gold 5220R, 2.20 GHz)

SPECspeed®2017\_fp\_peak = 138

CPU2017 License: 55

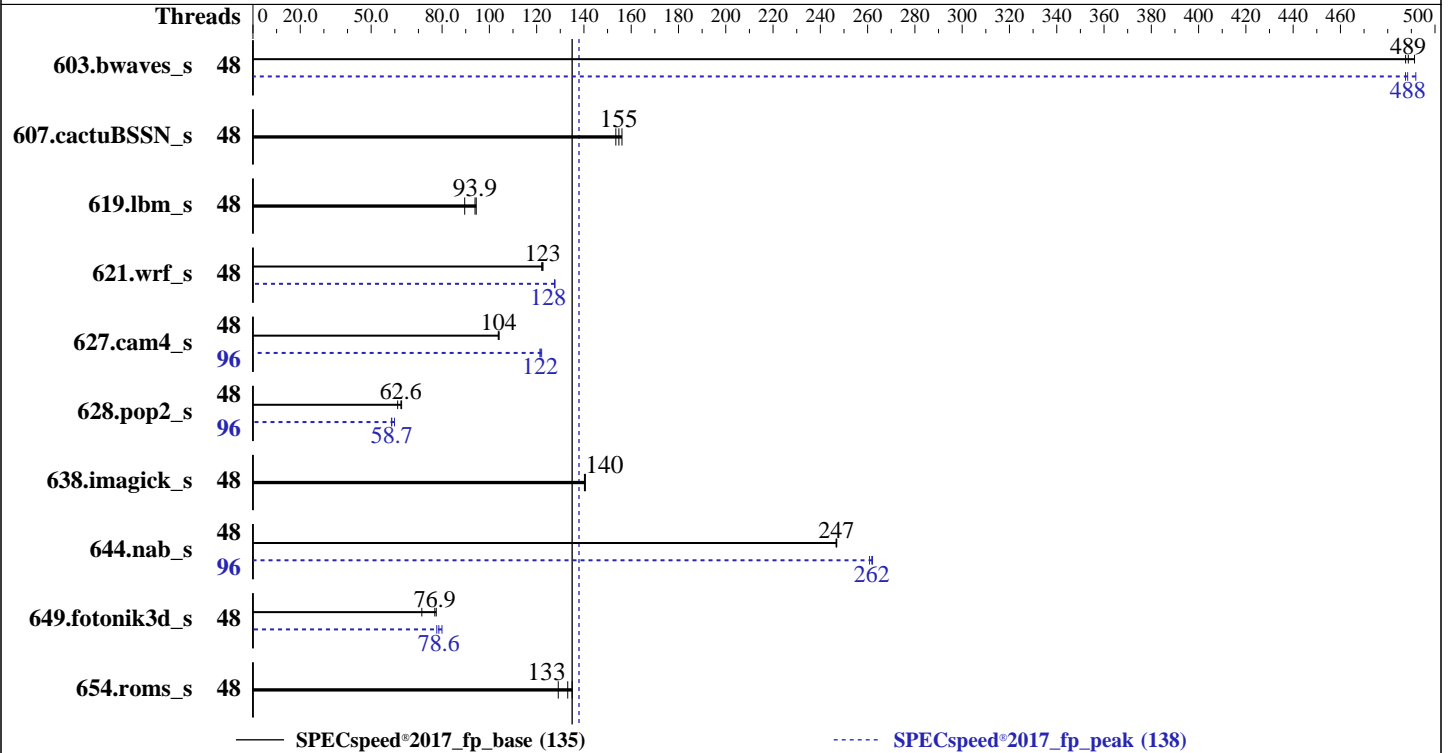
Test Date: Apr-2020

Test Sponsor: Dell Inc.

Hardware Availability: Feb-2020

Tested by: Dell Inc.

Software Availability: Feb-2020



### Hardware

CPU Name: Intel Xeon Gold 5220R  
 Max MHz: 4000  
 Nominal: 2200  
 Enabled: 48 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 35.75 MB I+D on chip per chip  
 Other: None  
 Memory: 384 GB (12 x 32 GB 2Rx8 PC4-2933V-R, running at 2666)  
 Storage: 1 x 480 GB SATA SSD  
 Other: None

### Software

OS: CentOS Linux 8.1.1911  
 kernel 4.18.0-147.5.1.el8\_1.x86\_64  
 Compiler: C/C++: Version 19.0.5.281 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 19.0.5.281 of Intel Fortran Compiler for Linux  
 Parallel: Yes  
 Firmware: Version 2.7.3 released Mar-2020  
 File System: tmpfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None  
 Power Management: BIOS set to prefer performance at the cost of additional power usage.



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017\_fp\_base = 135

PowerEdge C6420 (Intel Xeon Gold 5220R, 2.20 GHz)

SPECSpeed®2017\_fp\_peak = 138

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2020

Hardware Availability: Feb-2020

Software Availability: Feb-2020

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	48	121	488	120	491	<u>121</u>	<u>489</u>	48	<u>121</u>	<u>488</u>	120	492	121	488
607.cactuBSSN_s	48	<b>108</b>	<b>155</b>	109	153	107	156	48	<b>108</b>	<b>155</b>	109	153	107	156
619.lbm_s	48	<u>55.8</u>	<u>93.9</u>	58.5	89.6	55.5	94.4	48	<u>55.8</u>	<u>93.9</u>	58.5	89.6	55.5	94.4
621.wrf_s	48	108	123	108	122	<b>108</b>	<b>123</b>	48	104	128	104	128	<b>104</b>	<b>128</b>
627.cam4_s	48	<b>85.2</b>	<b>104</b>	85.4	104	85.2	104	96	73.0	121	72.6	122	<b>72.9</b>	<b>122</b>
628.pop2_s	48	194	61.2	<b>190</b>	<b>62.6</b>	189	62.9	96	198	59.9	<b>202</b>	<b>58.7</b>	203	58.6
638.imagick_s	48	103	141	<b>103</b>	<b>140</b>	103	140	48	103	141	<b>103</b>	<b>140</b>	103	140
644.nab_s	48	70.8	247	<b>70.8</b>	<b>247</b>	70.8	247	96	67.0	261	<b>66.7</b>	<b>262</b>	66.7	262
649.fotonik3d_s	48	128	71.4	<b>119</b>	<b>76.9</b>	118	77.5	48	<b>116</b>	<b>78.6</b>	117	77.7	114	80.0
654.roms_s	48	117	135	<b>118</b>	<b>133</b>	122	129	48	117	135	<b>118</b>	<b>133</b>	122	129

SPECSpeed®2017\_fp\_base = 135

SPECSpeed®2017\_fp\_peak = 138

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,compact,1,0"  
LD\_LIBRARY\_PATH = "/dev/shm/cpu2017/lib/intel64"  
OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017\_fp\_base = 135

PowerEdge C6420 (Intel Xeon Gold 5220R, 2.20 GHz)

SPECSpeed®2017\_fp\_peak = 138

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2020

Hardware Availability: Feb-2020

Software Availability: Feb-2020

## General Notes (Continued)

Transparent Huge Pages enabled by default  
 Prior to runcpu invocation  
 Filesystem page cache synced and cleared with:  
 sync; echo 3> /proc/sys/vm/drop\_caches  
 runcpu command invoked through numactl i.e.:  
 numactl --interleave=all runcpu <etc>

## Platform Notes

BIOS settings:  
 Sub NUMA Cluster enabled  
 Virtualization Technology disabled  
 System Profile set to Custom  
 CPU Performance set to Maximum Performance  
 C States set to Autonomous  
 C1E disabled  
 Uncore Frequency set to Dynamic  
 Energy Efficiency Policy set to Performance  
 Memory Patrol Scrub set to standard  
 Logical Processor enabled  
 CPU Interconnect Bus Link Power Management disabled  
 PCI ASPM L1 Link Power Management disabled  
 UPI Prefetch enabled  
 LLC Prefetch disabled  
 Dead Line LLC Alloc enabled  
 Directory AtoS disabled

Sysinfo program /dev/shm/cpu2017/bin/sysinfo  
 Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011  
 running on localhost.localdomain Mon Apr 27 11:41:49 2020

SUT (System Under Test) info as seen by some common utilities.  
 For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```

model name : Intel(R) Xeon(R) Gold 5220R CPU @ 2.20GHz
 2 "physical id"s (chips)
 96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 48
physical 0: cores 0 1 2 3 4 5 6 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
physical 1: cores 0 1 2 3 4 5 6 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017\_fp\_base = 135

PowerEdge C6420 (Intel Xeon Gold 5220R, 2.20 GHz)

SPECspeed®2017\_fp\_peak = 138

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Apr-2020  
Hardware Availability: Feb-2020  
Software Availability: Feb-2020

## Platform Notes (Continued)

```

From lscpu:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                96
On-line CPU(s) list:  0-95
Thread(s) per core:    2
Core(s) per socket:    24
Socket(s):              2
NUMA node(s):          4
Vendor ID:              GenuineIntel
CPU family:             6
Model:                  85
Model name:             Intel(R) Xeon(R) Gold 5220R CPU @ 2.20GHz
Stepping:               7
CPU MHz:                3403.806
CPU max MHz:            4000.0000
CPU min MHz:            1000.0000
BogoMIPS:               4400.00
Virtualization:         VT-x
L1d cache:              32K
L1i cache:              32K
L2 cache:               1024K
L3 cache:               36608K
NUMA node0 CPU(s):     0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68,72,76,80,84,88,92
NUMA node1 CPU(s):     1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69,73,77,81,85,89,93
NUMA node2 CPU(s):     2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70,74,78,82,86,90,94
NUMA node3 CPU(s):     3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,63,67,71,75,79,83,87,91,95
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx fl6c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_l1d
arch_capabilities

/proc/cpuinfo cache data
cache size : 36608 KB

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017\_fp\_base = 135

PowerEdge C6420 (Intel Xeon Gold 5220R, 2.20 GHz)

SPECspeed®2017\_fp\_peak = 138

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2020

Hardware Availability: Feb-2020

Software Availability: Feb-2020

## Platform Notes (Continued)

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)

node 0 cpus: 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60 64 68 72 76 80 84 88 92

node 0 size: 95278 MB

node 0 free: 70519 MB

node 1 cpus: 1 5 9 13 17 21 25 29 33 37 41 45 49 53 57 61 65 69 73 77 81 85 89 93

node 1 size: 96763 MB

node 1 free: 83772 MB

node 2 cpus: 2 6 10 14 18 22 26 30 34 38 42 46 50 54 58 62 66 70 74 78 82 86 90 94

node 2 size: 96763 MB

node 2 free: 68056 MB

node 3 cpus: 3 7 11 15 19 23 27 31 35 39 43 47 51 55 59 63 67 71 75 79 83 87 91 95

node 3 size: 96762 MB

node 3 free: 83814 MB

node distances:

node 0 1 2 3

0: 10 21 11 21

1: 21 10 21 11

2: 11 21 10 21

3: 21 11 21 10

From /proc/meminfo

MemTotal: 394821696 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

centos-release: CentOS Linux release 8.1.1911 (Core)

centos-release-upstream: Derived from Red Hat Enterprise Linux 8.1 (Source)

os-release:

NAME="CentOS Linux"

VERSION="8 (Core)"

ID="centos"

ID\_LIKE="rhel fedora"

VERSION\_ID="8"

PLATFORM\_ID="platform:el8"

PRETTY\_NAME="CentOS Linux 8 (Core)"

ANSI\_COLOR="0;31"

redhat-release: CentOS Linux release 8.1.1911 (Core)

system-release: CentOS Linux release 8.1.1911 (Core)

system-release-cpe: cpe:/o:centos:centos:8

uname -a:

Linux localhost.localdomain 4.18.0-147.5.1.el8\_1.x86\_64 #1 SMP Wed Feb 5 02:00:39 UTC 2020 x86\_64 x86\_64 x86\_64 GNU/Linux

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017\_fp\_base = 135

PowerEdge C6420 (Intel Xeon Gold 5220R, 2.20 GHz)

SPECspeed®2017\_fp\_peak = 138

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2020

Hardware Availability: Feb-2020

Software Availability: Feb-2020

## Platform Notes (Continued)

Kernel self-reported vulnerability status:

```

itlb_multihit: Processor vulnerable
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and __user
pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional,
RSB filling
tsx_async_abort: Mitigation: Clear CPU buffers; SMT vulnerable

```

run-level 3 Apr 25 07:51

SPEC is set to: /dev/shm/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
tmpfs	tmpfs	189G	68G	121G	37%	/dev/shm

```

From /sys/devices/virtual/dmi/id
BIOS: Dell Inc. 2.7.3 03/25/2020
Vendor: Dell Inc.
Product: PowerEdge C6420
Product Family: PowerEdge
Serial: 1234567

```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```

Memory:
8x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
1x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
3x 00AD069D00AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
4x Not Specified Not Specified

```

(End of data from sysinfo program)

## Compiler Version Notes

```

=====
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
  | 644.nab_s(base, peak)
=====

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017\_fp\_base = 135

PowerEdge C6420 (Intel Xeon Gold 5220R, 2.20 GHz)

SPECSpeed®2017\_fp\_peak = 138

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2020

Hardware Availability: Feb-2020

Software Availability: Feb-2020

## Compiler Version Notes (Continued)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.5.281 Build 20190815  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

=====  
C++, C, Fortran | 607.cactuBSSN\_s(base, peak)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.5.281 Build 20190815  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.5.281 Build 20190815  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.5.281 Build 20190815  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

=====  
Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak)  
| 654.roms\_s(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.5.281 Build 20190815  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

=====  
Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak)  
| 628.pop2\_s(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.5.281 Build 20190815  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.5.281 Build 20190815  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:  
icc

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017\_fp\_base = 135

PowerEdge C6420 (Intel Xeon Gold 5220R, 2.20 GHz)

SPECspeed®2017\_fp\_peak = 138

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2020

Hardware Availability: Feb-2020

Software Availability: Feb-2020

## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

## Base Portability Flags

```
603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
```

Fortran benchmarks:

```
-m64 -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-nostandard-realloc-lhs
```

Benchmarks using both Fortran and C:

```
-m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
```

(Continued on next page)





# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017\_fp\_base = 135

PowerEdge C6420 (Intel Xeon Gold 5220R, 2.20 GHz)

SPECspeed®2017\_fp\_peak = 138

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2020

Hardware Availability: Feb-2020

Software Availability: Feb-2020

## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

`-nostandard-realloc-lhs`

## Peak Compiler Invocation

C benchmarks:

`icc`

Fortran benchmarks:

`ifort`

Benchmarks using both Fortran and C:

`ifort icc`

Benchmarks using Fortran, C, and C++:

`icpc icc ifort`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

619.lbm\_s: `basepeak = yes`

638.imagick\_s: `basepeak = yes`

644.nab\_s: `-m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4  
-qopenmp -DSPEC_OPENMP`

Fortran benchmarks:

603.bwaves\_s: `-m64 -prof-gen(pass 1) -prof-use(pass 2)  
-DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -O2 -xCORE-AVX512  
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div  
-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs`

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017\_fp\_base = 135

PowerEdge C6420 (Intel Xeon Gold 5220R, 2.20 GHz)

SPECspeed®2017\_fp\_peak = 138

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2020

Hardware Availability: Feb-2020

Software Availability: Feb-2020

## Peak Optimization Flags (Continued)

649.fotonik3d\_s: Same as 603.bwaves\_s

654.roms\_s: basepeak = yes

Benchmarks using both Fortran and C:

```
621.wrf_s: -m64 -std=c11 -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-prefetch -ipo -O3 -ffinite-math-only
-no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs
```

```
627.cam4_s: -m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
-qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
```

628.pop2\_s: Same as 621.wrf\_s

Benchmarks using Fortran, C, and C++:

607.cactuBSSN\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic19.0u5-official-linux64\\_revD.html](http://www.spec.org/cpu2017/flags/Intel-ic19.0u5-official-linux64_revD.html)

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE9.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic19.0u5-official-linux64\\_revD.xml](http://www.spec.org/cpu2017/flags/Intel-ic19.0u5-official-linux64_revD.xml)

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE9.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.0 on 2020-04-27 11:41:49-0400.

Report generated on 2020-05-26 14:49:34 by CPU2017 PDF formatter v6255.

Originally published on 2020-05-26.