SPEC CPU®2017 Floating Point Rate Result

Inspur Corporation

SPECrate®2017_fp_base = 201

Inspur NF5280M5 (Intel Xeon Gold 6226R)

SPECrate®2017_fp_peak = 213
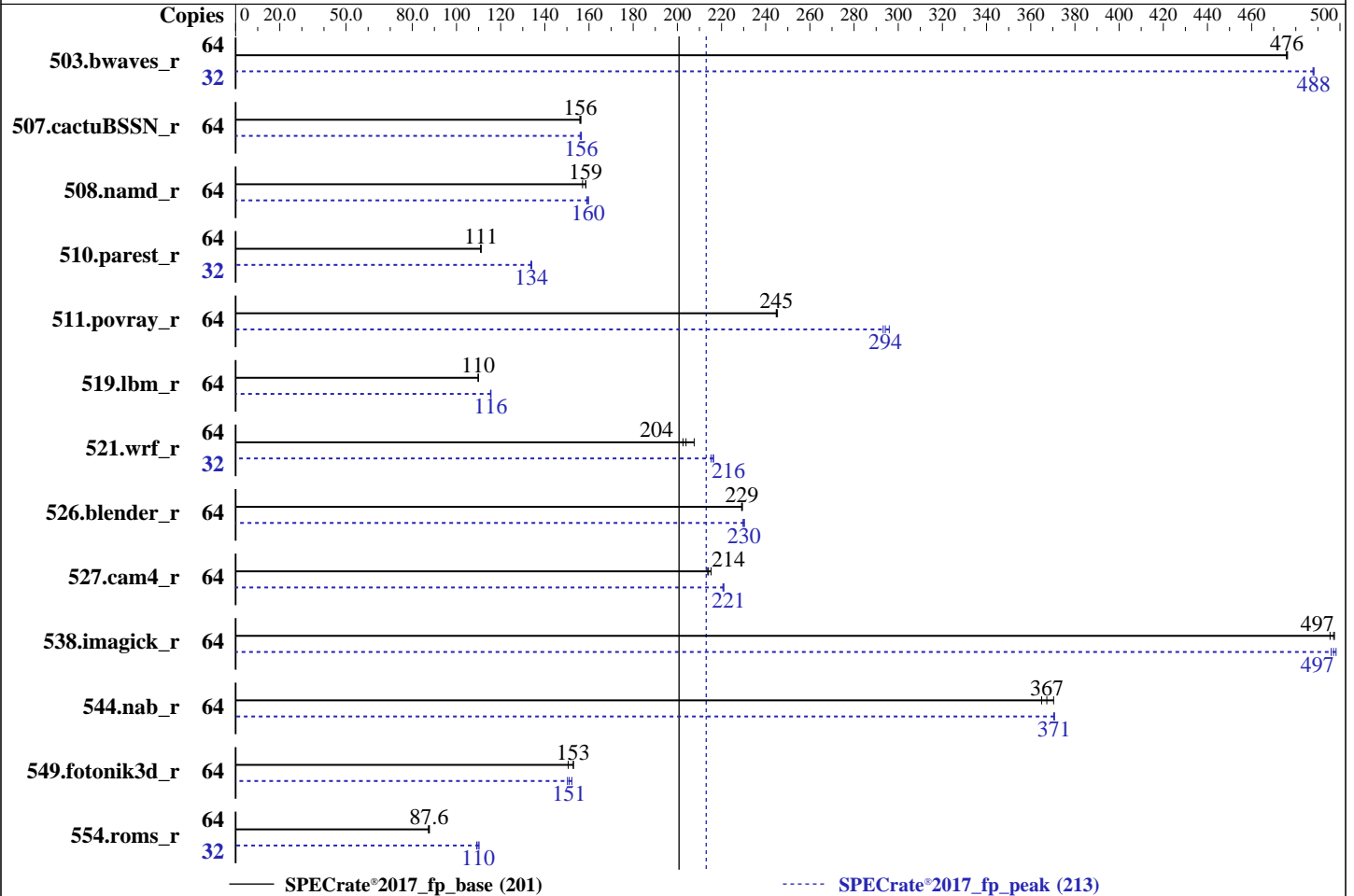
CPU2017 License: 3358	**Test Date:** Apr-2020
Test Sponsor: Inspur Corporation	**Hardware Availability:** Feb-2020
Tested by: Inspur Corporation	**Software Availability:** May-2019

Copies

Benchmark	Copies	base	peak
503.bwaves_r	64	476	
	32		488
507.cactuBSSN_r	64	156	156
508.namd_r	64	159	160
510.parest_r	64	111	
	32		134
511.povray_r	64	245	294
519.lbm_r	64	110	116
521.wrf_r	64	204	
	32		216
526.blender_r	64	229	230
527.cam4_r	64	214	221
538.imagick_r	64	497	497
544.nab_r	64	367	371
549.fotonik3d_r	64	153	151
554.roms_r	64	87.6	
	32		110

—— SPECrate®2017_fp_base (201)   ----- SPECrate®2017_fp_peak (213)

Hardware

CPU Name: Intel Xeon Gold 6226R
 Max MHz: 3900
 Nominal: 2900
Enabled: 32 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 22 MB I+D on chip per chip
 Other: None
Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R)
Storage: 1 x 1 TB SATA SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 12 SP4
 4.12.14-94.41-default
Compiler: C/C++: Version 19.0.4.227 of Intel C/C++
 Compiler Build 20190416 for Linux;
 Fortran: Version 19.0.4.227 of Intel Fortran
 Compiler Build 20190416 for Linux
Parallel: No
Firmware: Version 4.1.7 released Apr-2019
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.

Standard Performance Evaluation Corporation (info@spec.org) https://www.spec.org/

SPEC CPU®2017 Floating Point Rate Result

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Inspur Corporation

Inspur NF5280M5 (Intel Xeon Gold 6226R)

SPECrate®2017_fp_base = 201

SPECrate®2017_fp_peak = 213

CPU2017 License: 3358	**Test Date:** Apr-2020
Test Sponsor: Inspur Corporation	**Hardware Availability:** Feb-2020
Tested by: Inspur Corporation	**Software Availability:** May-2019

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	64	1349	476	1348	476	**1348**	**476**	32	658	488	**657**	**488**	657	488
507.cactuBSSN_r	64	518	156	520	156	**519**	**156**	64	518	157	**518**	**156**	519	156
508.namd_r	64	**384**	**159**	387	157	383	159	64	381	160	382	159	**381**	**160**
510.parest_r	64	1509	111	1506	111	**1507**	**111**	32	**625**	**134**	625	134	626	134
511.povray_r	64	**610**	**245**	611	245	609	245	64	**508**	**294**	505	296	510	293
519.lbm_r	64	614	110	614	110	**614**	**110**	64	584	116	584	115	**584**	**116**
521.wrf_r	64	691	208	708	203	**703**	**204**	32	331	216	**331**	**216**	333	215
526.blender_r	64	425	229	**425**	**229**	425	229	64	**423**	**230**	424	230	423	230
527.cam4_r	64	524	214	520	215	**523**	**214**	64	**507**	**221**	507	221	506	221
538.imagick_r	64	320	497	321	495	**320**	**497**	64	321	496	319	498	**320**	**497**
544.nab_r	64	**293**	**367**	291	370	295	365	64	291	370	291	371	**291**	**371**
549.fotonik3d_r	64	**1632**	**153**	1630	153	1656	151	64	1638	152	**1652**	**151**	1660	150
554.roms_r	64	**1161**	**87.6**	1165	87.3	1159	87.8	32	**463**	**110**	461	110	466	109
SPECrate®2017_fp_base =	**201**												
SPECrate®2017_fp_peak =	**213**												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
SCALING_GOVERNOR set to Performance

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:

(Continued on next page)

Inspur Corporation

Inspur NF5280M5 (Intel Xeon Gold 6226R)

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Tested by: Inspur Corporation

Test Date: Apr-2020
Hardware Availability: Feb-2020
Software Availability: May-2019

General Notes (Continued)

```
sync; echo 3>        /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
```

Platform Notes

```
BIOS configuration:
ENERGY_PERF_BIAS_CFG mode set to Performance
Hardware Prefetch set to Disable
VT Support set to Disable
C1E Support set to Disable
IMC (Integrated memory controller) Interleaving set to 1-way
Sub NUMA Cluster (SNC) set to Enable

Sysinfo program /home/CPU2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011
running on linux-jkf2 Mon Apr 27 23:48:26 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Gold 6226R CPU @ 2.90GHz
      2  "physical id"s (chips)
      64 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 16
      siblings  : 32
      physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
      physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
   Architecture:          x86_64
   CPU op-mode(s):        32-bit, 64-bit
   Byte Order:            Little Endian
   CPU(s):                64
```

(Continued on next page)

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Inspur NF5280M5 (Intel Xeon Gold 6226R)

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SPECrate®2017_fp_peak = 213

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Platform Notes (Continued)

```
       On-line CPU(s) list:    0-63
       Thread(s) per core:     2
       Core(s) per socket:     16
       Socket(s):              2
       NUMA node(s):           4
       Vendor ID:              GenuineIntel
       CPU family:             6
       Model:                  85
       Model name:             Intel(R) Xeon(R) Gold 6226R CPU @ 2.90GHz
       Stepping:               7
       CPU MHz:                2900.000
       CPU max MHz:            3900.0000
       CPU min MHz:            1200.0000
       BogoMIPS:               5800.00
       Virtualization:         VT-x
       L1d cache:              32K
       L1i cache:              32K
       L2 cache:               1024K
       L3 cache:               22528K
       NUMA node0 CPU(s):      0-3,8-11,32-35,40-43
       NUMA node1 CPU(s):      4-7,12-15,36-39,44-47
       NUMA node2 CPU(s):      16-19,24-27,48-51,56-59
       NUMA node3 CPU(s):      20-23,28-31,52-55,60-63
       Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
    pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
    lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
    aperfmperf pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
    pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
    rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single ssbd
    mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1
    hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap
    clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves
    cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts hwp
    hwp_act_window hwp_epp hwp_pkg_req pku ospke avx512_vnni flush_l1d arch_capabilities

 /proc/cpuinfo cache data
    cache size : 22528 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
   available: 4 nodes (0-3)
   node 0 cpus: 0 1 2 3 8 9 10 11 32 33 34 35 40 41 42 43
   node 0 size: 192032 MB
   node 0 free: 180816 MB
   node 1 cpus: 4 5 6 7 12 13 14 15 36 37 38 39 44 45 46 47
   node 1 size: 193530 MB
   node 1 free: 186789 MB
```

(Continued on next page)

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Inspur NF5280M5 (Intel Xeon Gold 6226R)

SPECrate®2017_fp_base = 201

SPECrate®2017_fp_peak = 213

CPU2017 License: 3358

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Tested by: Inspur Corporation

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Hardware Availability: Feb-2020

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Platform Notes (Continued)

```
    node 2 cpus: 16 17 18 19 24 25 26 27 48 49 50 51 56 57 58 59
    node 2 size: 193530 MB
    node 2 free: 186734 MB
    node 3 cpus: 20 21 22 23 28 29 30 31 52 53 54 55 60 61 62 63
    node 3 size: 193290 MB
    node 3 free: 186365 MB
    node distances:
    node   0   1   2   3
      0:  10  11  21  21
      1:  11  10  21  21
      2:  21  21  10  11
      3:  21  21  11  10

  From /proc/meminfo
     MemTotal:       790920876 kB
     HugePages_Total:        0
     Hugepagesize:        2048 kB

  /usr/bin/lsb_release -d
     SUSE Linux Enterprise Server 12 SP4

  From /etc/*release* /etc/*version*
     SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
        VERSION = 12
        PATCHLEVEL = 4
        # This file is deprecated and will be removed in a future service pack or release.
        # Please check /etc/os-release for details about this release.
     os-release:
        NAME="SLES"
        VERSION="12-SP4"
        VERSION_ID="12.4"
        PRETTY_NAME="SUSE Linux Enterprise Server 12 SP4"
        ID="sles"
        ANSI_COLOR="0;32"
        CPE_NAME="cpe:/o:suse:sles:12:sp4"

  uname -a:
     Linux linux-jkf2 4.12.14-94.41-default #1 SMP Wed Oct 31 12:25:04 UTC 2018 (3090901)
     x86_64 x86_64 x86_64 GNU/Linux

  Kernel self-reported vulnerability status:

  CVE-2018-3620 (L1 Terminal Fault):        Not affected
  Microarchitectural Data Sampling:         No status reported
  CVE-2017-5754 (Meltdown):                 Not affected
  CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
```

(Continued on next page)

Inspur Corporation

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Platform Notes (Continued)

```
                                       via prctl and seccomp
 CVE-2017-5753 (Spectre variant 1):    Mitigation: __user pointer sanitization
 CVE-2017-5715 (Spectre variant 2):    Mitigation: Indirect Branch Restricted
                                       Speculation, IBPB, IBRS_FW


 run-level 3 Apr 27 15:56 last=5

 SPEC is set to: /home/CPU2017
    Filesystem      Type  Size  Used Avail Use% Mounted on
    /dev/md126p11   xfs   882G   36G  846G   5% /home

 From /sys/devices/virtual/dmi/id
    BIOS:    American Megatrends Inc. 4.1.7 04/19/2019
    Vendor:  Inspur
    Product: NF5280M5
    Serial:  217406686


 Additional information from dmidecode follows.  WARNING: Use caution when you interpret
 this section. The 'dmidecode' program reads system data which is "intended to allow
 hardware to be accurately determined", but the intent may not be met, as there are
 frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   Memory:
     24x Hynix HMAA4GR7AJR8N-WM 32 GB 2 rank 2933

 (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C                   | 519.lbm_r(base, peak) 538.imagick_r(base, peak)
                    | 544.nab_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C++                 | 508.namd_r(base, peak) 510.parest_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
```

(Continued on next page)

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CPU2017 License: 3358
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Tested by: Inspur Corporation

Test Date: Apr-2020
Hardware Availability: Feb-2020
Software Availability: May-2019

Compiler Version Notes (Continued)

```
C++, C           | 511.povray_r(base, peak) 526.blender_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
Fortran          | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak)
                 | 554.roms_r(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
Fortran, C       | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------
```

SPEC CPU®2017 Floating Point Rate Result

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Base Compiler Invocation

C benchmarks:
`icc -m64 -std=c11`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`ifort -m64 icc -m64 -std=c11`

Benchmarks using both C and C++:
`icpc -m64 icc -m64 -std=c11`

Benchmarks using Fortran, C, and C++:
`icpc -m64 icc -m64 -std=c11 ifort -m64`

Base Portability Flags

503.bwaves_r: `-DSPEC_LP64`
507.cactuBSSN_r: `-DSPEC_LP64`
508.namd_r: `-DSPEC_LP64`
510.parest_r: `-DSPEC_LP64`
511.povray_r: `-DSPEC_LP64`
519.lbm_r: `-DSPEC_LP64`
521.wrf_r: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
526.blender_r: `-DSPEC_LP64 -DSPEC_LINUX -funsigned-char`
527.cam4_r: `-DSPEC_LP64 -DSPEC_CASE_FLAG`
538.imagick_r: `-DSPEC_LP64`
544.nab_r: `-DSPEC_LP64`
549.fotonik3d_r: `-DSPEC_LP64`
554.roms_r: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only`
`-qopt-mem-layout-trans=4`

C++ benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only`

(Continued on next page)

Inspur Corporation

Inspur NF5280M5 (Intel Xeon Gold 6226R)

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SPECrate®2017_fp_peak = 213

CPU2017 License: 3358
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Tested by: Inspur Corporation

Test Date: Apr-2020
Hardware Availability: Feb-2020
Software Availability: May-2019

Base Optimization Flags (Continued)

C++ benchmarks (continued):
```
-qopt-mem-layout-trans=4
```

Fortran benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte
```

Benchmarks using both Fortran and C:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte
```

Benchmarks using both C and C++:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4
```

Benchmarks using Fortran, C, and C++:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte
```

Peak Compiler Invocation

C benchmarks:
```
icc -m64 -std=c11
```

C++ benchmarks:
```
icpc -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using both C and C++:
```
icpc -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:
```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

544.nab_r: Same as 538.imagick_r

C++ benchmarks:

508.namd_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

510.parest_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

Fortran benchmarks:

503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -auto
-nostandard-realloc-lhs -align array32byte

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Inspur Corporation

Inspur NF5280M5 (Intel Xeon Gold 6226R)

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Tested by: Inspur Corporation

Test Date: Apr-2020
Hardware Availability: Feb-2020
Software Availability: May-2019

Peak Optimization Flags (Continued)

Benchmarks using both C and C++:

511.povray_r: –prof-gen(pass 1) –prof-use(pass 2) –ipo –xCORE-AVX2 –O3
–no-prec-div –qopt-prefetch –ffinite-math-only
–qopt-mem-layout-trans=4

526.blender_r: –xCORE-AVX2 –ipo –O3 –no-prec-div –qopt-prefetch
–ffinite-math-only –qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:
–xCORE-AVX2 –ipo –O3 –no-prec-div –qopt-prefetch –ffinite-math-only
–qopt-mem-layout-trans=4 –auto –nostandard-realloc-lhs
–align array32byte

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic19.0u1-official-linux64.2019-07-09.html
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V1.8.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.0u1-official-linux64.2019-07-09.xml
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V1.8.xml

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