



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_fp\_base = 167

PowerEdge R6515 (AMD EPYC 7F52, 3.50 GHz)

SPECrate®2017\_fp\_peak = 168

CPU2017 License: 55

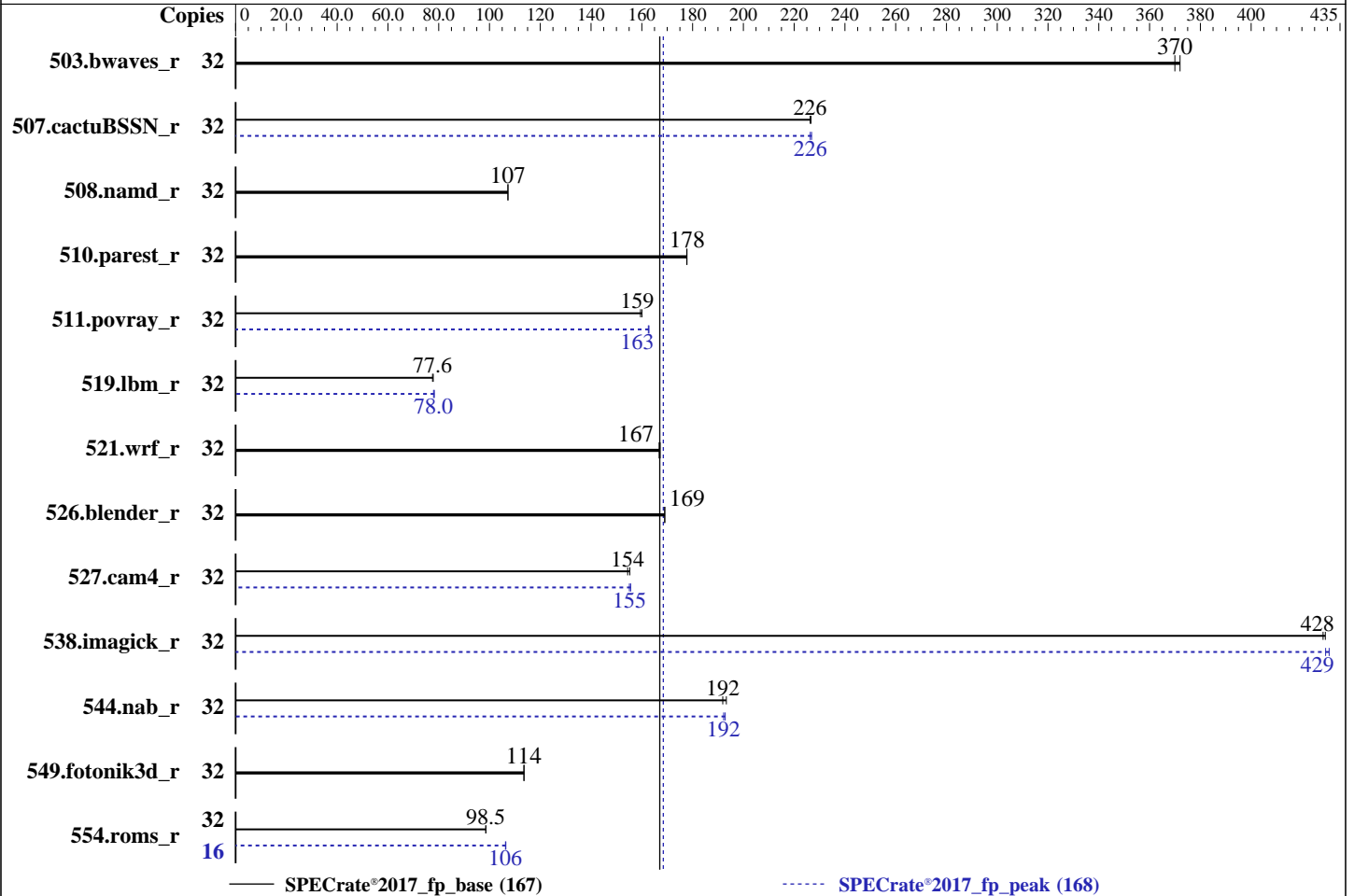
Test Date: Jan-2020

Test Sponsor: Dell Inc.

Hardware Availability: Apr-2020

Tested by: Dell Inc.

Software Availability: Aug-2019



### Hardware

CPU Name: AMD EPYC 7F52  
 Max MHz: 3900  
 Nominal: 3500  
 Enabled: 16 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 512 KB I+D on chip per core  
 L3: 256 MB I+D on chip per chip, 16 MB per core  
 Other: None  
 Memory: 128 GB (8 x 16 GB 2Rx8 PC4-3200AA-R, running at 3200)  
 Storage: 1 x 960 GB SATA SSD  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP1 kernel 4.12.14-195-default  
 Compiler: C/C++/Fortran: Version 2.0.0 of AOCC  
 Parallel: No  
 Firmware: Version 1.2.12 released Dec-2019  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc: jemalloc memory allocator library v5.2.0  
 Power Management: BIOS set to prefer performance at the cost of additional power usage.



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 167

PowerEdge R6515 (AMD EPYC 7F52, 3.50 GHz)

SPECrate®2017\_fp\_peak = 168

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Jan-2020  
Hardware Availability: Apr-2020  
Software Availability: Aug-2019

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	32	863	372	<b>867</b>	<b>370</b>			32	863	372	<b>867</b>	<b>370</b>		
507.cactuBSSN_r	32	<b>179</b>	<b>226</b>	179	227			32	<b>179</b>	<b>226</b>	179	227		
508.namd_r	32	283	107	<b>283</b>	<b>107</b>			32	283	107	<b>283</b>	<b>107</b>		
510.parest_r	32	471	178	<b>471</b>	<b>178</b>			32	471	178	<b>471</b>	<b>178</b>		
511.povray_r	32	467	160	<b>469</b>	<b>159</b>			32	<b>460</b>	<b>163</b>	459	163		
519.lbm_r	32	433	77.8	<b>435</b>	<b>77.6</b>			32	<b>433</b>	<b>78.0</b>	431	78.3		
521.wrf_r	32	429	167	<b>430</b>	<b>167</b>			32	429	167	<b>430</b>	<b>167</b>		
526.blender_r	32	288	169	<b>288</b>	<b>169</b>			32	288	169	<b>288</b>	<b>169</b>		
527.cam4_r	32	<b>362</b>	<b>154</b>	360	155			32	360	156	<b>361</b>	<b>155</b>		
538.imagick_r	32	185	429	<b>186</b>	<b>428</b>			32	<b>185</b>	<b>429</b>	185	431		
544.nab_r	32	279	193	<b>281</b>	<b>192</b>			32	<b>280</b>	<b>192</b>	279	193		
549.fotonik3d_r	32	1097	114	<b>1098</b>	<b>114</b>			32	1097	114	<b>1098</b>	<b>114</b>		
554.roms_r	32	516	98.6	<b>516</b>	<b>98.5</b>			16	<b>239</b>	<b>106</b>	239	106		

SPECrate®2017\_fp\_base = 167

SPECrate®2017\_fp\_peak = 168

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

Set dirty\_ratio=8 to limit dirty cache to 8% of memory  
Set swappiness=1 to swap only if necessary  
Set zone\_reclaim\_mode=1 to free local node memory and avoid remote memory  
sync then drop\_caches=3 to reset caches before invoking runcpu

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 167

PowerEdge R6515 (AMD EPYC 7F52, 3.50 GHz)

SPECrate®2017\_fp\_peak = 168

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Operating System Notes (Continued)

dirty\_ratio, swappiness, zone\_reclaim\_mode and drop\_caches were all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages set to 'always' for this run (OS default)

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH =

"/root/cpu2017-1.1.0/amd\_rate\_aocc200\_rome\_C\_lib/64:/root/cpu2017-1.1.0/amd\_rate\_aocc200\_rome\_C\_lib/32:"

MALLOC\_CONF = "retain:true"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flto  
jemalloc 5.2.0 is available here:

<https://github.com/jemalloc/jemalloc/releases/download/5.2.0/jemalloc-5.2.0.tar.bz2>

## Platform Notes

BIOS settings:

NUMA Nodes Per Socket set to 4

CCX as NUMA Domain set to Enabled

System Profile set to Custom

CPU Power Management set to Maximum Performance

Memory Frequency set to Maximum Performance

Turbo Boost Enabled

Cstates set to Enabled

Memory Patrol Scrub Disabled

Memory Refresh Rate set to 1x

PCI ASPM L1 Link Power Management Disabled

Determinism Slider set to Power Determinism

Efficiency Optimized Mode Disabled

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 167

PowerEdge R6515 (AMD EPYC 7F52, 3.50 GHz)

SPECrate®2017\_fp\_peak = 168

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Platform Notes (Continued)

Memory Interleaving set to Disabled  
Memory Freq set to 3200  
Fan Speed = Maximum

Sysinfo program /root/cpu2017-1.1.0/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011  
running on linux-g3ob Thu Jan 2 17:16:26 2020

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : AMD EPYC 7F52 16-Core Processor  
1 "physical id"s (chips)  
32 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 16  
siblings : 32  
physical 0: cores 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60

From lscpu:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
Address sizes: 43 bits physical, 48 bits virtual  
CPU(s): 32  
On-line CPU(s) list: 0-31  
Thread(s) per core: 2  
Core(s) per socket: 16  
Socket(s): 1  
NUMA node(s): 16  
Vendor ID: AuthenticAMD  
CPU family: 23  
Model: 49  
Model name: AMD EPYC 7F52 16-Core Processor  
Stepping: 0  
CPU MHz: 3493.811  
BogoMIPS: 6987.62  
Virtualization: AMD-V  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 512K  
L3 cache: 16384K  
NUMA node0 CPU(s): 0,16  
NUMA node1 CPU(s): 1,17

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 167

PowerEdge R6515 (AMD EPYC 7F52, 3.50 GHz)

SPECrate®2017\_fp\_peak = 168

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Platform Notes (Continued)

```

NUMA node2 CPU(s): 2,18
NUMA node3 CPU(s): 3,19
NUMA node4 CPU(s): 4,20
NUMA node5 CPU(s): 5,21
NUMA node6 CPU(s): 6,22
NUMA node7 CPU(s): 7,23
NUMA node8 CPU(s): 8,24
NUMA node9 CPU(s): 9,25
NUMA node10 CPU(s): 10,26
NUMA node11 CPU(s): 11,27
NUMA node12 CPU(s): 12,28
NUMA node13 CPU(s): 13,29
NUMA node14 CPU(s): 14,30
NUMA node15 CPU(s): 15,31

```

```

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl xtopology nonstop_tsc cpuid extd_apicid aperfmperf pni
pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx f16c
rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_l2 mwaitx cpb
cat_l3 cdp_l3 hw_pstate sme ssbd sev ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2 smep
bmi2 cqm rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves
cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr arat npt
lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
pfthreshold avic v_vmsave_vmload vgif umip rdpid overflow_recov succor smca

```

```

/proc/cpuinfo cache data
cache size : 512 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 16 nodes (0-15)
node 0 cpus: 0 16
node 0 size: 7485 MB
node 0 free: 7375 MB
node 1 cpus: 1 17
node 1 size: 8063 MB
node 1 free: 7969 MB
node 2 cpus: 2 18
node 2 size: 8063 MB
node 2 free: 7988 MB
node 3 cpus: 3 19
node 3 size: 8062 MB
node 3 free: 7982 MB
node 4 cpus: 4 20
node 4 size: 8063 MB
node 4 free: 7988 MB

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 167

PowerEdge R6515 (AMD EPYC 7F52, 3.50 GHz)

SPECrate®2017\_fp\_peak = 168

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Platform Notes (Continued)

```

node 5 cpus: 5 21
node 5 size: 8063 MB
node 5 free: 7998 MB
node 6 cpus: 6 22
node 6 size: 8063 MB
node 6 free: 7993 MB
node 7 cpus: 7 23
node 7 size: 8062 MB
node 7 free: 7996 MB
node 8 cpus: 8 24
node 8 size: 8063 MB
node 8 free: 8000 MB
node 9 cpus: 9 25
node 9 size: 8063 MB
node 9 free: 8002 MB
node 10 cpus: 10 26
node 10 size: 8063 MB
node 10 free: 7991 MB
node 11 cpus: 11 27
node 11 size: 8062 MB
node 11 free: 7998 MB
node 12 cpus: 12 28
node 12 size: 8034 MB
node 12 free: 7963 MB
node 13 cpus: 13 29
node 13 size: 8063 MB
node 13 free: 7995 MB
node 14 cpus: 14 30
node 14 size: 8063 MB
node 14 free: 7984 MB
node 15 cpus: 15 31
node 15 size: 8050 MB
node 15 free: 7977 MB
node distances:
node  0  1  2  3  4  5  6  7  8  9 10 11 12 13 14 15
0:  10 11 11 11 12 12 12 12 12 12 12 12 12 12 12 12
1:  11 10 11 11 12 12 12 12 12 12 12 12 12 12 12 12
2:  11 11 10 11 12 12 12 12 12 12 12 12 12 12 12 12
3:  11 11 11 10 12 12 12 12 12 12 12 12 12 12 12 12
4:  12 12 12 12 10 11 11 11 12 12 12 12 12 12 12 12
5:  12 12 12 12 11 10 11 11 12 12 12 12 12 12 12 12
6:  12 12 12 12 11 11 10 11 12 12 12 12 12 12 12 12
7:  12 12 12 12 11 11 11 10 12 12 12 12 12 12 12 12
8:  12 12 12 12 12 12 12 12 10 11 11 11 12 12 12 12
9:  12 12 12 12 12 12 12 12 11 10 11 11 12 12 12 12
10: 12 12 12 12 12 12 12 12 11 11 10 11 12 12 12 12
11: 12 12 12 12 12 12 12 12 11 11 11 10 12 12 12 12

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 167

PowerEdge R6515 (AMD EPYC 7F52, 3.50 GHz)

SPECrate®2017\_fp\_peak = 168

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Platform Notes (Continued)

12:	12	12	12	12	12	12	12	12	12	12	12	12	10	11	11	11
13:	12	12	12	12	12	12	12	12	12	12	12	12	11	10	11	11
14:	12	12	12	12	12	12	12	12	12	12	12	12	11	11	10	11
15:	12	12	12	12	12	12	12	12	12	12	12	12	11	11	11	10

From /proc/meminfo

MemTotal: 131472672 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

os-release:

NAME="SLES"

VERSION="15-SP1"

VERSION\_ID="15.1"

PRETTY\_NAME="SUSE Linux Enterprise Server 15 SP1"

ID="sles"

ID\_LIKE="suse"

ANSI\_COLOR="0;32"

CPE\_NAME="cpe:/o:suse:sles:15:sp1"

uname -a:

Linux linux-g3ob 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)

x86\_64 x86\_64 x86\_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault): Not affected

Microarchitectural Data Sampling: Not affected

CVE-2017-5754 (Meltdown): Not affected

CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp

CVE-2017-5753 (Spectre variant 1): Mitigation: \_\_user pointer sanitization

CVE-2017-5715 (Spectre variant 2): Mitigation: Full AMD retpoline, IBPB: conditional, IBRS\_FW, STIBP: conditional, RSB filling

run-level 3 Jan 2 13:20 last=5

SPEC is set to: /root/cpu2017-1.1.0

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	xfs	440G	36G	405G	9%	/

From /sys/devices/virtual/dmi/id

BIOS: Dell Inc. 1.2.12 12/12/2019

Vendor: Dell Inc.

Product: PowerEdge R6515

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 167

PowerEdge R6515 (AMD EPYC 7F52, 3.50 GHz)

SPECrate®2017\_fp\_peak = 168

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Platform Notes (Continued)

Product Family: PowerEdge

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

8x 80AD80B380AD HMA82GR7CJR8N-XN 16 GB 2 rank 3200

8x Not Specified Not Specified

(End of data from sysinfo program)

## Compiler Version Notes

=====  
C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak)  
544.nab\_r(base, peak)

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
-----

=====  
C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)  
-----

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
-----

=====  
C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)  
-----

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu

(Continued on next page)





# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 167

PowerEdge R6515 (AMD EPYC 7F52, 3.50 GHz)

SPECrate®2017\_fp\_peak = 168

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Compiler Version Notes (Continued)

Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)

-----  
AOCCLLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCCLLVM.2.0.0.B191.2019\_07\_19) (based on LLVM AOCCLLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
AOCCLLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCCLLVM.2.0.0.B191.2019\_07\_19) (based on LLVM AOCCLLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
AOCCLLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCCLLVM.2.0.0.B191.2019\_07\_19) (based on LLVM AOCCLLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
-----

=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak)  
| 554.roms\_r(base, peak)

-----  
AOCCLLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCCLLVM.2.0.0.B191.2019\_07\_19) (based on LLVM AOCCLLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
-----

=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)

-----  
AOCCLLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCCLLVM.2.0.0.B191.2019\_07\_19) (based on LLVM AOCCLLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
AOCCLLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCCLLVM.2.0.0.B191.2019\_07\_19) (based on LLVM AOCCLLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 167

PowerEdge R6515 (AMD EPYC 7F52, 3.50 GHz)

SPECrate®2017\_fp\_peak = 168

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Compiler Version Notes (Continued)

InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
526.blender\_r: -funsigned-char -D\_\_BOOL\_DEFINED -DSPEC\_LP64  
527.cam4\_r: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 167

PowerEdge R6515 (AMD EPYC 7F52, 3.50 GHz)

SPECrate®2017\_fp\_peak = 168

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Base Optimization Flags

### C benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -z muldefs -lmvec -lamdlibm -ljemalloc
-lflang
```

### C++ benchmarks:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-mllvm -loop-unswitch-threshold=200000 -mllvm -vector-library=LIBMVEC
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-partial-unswitch -z muldefs -lmvec -lamdlibm
-ljemalloc -lflang
```

### Fortran benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang
```

### Benchmarks using both Fortran and C:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -funroll-loops -Mrecursive -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang
```

### Benchmarks using both C and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 167

PowerEdge R6515 (AMD EPYC 7F52, 3.50 GHz)

SPECrate®2017\_fp\_peak = 168

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):

```
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch -z muldefs
-lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch
-funroll-loops -Mrecursive -z muldefs -Kieee -fno-finite-math-only
-lmvec -lamdlibm -ljemalloc -lflang
```

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 167

PowerEdge R6515 (AMD EPYC 7F52, 3.50 GHz)

SPECrate®2017\_fp\_peak = 168

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver2
-mno-sse4a -fstruct-layout=5 -mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -lmvec -lamdlibm -ljemalloc -lflang
```

C++ benchmarks:

508.namd\_r: basepeak = yes

510.parest\_r: basepeak = yes

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

```
554.roms_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc
-lflang
```

Benchmarks using both Fortran and C:

521.wrf\_r: basepeak = yes

```
527.cam4_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 167

PowerEdge R6515 (AMD EPYC 7F52, 3.50 GHz)

SPECrate®2017\_fp\_peak = 168

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Peak Optimization Flags (Continued)

527.cam4\_r (continued):

```
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -O3 -funroll-loops
-Mrecursive -Kieee -fno-finite-math-only -lmvec
-lamdlibm -ljemalloc -lflang
```

Benchmarks using both C and C++:

511.povray\_r: -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize

```
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000 -lmvec -lamdlibm
-ljemalloc -lflang
```

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver2
-mno-sse4a -fstruct-layout=5 -mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch -mllvm -loop-unswitch-threshold=200000
-O3 -funroll-loops -Mrecursive -Kieee -fno-finite-math-only -lmvec
-lamdlibm -ljemalloc -lflang
```



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 167

PowerEdge R6515 (AMD EPYC 7F52, 3.50 GHz)

SPECrate®2017\_fp\_peak = 168

**CPU2017 License:** 55

**Test Date:** Jan-2020

**Test Sponsor:** Dell Inc.

**Hardware Availability:** Apr-2020

**Tested by:** Dell Inc.

**Software Availability:** Aug-2019

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc200-flags-B1-speed-Dell.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE8.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc200-flags-B1-speed-Dell.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE8.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.0 on 2020-01-02 18:16:25-0500.

Report generated on 2020-04-14 15:19:31 by CPU2017 PDF formatter v6255.

Originally published on 2020-04-14.