SPEC CPU®2017 Integer Speed Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

MicroServer Gen10 Plus
(3.40 GHz, Intel Xeon E-2224)

$SPECspeed^{®}2017\_int\_base =$ 10.2

$SPECspeed^{®}2017\_int\_peak =$ Not Run

CPU2017 License: 3	**Test Date:** Jan-2020
Test Sponsor: HPE	**Hardware Availability:** Apr-2020
Tested by: HPE	**Software Availability:** May-2019

Threads

- 600.perlbench_s 4 — 7.19
- 602.gcc_s 4 — 11.7
- 605.mcf_s 4 — 14.8
- 620.omnetpp_s 4 — 7.05
- 623.xalancbmk_s 4 — 14.3
- 625.x264_s 4 — 17.2
- 631.deepsjeng_s 4 — 6.41
- 641.leela_s 4 — 5.26
- 648.exchange2_s 4 — 18.6
- 657.xz_s 4 — 8.83

—— SPECspeed®2017_int_base (10.2)

Hardware

CPU Name: Intel Xeon E-2224
 Max MHz: 4600
 Nominal: 3400
Enabled: 4 cores, 1 chip
Orderable: 1 chip
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 256 KB I+D on chip per core
 L3: 8 MB I+D on chip per chip
 Other: None
Memory: 32 GB (2 x 16 GB 2Rx8 PC4-2666V-U)
Storage: 1 x 1 TB 7.2 K SATA HDD
Other: None

Software

OS: Red Hat Enterprise Linux release 8.0 (Ootpa)
 Kernel 4.18.0-80.el8.x86_64
Compiler: C/C++: Version 19.0.4.227 of Intel C/C++
 Compiler Build 20190416 for Linux;
 Fortran: Version 19.0.4.227 of Intel Fortran
 Compiler Build 20190416 for Linux
Parallel: Yes
Firmware: HPE BIOS Version U48 2.00 (11/19/2019) released Apr-2020
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of additional power usage

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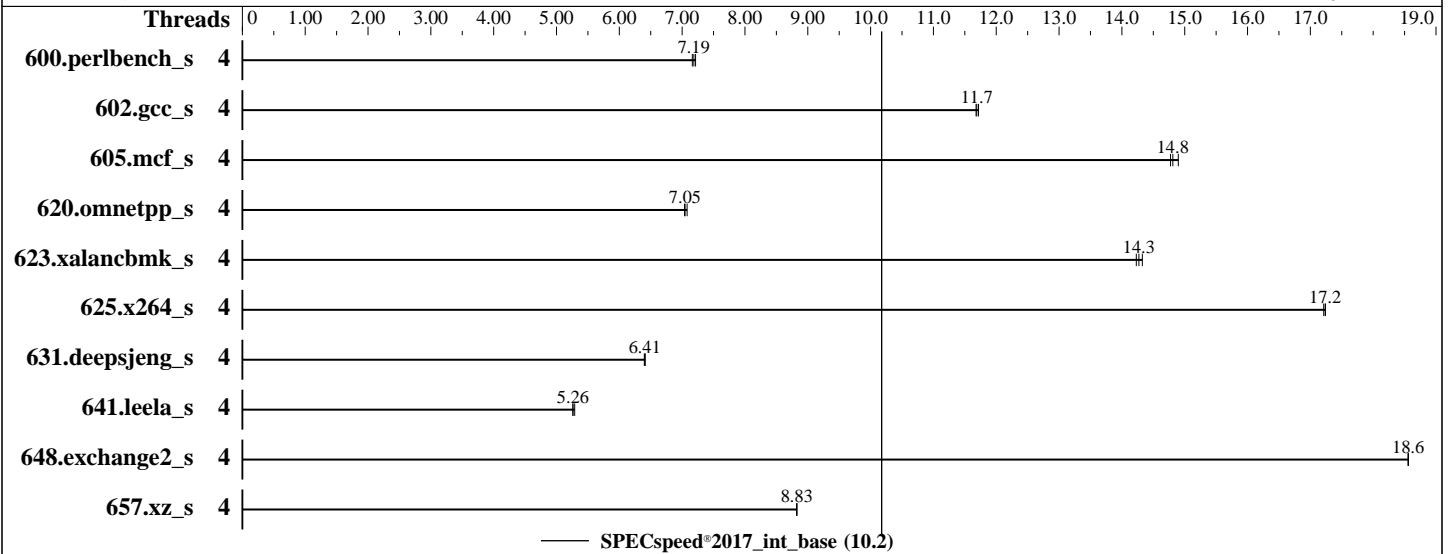
SPECspeed®2017_int_peak = Not Run

CPU2017 License: 3
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Test Date: Jan-2020
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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	4	248	7.16	246	7.21	**247**	**7.19**							
602.gcc_s	4	**341**	**11.7**	340	11.7	341	11.7							
605.mcf_s	4	320	14.8	**319**	**14.8**	317	14.9							
620.omnetpp_s	4	230	7.08	232	7.04	**231**	**7.05**							
623.xalancbmk_s	4	98.9	14.3	**99.3**	**14.3**	99.6	14.2							
625.x264_s	4	102	17.2	**102**	**17.2**	102	17.2							
631.deepsjeng_s	4	224	6.40	223	6.41	**224**	**6.41**							
641.leela_s	4	323	5.29	324	5.26	**324**	**5.26**							
648.exchange2_s	4	158	18.6	158	18.6	**158**	**18.6**							
657.xz_s	4	**700**	**8.83**	701	8.82	700	8.83							
SPECspeed®2017_int_base =		10.2												
SPECspeed®2017_int_peak =		Not Run												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>   /proc/sys/vm/drop_caches

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)

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Standard Performance Evaluation Corporation (info@spec.org) https://www.spec.org/

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CPU2017 License:	3	**Test Date:**	Jan-2020
Test Sponsor:	HPE	**Hardware Availability:**	Apr-2020
Tested by:	HPE	**Software Availability:**	May-2019

General Notes (Continued)

```
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
 jemalloc, a general purpose malloc implementation
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
 sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
 BIOS Configuration:
   Thermal Configuration set to Maximum Cooling

 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011
 running on localhost.localdomain Fri Jan 17 11:56:18 2020

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) E-2224 CPU @ 3.40GHz
       1  "physical id"s (chips)
       4 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 4
       siblings  : 4
       physical 0: cores 0 1 2 3

 From lscpu:
     Architecture:        x86_64
     CPU op-mode(s):      32-bit, 64-bit
     Byte Order:          Little Endian
     CPU(s):              4
     On-line CPU(s) list: 0-3
     Thread(s) per core:  1
     Core(s) per socket:  4
     Socket(s):           1
     NUMA node(s):        1
     Vendor ID:           GenuineIntel
     CPU family:          6
     Model:               158
     Model name:          Intel(R) Xeon(R) E-2224 CPU @ 3.40GHz
```

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Test Sponsor: HPE
Tested by: HPE

Test Date: Jan-2020
Hardware Availability: Apr-2020
Software Availability: May-2019

Platform Notes (Continued)

```
      Stepping:              10
      CPU MHz:               4335.270
      BogoMIPS:              6816.00
      Virtualization:        VT-x
      L1d cache:             32K
      L1i cache:             32K
      L2 cache:              256K
      L3 cache:              8192K
      NUMA node0 CPU(s):     0-3
      Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
   pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
   lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
   aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3
   sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer
   aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb invpcid_single
   pti ssbd ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust
   bmi1 hle avx2 smep bmi2 erms invpcid rtm mpx rdseed adx smap clflushopt intel_pt
   xsaveopt xsavec xgetbv1 xsaves dtherm ida arat pln pts flush_l1d

 /proc/cpuinfo cache data
    cache size : 8192 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
   available: 1 nodes (0)
   node 0 cpus: 0 1 2 3
   node 0 size: 32014 MB
   node 0 free: 31353 MB
   node distances:
   node   0
     0:  10

 From /proc/meminfo
    MemTotal:       32783352 kB
    HugePages_Total:       0
    Hugepagesize:       2048 kB

 From /etc/*release* /etc/*version*
    os-release:
      NAME="Red Hat Enterprise Linux"
      VERSION="8.0 (Ootpa)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="8.0"
      PLATFORM_ID="platform:el8"
      PRETTY_NAME="Red Hat Enterprise Linux 8.0 (Ootpa)"
      ANSI_COLOR="0;31"
```

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$SPECspeed®2017\_int\_base =$ 10.2

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Platform Notes (Continued)

```
    redhat-release: Red Hat Enterprise Linux release 8.0 (Ootpa)
    system-release: Red Hat Enterprise Linux release 8.0 (Ootpa)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:8.0:ga

  uname -a:
    Linux localhost.localdomain 4.18.0-80.el8.x86_64 #1 SMP Wed Mar 13 12:02:46 UTC 2019
    x86_64 x86_64 x86_64 GNU/Linux


  Kernel self-reported vulnerability status:

  CVE-2018-3620 (L1 Terminal Fault):       Mitigation: PTE Inversion; VMX: conditional
                                           cache flushes, SMT disabled
  Microarchitectural Data Sampling:        No status reported
  CVE-2017-5754 (Meltdown):                Mitigation: PTI
  CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
                                           via prctl and seccomp
  CVE-2017-5753 (Spectre variant 1):       Mitigation: __user pointer sanitization
  CVE-2017-5715 (Spectre variant 2):       Mitigation: Full generic retpoline, IBPB:
                                           conditional, IBRS_FW, STIBP: disabled, RSB
                                           filling


  run-level 3 Jan 17 10:58


  SPEC is set to: /home/cpu2017
    Filesystem              Type  Size  Used Avail Use% Mounted on
    /dev/mapper/rhel-home xfs   864G   33G  832G   4% /home

  From /sys/devices/virtual/dmi/id
      BIOS:    HPE U48 11/19/2019
      Vendor:  HPE
      Product: ProLiant MicroServer Gen10 Plus
      Product Family: ProLiant
      Serial:  7CE936P1AD


  Additional information from dmidecode follows.  WARNING: Use caution when you interpret
  this section. The 'dmidecode' program reads system data which is "intended to allow
  hardware to be accurately determined", but the intent may not be met, as there are
  frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
    Memory:
      2x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666


  (End of data from sysinfo program)
```

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Compiler Version Notes

```
================================================================================
C         | 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base)
          | 625.x264_s(base) 657.xz_s(base)
--------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------------


================================================================================
C++       | 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
          | 641.leela_s(base)
--------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------------


================================================================================
Fortran | 648.exchange2_s(base)
--------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`icc -m64 -std=c11`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Base Portability Flags

600.perlbench_s: `-DSPEC_LP64 -DSPEC_LINUX_X64`
602.gcc_s: `-DSPEC_LP64`
605.mcf_s: `-DSPEC_LP64`
620.omnetpp_s: `-DSPEC_LP64`

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Base Portability Flags (Continued)

623.xalancbmk_s: `-DSPEC_LP64 -DSPEC_LINUX`
625.x264_s: `-DSPEC_LP64`
631.deepsjeng_s: `-DSPEC_LP64`
641.leela_s: `-DSPEC_LP64`
648.exchange2_s: `-DSPEC_LP64`
657.xz_s: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

C++ benchmarks:
```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc
```

Fortran benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.html
http://www.spec.org/cpu2017/flags/Intel-ic19.0u1-official-linux64.2019-07-09.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.xml
http://www.spec.org/cpu2017/flags/Intel-ic19.0u1-official-linux64.2019-07-09.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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