



SPEC® CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed2017_int_base = 9.04

H3C UniServer R6900 G3 (Intel Xeon Platinum 8160)

SPECspeed2017_int_peak = Not Run

CPU2017 License: 9066

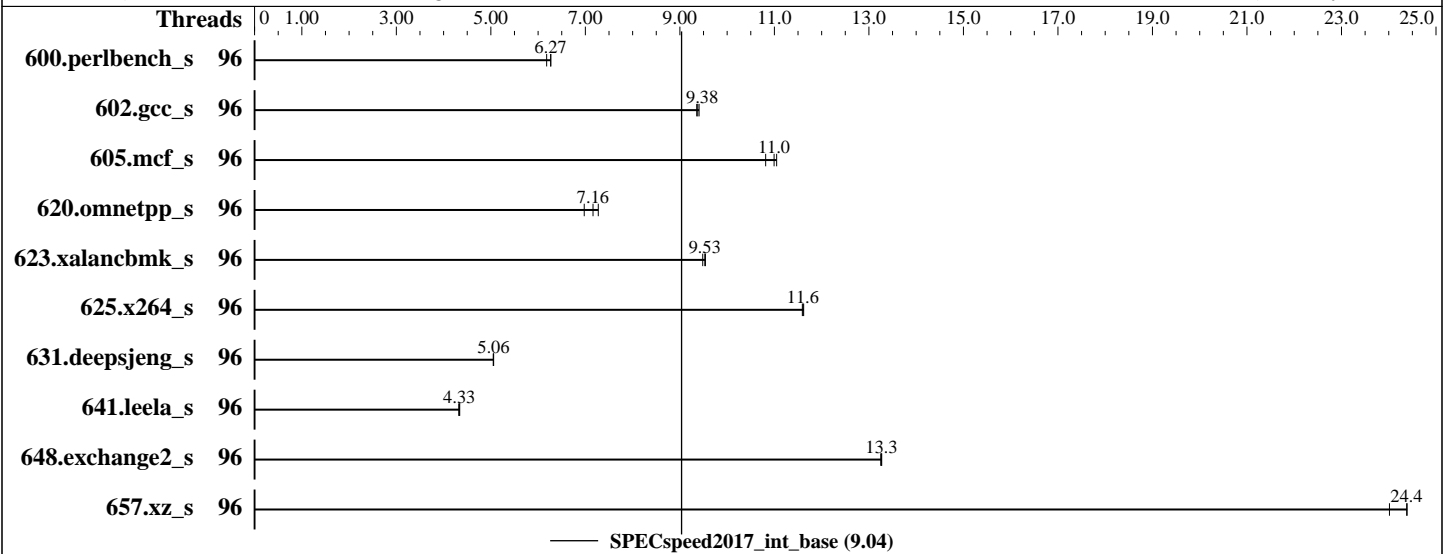
Test Date: Jul-2018

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Sep-2017

Tested by: New H3C Technologies Co., Ltd.

Software Availability: May-2018



Hardware

CPU Name: Intel Xeon Platinum 8160
 Max MHz.: 3700
 Nominal: 2100
 Enabled: 96 cores, 4 chips
 Orderable: 1,2,3,4 chips
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 33 MB I+D on chip per chip
 Other: None
 Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R)
 Storage: 1 x 800 GB SATA SSD
 Other: None

Software

OS: CentOS Linux release 7.5.1804 (Core)
 3.10.0-862.3.2.el7.x86_64
 Compiler: C/C++: Version 18.0.2.199 of Intel C/C++
 Compiler for Linux;
 Fortran: Version 18.0.2.199 of Intel Fortran
 Compiler for Linux
 Parallel: Yes
 Firmware: INSYDE Corp. BIOS Version 1.00.16 released May-2018
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other: jemalloc memory allocator v5.0.1



SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed2017_int_base = 9.04

H3C UniServer R6900 G3 (Intel Xeon Platinum 8160)

SPECspeed2017_int_peak = Not Run

CPU2017 License: 9066

Test Date: Jul-2018

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Sep-2017

Tested by: New H3C Technologies Co., Ltd.

Software Availability: May-2018

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	96	283	6.27	287	6.18	283	6.27							
602.gcc_s	96	425	9.38	426	9.36	423	9.41							
605.mcf_s	96	427	11.0	437	10.8	429	11.0							
620.omnetpp_s	96	234	6.98	228	7.16	224	7.27							
623.xalancbmk_s	96	149	9.48	149	9.53	149	9.54							
625.x264_s	96	152	11.6	152	11.6	152	11.6							
631.deepsjeng_s	96	284	5.05	283	5.06	283	5.06							
641.leela_s	96	394	4.33	395	4.32	393	4.34							
648.exchange2_s	96	222	13.3	222	13.2	222	13.3							
657.xz_s	96	254	24.4	253	24.4	257	24.0							

SPECspeed2017_int_base = 9.04

SPECspeed2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/home/speccpu/lib/ia32:/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-32:/home/speccpu/je5.0.1-64"

OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed2017_int_base = 9.04

H3C UniServer R6900 G3 (Intel Xeon Platinum 8160)

SPECspeed2017_int_peak = Not Run

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Jul-2018

Hardware Availability: Sep-2017

Software Availability: May-2018

Platform Notes

BIOS configuration:

Set Sub NUMA Cluster to Disabled
Set IMC Interleaving to Auto
Set Power Supply Mode to Performance
Set Workload Configuration to NUMA
Set Hyper Threading to Disabled
Set ClE to Disabled
Set Patrol Scrub to Disabled

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on localhost.localdomain Mon Jul 9 22:16:18 2018

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Platinum 8160 CPU @ 2.10GHz

4 "physical id"s (chips)

96 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 24

siblings : 24

physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

From lscpu:

Architecture: x86_64

CPU op-mode(s): 32-bit, 64-bit

Byte Order: Little Endian

CPU(s): 96

On-line CPU(s) list: 0-95

Thread(s) per core: 1

Core(s) per socket: 24

Socket(s): 4

NUMA node(s): 4

Vendor ID: GenuineIntel

CPU family: 6

Model: 85

Model name: Intel(R) Xeon(R) Platinum 8160 CPU @ 2.10GHz

Stepping: 4

CPU MHz: 3143.463

CPU max MHz: 3700.0000

CPU min MHz: 1000.0000

(Continued on next page)



SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed2017_int_base = 9.04

H3C UniServer R6900 G3 (Intel Xeon Platinum 8160)

SPECspeed2017_int_peak = Not Run

CPU2017 License: 9066

Test Date: Jul-2018

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Sep-2017

Tested by: New H3C Technologies Co., Ltd.

Software Availability: May-2018

Platform Notes (Continued)

BogoMIPS: 4200.00
 Virtualization: VT-x
 L1d cache: 32K
 L1i cache: 32K
 L2 cache: 1024K
 L3 cache: 33792K
 NUMA node0 CPU(s): 0-23
 NUMA node1 CPU(s): 24-47
 NUMA node2 CPU(s): 48-71
 NUMA node3 CPU(s): 72-95

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
 pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
 lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
 aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
 fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
 xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 intel_ppin
 intel_pt mba tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2
 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt
 clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc
 cqm_mbm_total cqm_mbm_local ibpb ibrs stibp dtherm ida arat pln pts hwp
 hwp_act_window hwp_epp hwp_pkg_req pku ospke spec_ctrl intel_stibp

```

/proc/cpuinfo cache data
cache size : 33792 KB
  
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
node 0 size: 194957 MB
node 0 free: 189422 MB
node 1 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47
node 1 size: 196608 MB
node 1 free: 191717 MB
node 2 cpus: 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71
node 2 size: 196608 MB
node 2 free: 191513 MB
node 3 cpus: 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95
node 3 size: 196608 MB
node 3 free: 191745 MB
node distances:
node  0  1  2  3
0:  10  21  21  21
1:  21  10  21  21
2:  21  21  10  21
3:  21  21  21  10
  
```

(Continued on next page)



SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed2017_int_base = 9.04

H3C UniServer R6900 G3 (Intel Xeon Platinum 8160)

SPECspeed2017_int_peak = Not Run

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Jul-2018

Hardware Availability: Sep-2017

Software Availability: May-2018

Platform Notes (Continued)

From /proc/meminfo

MemTotal: 790695820 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb_release -d

CentOS Linux release 7.5.1804 (Core)

From /etc/*release* /etc/*version*

centos-release: CentOS Linux release 7.5.1804 (Core)

centos-release-upstream: Derived from Red Hat Enterprise Linux 7.5 (Source)

os-release:

NAME="CentOS Linux"

VERSION="7 (Core)"

ID="centos"

ID_LIKE="rhel fedora"

VERSION_ID="7"

PRETTY_NAME="CentOS Linux 7 (Core)"

ANSI_COLOR="0;31"

CPE_NAME="cpe:/o:centos:centos:7"

redhat-release: CentOS Linux release 7.5.1804 (Core)

system-release: CentOS Linux release 7.5.1804 (Core)

system-release-cpe: cpe:/o:centos:centos:7

uname -a:

Linux localhost.localdomain 3.10.0-862.3.2.el7.x86_64 #1 SMP Mon May 21 23:36:36 UTC 2018 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 9 18:55

SPEC is set to: /home/speccpu

Filesystem Type Size Used Avail Use% Mounted on

/dev/mapper/centos-home xfs 690G 9.8G 680G 2% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 1.00.16P00 05/30/2018

Memory:

24x Micron 36ASF4G72PZ-2G6D1 32 GB 2 rank 2666

24x NO DIMM NO DIMM

(End of data from sysinfo program)



SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed2017_int_base = 9.04

H3C UniServer R6900 G3 (Intel Xeon Platinum 8160)

SPECspeed2017_int_peak = Not Run

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Jul-2018

Hardware Availability: Sep-2017

Software Availability: May-2018

Compiler Version Notes

=====
CC 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base)
657.xz_s(base)

icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====
CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
641.leela_s(base)

icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====
FC 648.exchange2_s(base)

ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64

(Continued on next page)



SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed2017_int_base = 9.04

H3C UniServer R6900 G3 (Intel Xeon Platinum 8160)

SPECspeed2017_int_peak = Not Run

CPU2017 License: 9066

Test Date: Jul-2018

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Sep-2017

Tested by: New H3C Technologies Co., Ltd.

Software Availability: May-2018

Base Portability Flags (Continued)

641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-L/usr/local/je5.0.1-64/lib -ljemalloc

Base Other Flags

C benchmarks:

-m64 -std=c11

C++ benchmarks:

-m64

Fortran benchmarks:

-m64

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html>

http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.3-SKL-RevC.html

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml>

http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.3-SKL-RevC.xml



SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

SPECspeed2017_int_base = 9.04

H3C UniServer R6900 G3 (Intel Xeon Platinum 8160)

SPECspeed2017_int_peak = Not Run

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Jul-2018

Hardware Availability: Sep-2017

Software Availability: May-2018

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-07-09 10:16:17-0400.

Report generated on 2018-10-31 18:24:36 by CPU2017 PDF formatter v6067.

Originally published on 2018-08-07.