



SPEC® CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECspeed2017_int_base = 9.32

Huawei XH321 V5 (Intel Xeon Platinum 8176)

SPECspeed2017_int_peak = 9.62

CPU2017 License: 3175

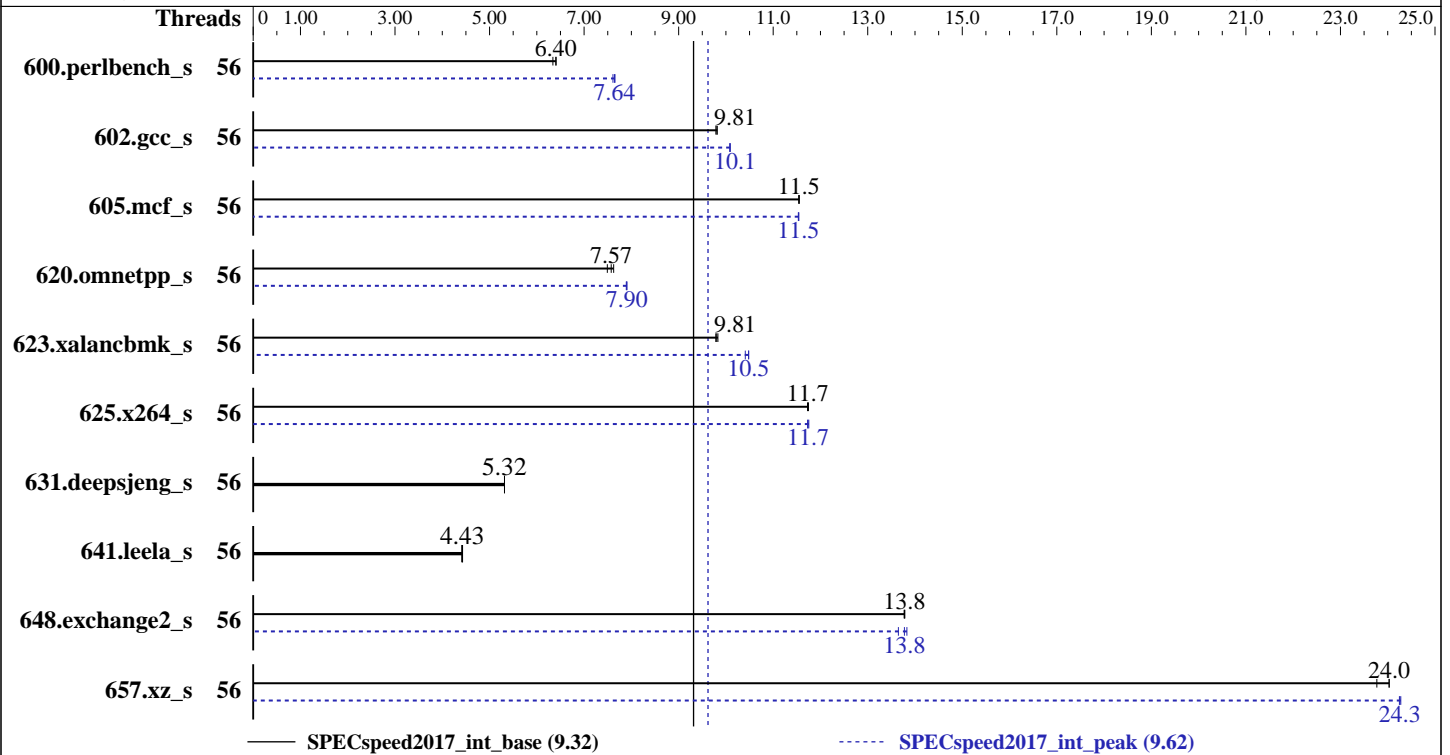
Test Sponsor: Huawei

Tested by: Huawei

Test Date: May-2018

Hardware Availability: Jul-2017

Software Availability: Jan-2018



Hardware

CPU Name: Intel Xeon Platinum 8176
 Max MHz.: 3800
 Nominal: 2100
 Enabled: 56 cores, 2 chips
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 38.5 MB I+D on chip per chip
 Other: None
 Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)
 Storage: 1 x 1200 GB SAS, 10000 RPM
 Other: None

Software

OS: Red Hat Enterprise Linux Server release 7.3 (Maipo)
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;
 Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
 Parallel: Yes
 Firmware: Version 0.59 Released Feb-2018
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other: jemalloc: jemalloc memory allocator library V5.0.1;



SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECspeed2017_int_base = 9.32

Huawei XH321 V5 (Intel Xeon Platinum 8176)

SPECspeed2017_int_peak = 9.62

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: May-2018
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	56	280	6.34	<u>277</u>	<u>6.40</u>	277	6.41	56	232	7.65	233	7.61	<u>232</u>	<u>7.64</u>
602.gcc_s	56	407	9.79	<u>406</u>	<u>9.81</u>	406	9.81	56	395	10.1	395	10.1	<u>395</u>	<u>10.1</u>
605.mcf_s	56	<u>409</u>	<u>11.5</u>	409	11.5	409	11.6	56	409	11.5	<u>409</u>	<u>11.5</u>	409	11.5
620.omnetpp_s	56	218	7.49	<u>215</u>	<u>7.57</u>	214	7.63	56	206	7.91	207	7.89	<u>206</u>	<u>7.90</u>
623.xalancbmk_s	56	144	9.83	<u>145</u>	<u>9.81</u>	145	9.79	56	135	10.5	136	10.4	<u>135</u>	<u>10.5</u>
625.x264_s	56	<u>150</u>	<u>11.7</u>	150	11.7	150	11.7	56	150	11.7	150	11.8	<u>150</u>	<u>11.7</u>
631.deepsjeng_s	56	<u>270</u>	<u>5.32</u>	270	5.32	270	5.32	56	<u>270</u>	<u>5.32</u>	270	5.32	270	5.32
641.leela_s	56	387	4.41	<u>385</u>	<u>4.43</u>	385	4.43	56	387	4.41	<u>385</u>	<u>4.43</u>	385	4.43
648.exchange2_s	56	<u>213</u>	<u>13.8</u>	214	13.8	213	13.8	56	<u>213</u>	<u>13.8</u>	215	13.6	213	13.8
657.xz_s	56	257	24.0	<u>257</u>	<u>24.0</u>	260	23.8	56	255	24.2	255	24.3	<u>255</u>	<u>24.3</u>

SPECspeed2017_int_base = 9.32

SPECspeed2017_int_peak = 9.62

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/spec/lib/ia32:/spec/lib/intel64:/spec/je5.0.1-32:/spec/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4

Transparent Huge Pages enabled by default
Prior to runcpu invocation

Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
jemalloc: configured and built at default for
32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4,
and the system compiler gcc 4.8.5;
jemalloc: sources available from jemalloc.net or
<https://github.com/jemalloc/jemalloc/releases>

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.



SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECspeed2017_int_base = 9.32

Huawei XH321 V5 (Intel Xeon Platinum 8176)

SPECspeed2017_int_peak = 9.62

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: May-2018

Hardware Availability: Jul-2017

Software Availability: Jan-2018

Platform Notes

BIOS configuration:

Power Efficiency Mode Set to Custom

Hyper-Threading Set to Disable

ADDDC Sparing Set to Disabled

Sysinfo program /spec/bin/sysinfo

Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f

running on localhost.localdomain Sat May 19 09:13:08 2018

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Platinum 8176 CPU @ 2.10GHz

2 "physical id"s (chips)

56 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 28

siblings : 28

physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30

physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30

From lscpu:

Architecture: x86_64

CPU op-mode(s): 32-bit, 64-bit

Byte Order: Little Endian

CPU(s): 56

On-line CPU(s) list: 0-55

Thread(s) per core: 1

Core(s) per socket: 28

Socket(s): 2

NUMA node(s): 2

Vendor ID: GenuineIntel

CPU family: 6

Model: 85

Model name: Intel(R) Xeon(R) Platinum 8176 CPU @ 2.10GHz

Stepping: 4

CPU MHz: 2101.000

BogoMIPS: 4204.87

Virtualization: VT-x

L1d cache: 32K

L1i cache: 32K

L2 cache: 1024K

L3 cache: 39424K

(Continued on next page)



SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECspeed2017_int_base = 9.32

Huawei XH321 V5 (Intel Xeon Platinum 8176)

SPECspeed2017_int_peak = 9.62

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: May-2018

Hardware Availability: Jul-2017

Software Availability: Jan-2018

Platform Notes (Continued)

NUMA node0 CPU(s): 0-27
NUMA node1 CPU(s): 28-55

/proc/cpuinfo cache data
cache size : 39424 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
node 0 size: 194741 MB
node 0 free: 189351 MB
node 1 cpus: 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55
node 1 size: 196608 MB
node 1 free: 192108 MB
node distances:
node  0  1
  0: 10  21
  1: 21  10
```

From /proc/meminfo
MemTotal: 394174996 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server
```

```
uname -a:
Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST
2017 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 May 19 09:12

SPEC is set to: /spec

(Continued on next page)



SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECspeed2017_int_base = 9.32

Huawei XH321 V5 (Intel Xeon Platinum 8176)

SPECspeed2017_int_peak = 9.62

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: May-2018
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Platform Notes (Continued)

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda8	xfs	325G	27G	299G	9%	/

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.59 02/24/2018

Memory:

4x NO DIMM NO DIMM

12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

```
=====
CC 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base,
   peak) 657.xz_s(base)
-----
```

icc (ICC) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

```
=====
CC 600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 657.xz_s(peak)
-----
```

icc (ICC) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

```
=====
CXXC 620.omnetpp_s(base) 623.xalanbmk_s(base) 631.deepsjeng_s(base)
     641.leela_s(base)
-----
```

icpc (ICC) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

```
=====
CXXC 620.omnetpp_s(peak) 623.xalanbmk_s(peak) 631.deepsjeng_s(peak)
     641.leela_s(peak)
-----
```

icpc (ICC) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

(Continued on next page)



SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECspeed2017_int_base = 9.32

Huawei XH321 V5 (Intel Xeon Platinum 8176)

SPECspeed2017_int_peak = 9.62

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: May-2018
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Compiler Version Notes (Continued)

=====
FC 648.exchange2_s(base, peak)
=====

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
=====

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div

(Continued on next page)



SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECspeed2017_int_base = 9.32

Huawei XH321 V5 (Intel Xeon Platinum 8176)

SPECspeed2017_int_peak = 9.62

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: May-2018

Hardware Availability: Jul-2017

Software Availability: Jan-2018

Base Optimization Flags (Continued)

C++ benchmarks (continued):

`-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

Fortran benchmarks:

`-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div`

`-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`

`-L/usr/local/je5.0.1-64/lib -ljemalloc`

Base Other Flags

C benchmarks:

`-m64 -std=c11`

C++ benchmarks:

`-m64`

Fortran benchmarks:

`-m64`

Peak Compiler Invocation

C benchmarks:

`icc`

C++ benchmarks:

`icpc`

Fortran benchmarks:

`ifort`

Peak Portability Flags

600.perlbench_s: `-DSPEC_LP64 -DSPEC_LINUX_X64`

602.gcc_s: `-DSPEC_LP64`

605.mcf_s: `-DSPEC_LP64`

620.omnetpp_s: `-DSPEC_LP64`

623.xalancbmk_s: `-D_FILE_OFFSET_BITS=64 -DSPEC_LINUX`

625.x264_s: `-DSPEC_LP64`

631.deepsjeng_s: `-DSPEC_LP64`

(Continued on next page)



SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECspeed2017_int_base = 9.32

Huawei XH321 V5 (Intel Xeon Platinum 8176)

SPECspeed2017_int_peak = 9.62

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: May-2018

Hardware Availability: Jul-2017

Software Availability: Jan-2018

Peak Portability Flags (Continued)

641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-mem-layout-trans=3 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-mem-layout-trans=3 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: Same as 602.gcc_s

C++ benchmarks:

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

623.xalancbmk_s: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-32/lib -ljemalloc

(Continued on next page)



SPEC CPU2017 Integer Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECspeed2017_int_base = 9.32

Huawei XH321 V5 (Intel Xeon Platinum 8176)

SPECspeed2017_int_peak = 9.62

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: May-2018

Hardware Availability: Jul-2017

Software Availability: Jan-2018

Peak Optimization Flags (Continued)

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

Peak Other Flags

C benchmarks:

```
-m64 -std=c11
```

C++ benchmarks (except as noted below):

```
-m64
```

623.xalancbmk_s: -m32

Fortran benchmarks:

```
-m64
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-05-19 09:13:08-0400.

Report generated on 2018-10-31 18:01:01 by CPU2017 PDF formatter v6067.

Originally published on 2018-06-12.