



# SPEC® CINT2006 Result

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## Fujitsu

PRIMERGY TX2550 M4, Intel Xeon Platinum 8164  
2.00GHz

SPECint®\_rate2006 = Not Run

SPECint\_rate\_base2006 = 2350

CPU2006 license: 19

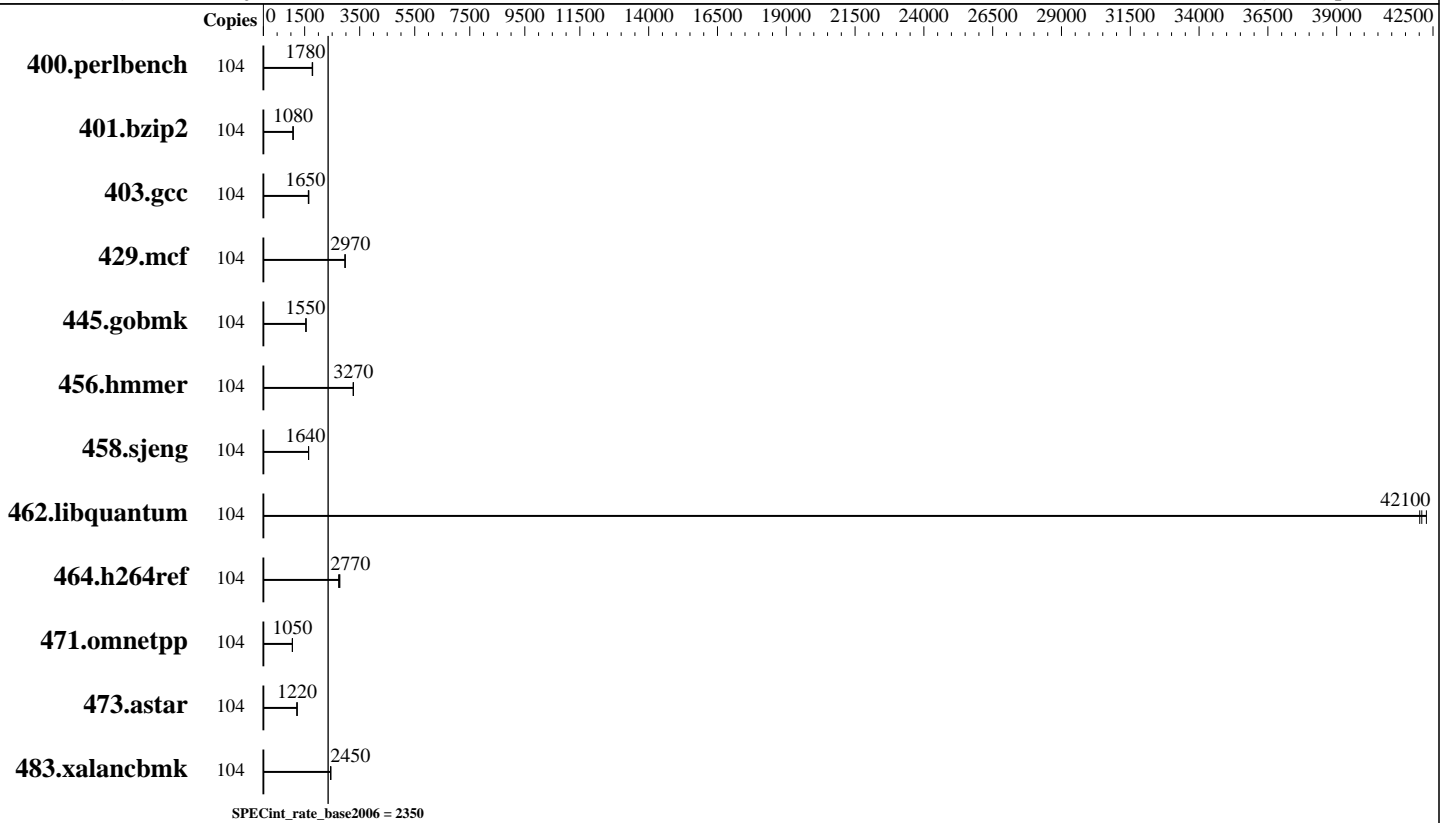
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Dec-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017



### Hardware

CPU Name: Intel Xeon Platinum 8164  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 52 cores, 2 chips, 26 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 35.75 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)  
 Disk Subsystem: 1 x SATA HDD, 1000 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 12 SP2  
 4.4.21-69-default  
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++  
 Compiler for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	104	<b>571</b>	<b>1780</b>	567	1790	571	1780							
401.bzip2	104	932	1080	927	1080	<b>930</b>	<b>1080</b>							
403.gcc	104	508	1650	513	1630	<b>509</b>	<b>1650</b>							
429.mcf	104	319	2970	<b>319</b>	<b>2970</b>	320	2970							
445.gobmk	104	704	1550	<b>705</b>	<b>1550</b>	705	1550							
456.hammer	104	297	3270	<b>297</b>	<b>3270</b>	297	3270							
458.sjeng	104	<b>766</b>	<b>1640</b>	767	1640	766	1640							
462.libquantum	104	51.3	42000	<b>51.2</b>	<b>42100</b>	51.0	42300							
464.h264ref	104	842	2730	829	2780	<b>831</b>	<b>2770</b>							
471.omnetpp	104	<b>616</b>	<b>1050</b>	616	1060	617	1050							
473.astar	104	597	1220	597	1220	<b>597</b>	<b>1220</b>							
483.xalancbmk	104	293	2450	<b>293</b>	<b>2450</b>	294	2440							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Set Kernel Boot Parameter : nohz_full=1-103
Set CPU frequency governor to maximum performance with:
cpupower -c all frequency-set -g performance
Process tuning settings:
echo 10000000 > /proc/sys/kernel/sched_min_granularity_ns
echo 15000000 > /proc/sys/kernel/sched_wakeup_granularity_ns
cpu idle state set with:
cpupower idle-set -d 1
cpupower idle-set -d 2
```

## Platform Notes

```
BIOS configuration:
HWPM Support = Disabled
Intel Virtualization Technology = Disabled
Link Frequency Select = 10.4 GT/s
Sub NUMA Clustering = Enabled
IMC Interleaving = 1-way
LLC Dead Line Alloc = Disabled
Stale AtoS = Enabled
```

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### Platform Notes (Continued)

Fan Control = Full  
Sysinfo program /home/Benchmark/speccpu-icc18-20171020/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on TX2550M4 Tue Dec 19 17:40:16 2017

This section contains SUT (System Under Test) info as seen by  
some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) Platinum 8164 CPU @ 2.00GHZ
 2 "physical id"s (chips)
 104 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores      : 26
siblings       : 52
physical 0:    cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25
                26 27 28 29
physical 1:    cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25
                26 27 28 29
cache size     : 36608 KB
```

```
From /proc/meminfo
MemTotal:      394412136 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

```
uname -a:
Linux TX2550M4 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux
Continued on next page
```



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### Platform Notes (Continued)

run-level 3 Dec 19 17:36

SPEC is set to: /home/Benchmark/speccpu-icc18-20171020  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda4 xfs 889G 44G 846G 5% /home  
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS FUJITSU // American Megatrends Inc. V5.0.0.12 R1.13.0 for D3386-A1x  
11/02/2017  
Memory:  
12x Micron 36ASF4G72PZ-2G6D1 32 GB 2 rank 2666 MHz

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/Benchmark/speccpu-icc18-20171020/icc2018lib/ia32"  
LD\_LIBRARY\_PATH = "\$LD\_LIBRARY\_PATH:/home/Benchmark/speccpu-icc18-20171020/icc2018lib/intel64"  
LD\_LIBRARY\_PATH = "\$LD\_LIBRARY\_PATH:/home/Benchmark/speccpu-icc18-20171020/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

### Base Compiler Invocation

C benchmarks:  
icc -m32 -L/opt/intel/compilers\_and\_libraries\_2018.0.128/linux/compiler/lib/ia32  
C++ benchmarks:  
icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2018.0.128/linux/compiler/lib/ia32



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## Base Portability Flags

```
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3

C++ benchmarks:  
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>  
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-SKL-RevD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>  
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-SKL-RevD.xml>



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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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