



# SPEC<sup>®</sup> CFP2006 Result

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**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

Synergy 480 Gen10  
(2.00 GHz, Intel Xeon Gold 6138)

**SPECfp<sup>®</sup>\_rate2006 = Not Run**

**SPECfp\_rate\_base2006 = 1340**

**CPU2006 license:** 3

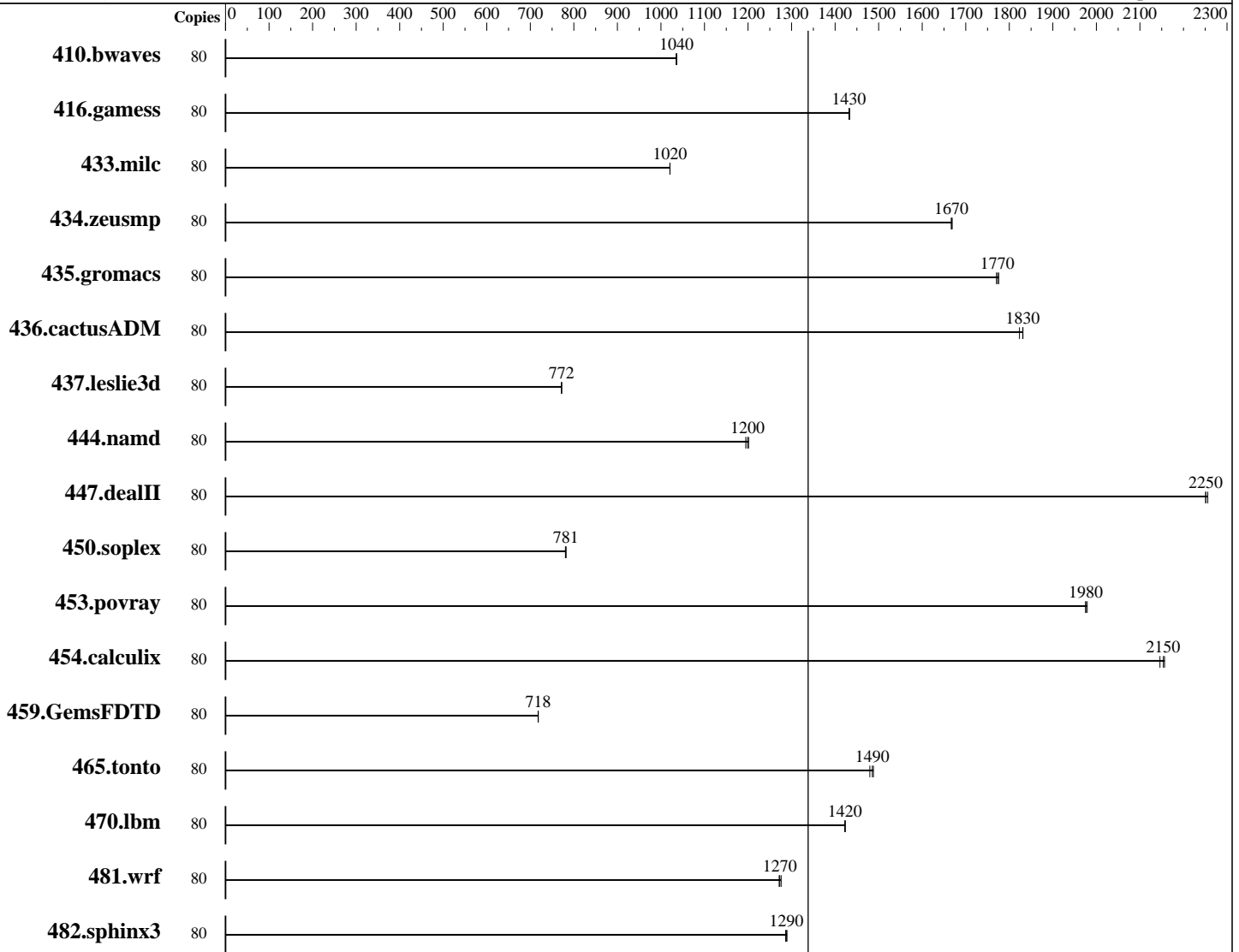
**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Nov-2017

**Hardware Availability:** Oct-2017

**Software Availability:** Apr-2017



SPECfp\_rate\_base2006 = 1340

### Hardware

CPU Name: Intel Xeon Gold 6138  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 40 cores, 2 chips, 20 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chip(s)  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 (x86\_64) SP2  
 Kernel 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 27.5 MB I+D on chip per chip  
Other Cache: None  
Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)  
Disk Subsystem: 1 x 960 GB SATA SSD, RAID 0  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: Not Applicable  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	80	1049	1040	1051	1030	<u>1049</u>	<u>1040</u>							
416.gamess	80	1094	1430	1093	1430	<u>1094</u>	<u>1430</u>							
433.milc	80	720	1020	<u>719</u>	<u>1020</u>	719	1020							
434.zeusmp	80	<u>437</u>	<u>1670</u>	436	1670	437	1670							
435.gromacs	80	<u>322</u>	<u>1770</u>	323	1770	322	1780							
436.cactusADM	80	524	1820	522	1830	<u>522</u>	<u>1830</u>							
437.leslie3d	80	<u>974</u>	<u>772</u>	975	772	974	772							
444.namd	80	<u>535</u>	<u>1200</u>	537	1190	534	1200							
447.dealII	80	<u>407</u>	<u>2250</u>	406	2260	407	2250							
450.soplex	80	855	781	<u>854</u>	<u>781</u>	853	782							
453.povray	80	215	1980	<u>215</u>	<u>1980</u>	215	1980							
454.calculix	80	<u>306</u>	<u>2150</u>	306	2160	308	2150							
459.GemsFDTD	80	1182	718	<u>1182</u>	<u>718</u>	1182	718							
465.tonto	80	<u>530</u>	<u>1490</u>	529	1490	532	1480							
470.lbm	80	773	1420	<u>773</u>	<u>1420</u>	772	1420							
481.wrf	80	<u>702</u>	<u>1270</u>	700	1280	703	1270							
482.sphinx3	80	1212	1290	1209	1290	<u>1211</u>	<u>1290</u>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Filesystem page cache cleared with:  
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
irqbalance disabled with "service irqbalance stop"  
tuned profile set with "tuned-adm profile throughput-performance"  
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## Operating System Notes (Continued)

VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty\_ratio"  
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa\_balancing"

## Platform Notes

### BIOS Configuration:

Thermal Configuration set to Maximum Cooling  
LLC Prefetch set to Enabled  
LLC Dead Line Allocation set to Disabled  
Memory Patrol Scrubbing set to Disabled  
Workload Profile set to General Throughput Compute  
Minimum Processor Idle Power Core C-State set to C1E State  
Sysinfo program /home/cpu2006/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on linux-vjuj Tue Nov 21 17:22:39 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

### From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) Gold 6138 CPU @ 2.00GHz
 2 "physical id"s (chips)
 80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores    : 20
  siblings    : 40
  physical 0: cores 0 1 2 3 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
  physical 1: cores 0 1 2 3 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
cache size     : 28160 KB
```

### From /proc/meminfo

```
MemTotal:      395921384 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

### /usr/bin/lsb\_release -d

```
SUSE Linux Enterprise Server 12 SP2
```

### From /etc/\*release\* /etc/\*version\*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
```

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## Platform Notes (Continued)

```
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

```
uname -a:
Linux linux-vjuj 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 21 11:14
```

```
SPEC is set to: /home/cpu2006
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda4        xfs       852G      41G  812G   5% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE I42 09/27/2017

Memory:

24x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

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## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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