



SPEC[®] CFP2006 Result

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Sugon

SPECfp[®]_rate2006 = Not Run

Sugon A620-G30 (AMD EPYC 7601)

SPECfp_rate_base2006 = 1800

CPU2006 license: 9046

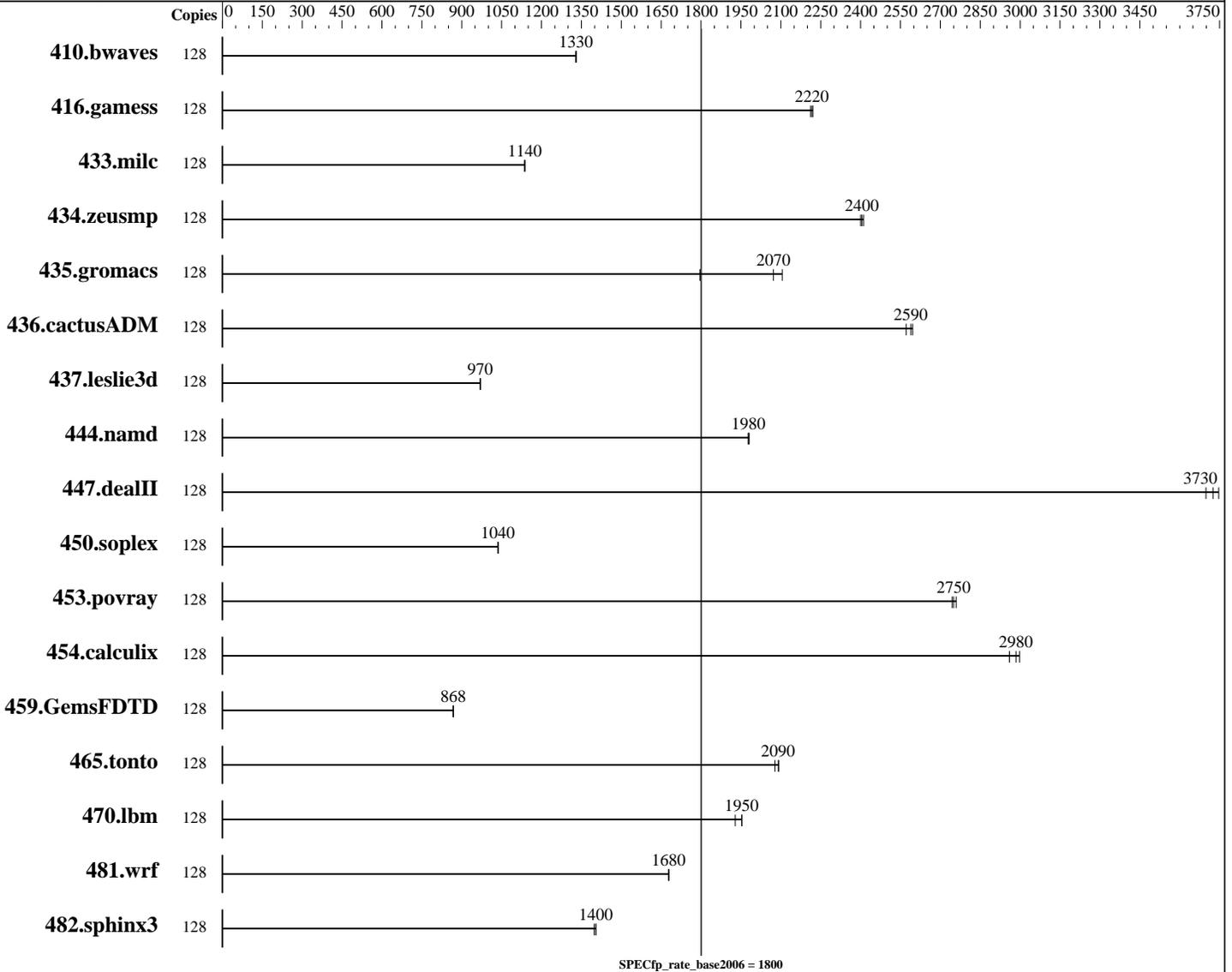
Test sponsor: Sugon

Tested by: Sugon

Test date: Dec-2017

Hardware Availability: Dec-2017

Software Availability: Oct-2017



Hardware

CPU Name: AMD EPYC 7601
 CPU Characteristics: AMD Turbo CORE technology up to 3.20 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 64 cores, 2 chips, 32 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 64 KB I + 32 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 12 SP3
 Kernel 4.4.73-5-default
 Compiler: C/C++/Fortran: Version 4.5.2.1 of x86 Open64 Compiler Suite (from AMD)
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (Multi User)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit

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L3 Cache: 64 MB I+D on chip per chip, 8 MB shared / 4 cores
Other Cache: None
Memory: 1 TB (16 x 64 GB 4Rx4 PC4-2667V-L)
Disk Subsystem: 1 x 800 GB SATA, SSD
Other Hardware: None

Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	128	1307	1330	<u>1308</u>	<u>1330</u>	1309	1330							
416.gamess	128	1133	2210	<u>1130</u>	<u>2220</u>	1129	2220							
433.milc	128	1033	1140	<u>1033</u>	<u>1140</u>	1034	1140							
434.zeusmp	128	<u>484</u>	<u>2400</u>	485	2400	483	2410							
435.gromacs	128	434	2110	<u>441</u>	<u>2070</u>	509	1800							
436.cactusADM	128	589	2600	595	2570	<u>591</u>	<u>2590</u>							
437.leslie3d	128	1241	970	1241	969	<u>1241</u>	<u>970</u>							
444.namd	128	519	1980	518	1980	<u>519</u>	<u>1980</u>							
447.dealII	128	396	3700	<u>393</u>	<u>3730</u>	391	3750							
450.soplex	128	1029	1040	1031	1040	<u>1030</u>	<u>1040</u>							
453.povray	128	<u>248</u>	<u>2750</u>	248	2740	247	2760							
454.calculix	128	352	3000	<u>354</u>	<u>2980</u>	357	2960							
459.GemsFDTD	128	1565	868	<u>1564</u>	<u>868</u>	1564	868							
465.tonto	128	606	2080	602	2090	<u>603</u>	<u>2090</u>							
470.lbm	128	<u>901</u>	<u>1950</u>	900	1950	912	1930							
481.wrf	128	852	1680	<u>852</u>	<u>1680</u>	852	1680							
482.sphinx3	128	<u>1777</u>	<u>1400</u>	1775	1410	1784	1400							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary

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Operating System Notes (Continued)

Set zone_reclaim_mode=1 to free local node memory and avoid remote memory sync then drop_caches=3 to reset caches before invoking runcpu

Transparent huge pages were enabled for this run (OS default)

Set vm/nr_hugepages=114688 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages

Platform Notes

BIOS settings:

Determinism Slider = Power

cTDP Control = Manual

cTDP = 200

This system Sugon A620-G30 is electrically equal with Sugon A420-G30 populated with the same processors and memories.

General Notes

Environment variables set by runspec before the start of the run:

HUGETLB_LIMIT = "896"

LD_LIBRARY_PATH = "/home/cpu2006/amd1603-rate-libs-revB/32:/home/cpu2006/amd1603-rate-libs-revB/64"

The binaries were built with the AMD supported x86 Open64 Compiler Suite, which is only available from AMD at

<http://developer.amd.com/tools-and-sdks/cpu-development/x86-open64-compiler-suite/>

Binaries were compiled on a system with 2 x AMD Opteron 6378 chips + 128 GB Memory using RHEL 6.3

Base Compiler Invocation

C benchmarks:

opencc

C++ benchmarks:

openCC

Fortran benchmarks:

openf95

Benchmarks using both Fortran and C:

opencc openf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64
      -fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000
-IPA:small_pu=100 -mso -march=bdver1 -mno-fma4 -mno-xop -mno-tbm
-WB, -Wl, -z,muldefs

```

C++ benchmarks:

```

-Ofast -static -CG:load_exe=0 -OPT:malloc_alg=1 -INLINE:aggressive=on
-HP:bd=2m:heap=2m -D__OPEN64_FAST_SET -march=bdver2 -mno-fma4
-mno-xop -mno-tbm -WB, -Wl, -z,muldefs

```

Fortran benchmarks:

```

-Ofast -LNO:blocking=off -LNO:simd_peel_align=on -OPT:rsqrt=2
-OPT:unroll_size=256 -HP:bd=2m:heap=2m -mso -march=bdver1 -mno-fma4
-mno-xop -mno-tbm -WB, -Wl, -z,muldefs

```

Benchmarks using both Fortran and C:

```

-Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000
-IPA:small_pu=100 -mso -march=bdver1 -mno-fma4 -mno-xop -mno-tbm
-WB, -Wl, -z,muldefs -LNO:blocking=off -LNO:simd_peel_align=on
-OPT:rsqrt=2 -OPT:unroll_size=256

```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-openflags-rate-revA-I.html>

<http://www.spec.org/cpu2006/flags/Sugon-Naples-Platform-Settings-revC-I.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-openflags-rate-revA-I.xml>

<http://www.spec.org/cpu2006/flags/Sugon-Naples-Platform-Settings-revC-I.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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