



SPEC® CFP2006 Result

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Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant DL380 Gen10
(2.30 GHz, Intel Xeon Platinum 8165)

SPECfp®_rate2006 = Not Run

SPECfp_rate_base2006 = 1630

CPU2006 license: 3

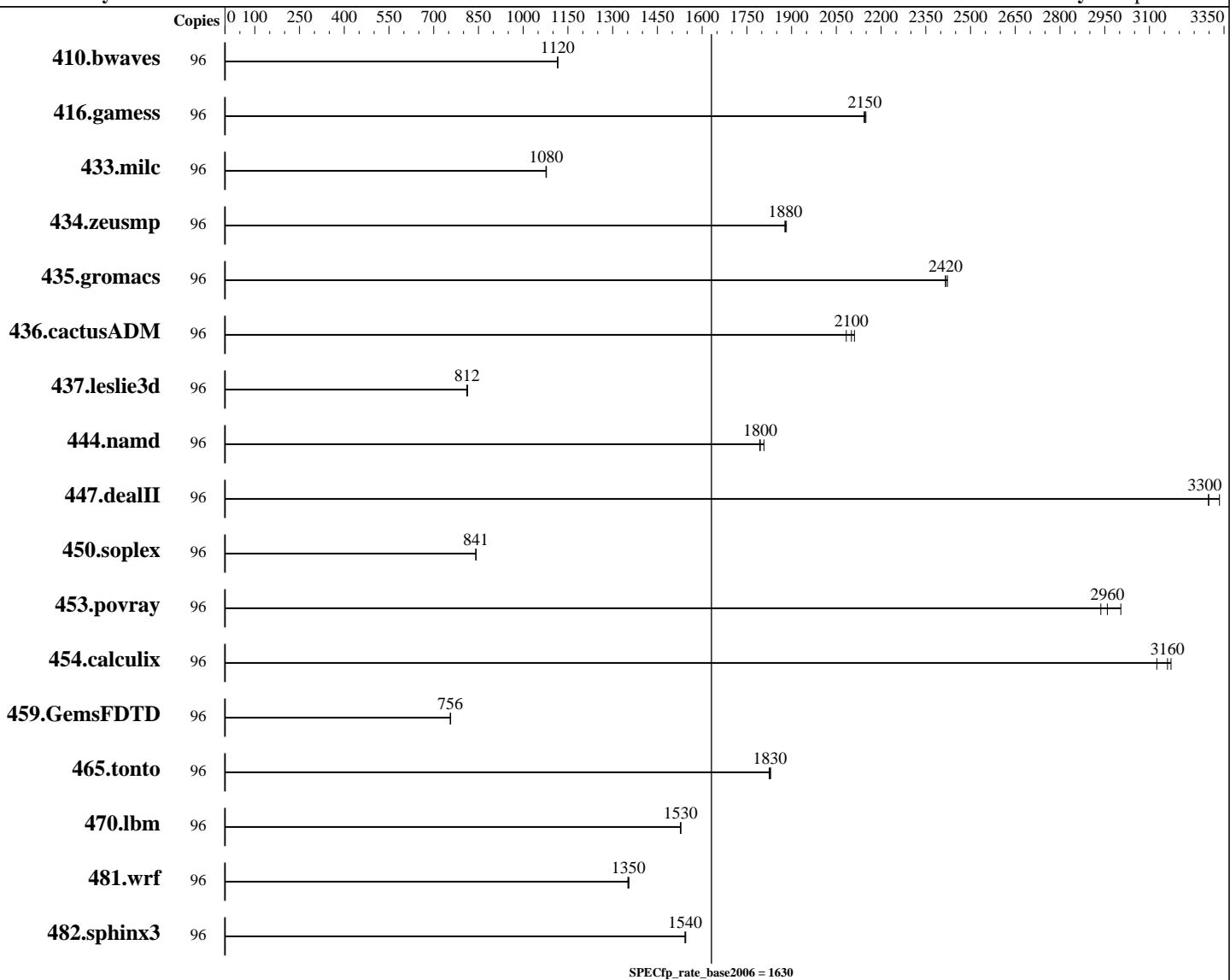
Test sponsor: HPE

Tested by: HPE

Test date: Nov-2017

Hardware Availability: Dec-2017

Software Availability: Apr-2017



Hardware

CPU Name: Intel Xeon Platinum 8165
CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz: 2300
FPU: Integrated
CPU(s) enabled: 48 cores, 2 chips, 24 cores/chip, 2 threads/core
CPU(s) orderable: 1, 2 chip(s)
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 1 MB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP2
Compiler: Kernel 4.4.21-69-default
C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;
Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
Auto Parallel: No
File System: xfs
System State: Run level 3 (multi-user)

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L3 Cache: 33 MB I+D on chip per chip
 Other Cache: None
 Memory: 192 GB (24 x 8 GB 2Rx8 PC4-2666V-R)
 Disk Subsystem: 1 x 960 GB SATA SSD, RAID 0
 Other Hardware: None

Base Pointers: 32/64-bit
 Peak Pointers: Not Applicable
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	96	1170	1120	1169	1120	1169	1120							
416.gamess	96	875	2150	876	2150	877	2140							
433.milc	96	819	1080	818	1080	818	1080							
434.zeusmp	96	465	1880	465	1880	464	1880							
435.gromacs	96	284	2420	283	2420	284	2420							
436.cactusADM	96	551	2080	546	2100	543	2110							
437.leslie3d	96	1109	813	1112	812	1112	812							
444.namd	96	426	1810	429	1800	429	1790							
447.dealII	96	333	3300	333	3300	329	3330							
450.soplex	96	952	841	952	841	951	842							
453.povray	96	174	2940	170	3000	173	2960							
454.calculix	96	251	3160	253	3120	250	3170							
459.GemsFDTD	96	1346	757	1348	756	1348	756							
465.tonto	96	518	1820	517	1830	516	1830							
470.lbm	96	863	1530	864	1530	863	1530							
481.wrf	96	793	1350	792	1350	794	1350							
482.sphinx3	96	1211	1550	1213	1540	1213	1540							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

irqbalance disabled with "service irqbalance stop"

tuned profile set with "tuned-adm profile throughput-performance"

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Operating System Notes (Continued)

VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel numa_balancing"

Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
Memory Patrol Scrubbing set to Disabled
Workload Profile set to General Throughput Compute
Minimum Processor Idle Power Core C-State set to C1E
Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on dl380gen10 Fri Nov 17 10:02:59 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8165 CPU @ 2.30GHz
2 "physical id"s (chips)
96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 48
physical 0: cores 0 1 2 3 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28
29
physical 1: cores 0 1 2 3 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28
29
cache size : 33792 KB

From /proc/meminfo
MemTotal: 197545204 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
This file is deprecated and will be removed in a future service pack or release.
Please check /etc/os-release for details about this release.

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Platform Notes (Continued)

```
os-release:  
  NAME="SLES"  
  VERSION="12-SP2"  
  VERSION_ID="12.2"  
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"  
  ID="sles"  
  ANSI_COLOR="0;32"  
  CPE_NAME="cpe:/o:suse:sles:12:sp2"  
  
uname -a:  
  Linux dl380gen10 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016  
  (9464f67) x86_64 x86_64 x86_64 GNU/Linux  
  
run-level 3 Nov 17 09:59  
  
SPEC is set to: /home/cpu2006  
Filesystem      Type  Size  Used  Avail Use% Mounted on  
  /dev/sdb4        xfs   852G  147G  705G  18%  /home  
Additional information from dmidecode:  
  
Warning: Use caution when you interpret this section. The 'dmidecode' program  
reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to  
hardware, firmware, and the "DMTF SMBIOS" standard.  
  
BIOS HPE U30 09/29/2017  
Memory:  
  24x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666 MHz  
  
(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
  434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
  465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.xml>

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For other inquiries, please contact webmaster@spec.org.

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