SPEC® CINT2006 Result
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Hewlett Packard Enterprise
(Test Sponsor: HPE)

Synergy 480 Gen10
(3.00 GHz, Intel Xeon Gold 6154)

$SPECint®_rate2006 = $ Not Run

$SPECint_rate_base2006 = $ 2010

CPU2006 license: 3	**Test date:** Oct-2017
Test sponsor: HPE	**Hardware Availability:** Oct-2017
Tested by: HPE	**Software Availability:** Apr-2017

Copies

- 400.perlbench 72 1570
- 401.bzip2 72 891
- 403.gcc 72 1410
- 429.mcf 72 2510
- 445.gobmk 72 1320
- 456.hmmer 72 2830
- 458.sjeng 72 1390
- 462.libquantum 72 34300
- 464.h264ref 72 2350
- 471.omnetpp 72 934
- 473.astar 72 1080
- 483.xalancbmk 72 2120

SPECint_rate_base2006 = 2010

Hardware

CPU Name: Intel Xeon Gold 6154
CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip, 2 threads/core
CPU(s) orderable: 1, 2 chip(s)
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 1 MB I+D on chip per core
L3 Cache: 24.75 MB I+D on chip per chip
Other Cache: None
Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)
Disk Subsystem: 1 x 960 GB SSD SATA, RAID 0
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP2
Kernel 4.4.21-69-default
Compiler: C/C++: Version 17.0.3.191 of Intel C/C++
Compiler for Linux
Auto Parallel: No
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: Microquill SmartHeap V10.2

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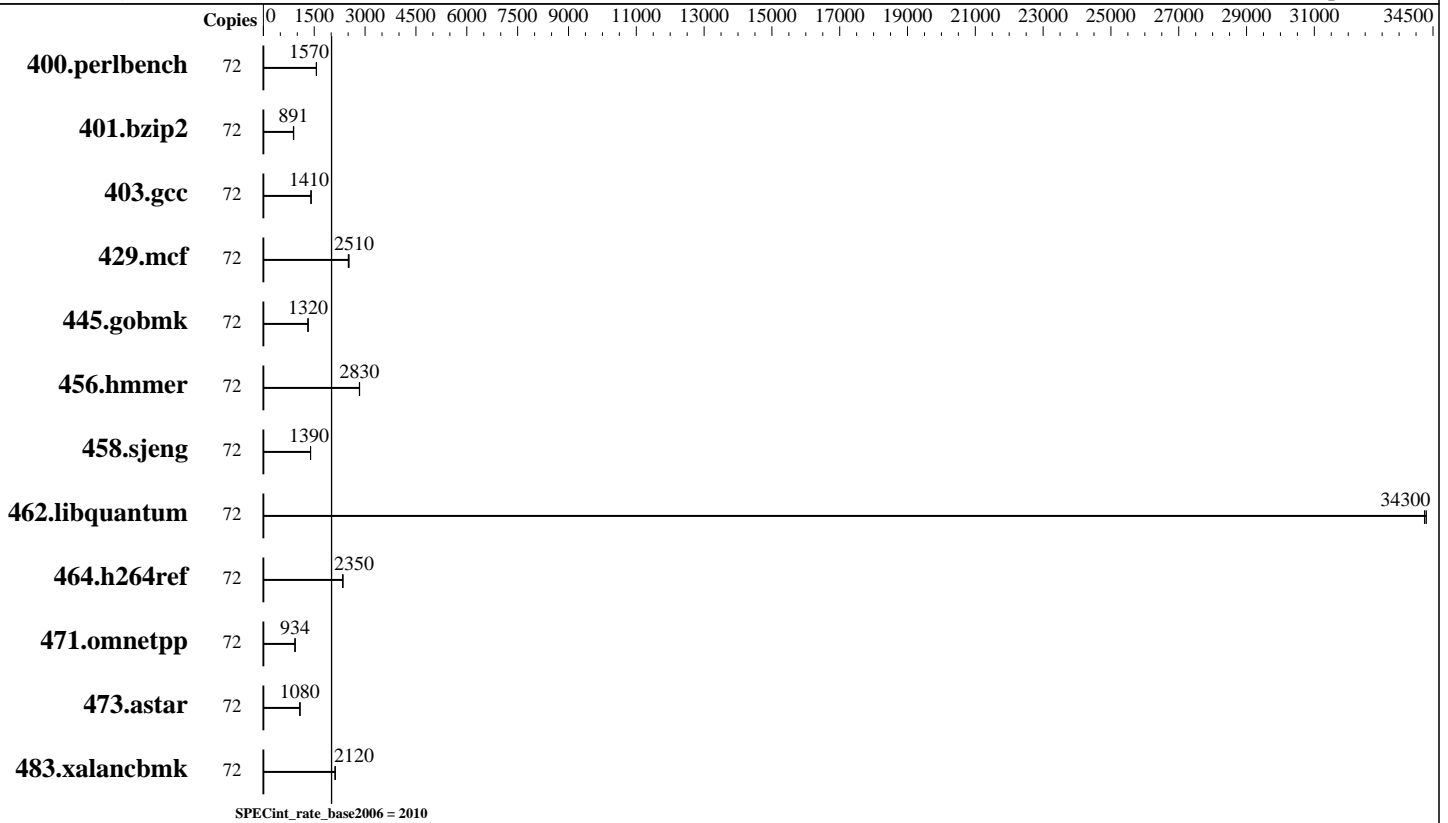
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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	72	**449**	**1570**	449	1570	451	1560							
401.bzip2	72	785	885	777	894	**780**	**891**							
403.gcc	72	412	1410	412	1410	**412**	**1410**							
429.mcf	72	262	2510	**262**	**2510**	260	2530							
445.gobmk	72	573	1320	573	1320	**573**	**1320**							
456.hmmer	72	237	2830	237	2840	**237**	**2830**							
458.sjeng	72	**626**	**1390**	625	1390	626	1390							
462.libquantum	72	43.6	34200	43.5	34300	**43.5**	**34300**							
464.h264ref	72	682	2340	679	2350	**679**	**2350**							
471.omnetpp	72	**482**	**934**	481	936	482	933							
473.astar	72	**469**	**1080**	468	1080	470	1080							
483.xalancbmk	72	234	2120	235	2120	**235**	**2120**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

   The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

   Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
 shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>

Platform Notes

   BIOS Configuration:
 Thermal Configuration set to Maximum Cooling
 Memory Patrol Scrubbing set to Disabled
 LLC Prefetcher set to Enabled
 LLC Dead Line Allocation set to Disabled
 Stale A to S set to Disabled
 Workload Pofile set to General Throughput Compute
  Minimum Processor Idle Power Core C-State set to C1E

 Sysinfo program /home/cpu2006/config/sysinfo.rev6993
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
 running on linux-vjuj Wed Oct 25 09:40:40 2017

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Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Gold 6154 CPU @ 3.00GHz
      2 "physical id"s (chips)
      72 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 18
      siblings  : 36
      physical 0: cores 0 1 2 3 8 9 10 11 16 17 18 19 20 24 25 26 27
      physical 1: cores 0 1 2 3 8 9 10 11 16 17 18 19 20 24 25 26 27
   cache size : 25344 KB

From /proc/meminfo
   MemTotal:       395922444 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 2
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP2"
      VERSION_ID="12.2"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
   Linux linux-vjuj 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
   (9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 25 09:40

SPEC is set to: /home/cpu2006
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda4      xfs   852G  6.3G  846G   1% /home
```

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Platform Notes (Continued)

```
  Additional information from dmidecode:

    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS HPE I42 09/27/2017
    Memory:
    24x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

  Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
  memory using Redhat Enterprise Linux 7.2
```

Base Compiler Invocation

C benchmarks:
```
    icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

C++ benchmarks:
```
    icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

Base Portability Flags

```
     400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
       401.bzip2: -D_FILE_OFFSET_BITS=64
         403.gcc: -D_FILE_OFFSET_BITS=64
         429.mcf: -D_FILE_OFFSET_BITS=64
       445.gobmk: -D_FILE_OFFSET_BITS=64
       456.hmmer: -D_FILE_OFFSET_BITS=64
        458.sjeng: -D_FILE_OFFSET_BITS=64
    462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
      464.h264ref: -D_FILE_OFFSET_BITS=64
      471.omnetpp: -D_FILE_OFFSET_BITS=64
        473.astar: -D_FILE_OFFSET_BITS=64
    483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```

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Base Optimization Flags

C benchmarks:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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