



# SPEC® CFP2006 Result

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Hewlett Packard Enterprise  
(Test Sponsor: HPE)

ProLiant DL380 Gen10  
(2.10 GHz, Intel Xeon Silver 4116)

**SPECfp\_rate2006 = Not Run**

**SPECfp\_rate\_base2006 = 923**

**CPU2006 license:** 3

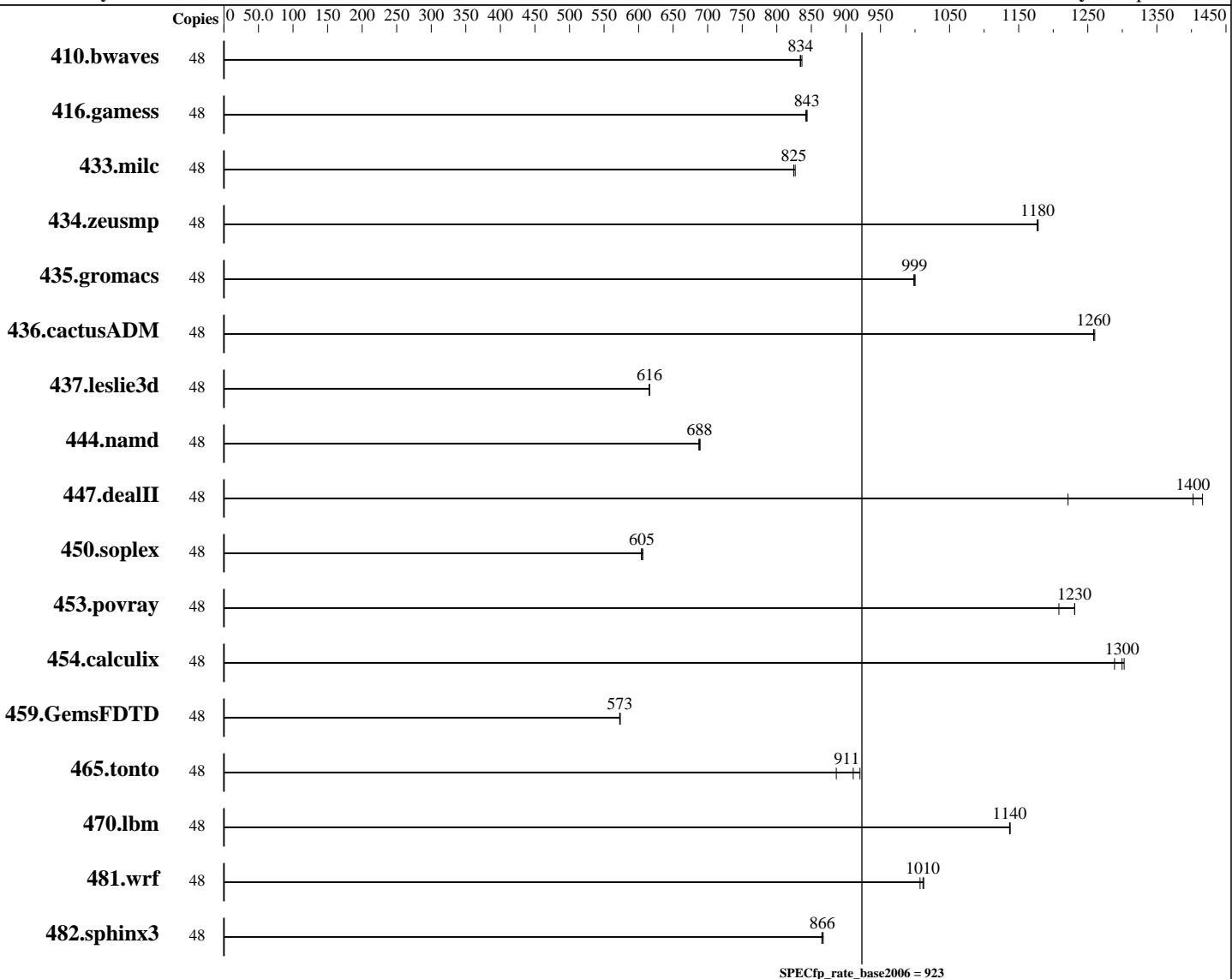
**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Oct-2017

**Hardware Availability:** Oct-2017

**Software Availability:** Apr-2017



## Hardware

CPU Name: Intel Xeon Silver 4116  
CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
CPU MHz: 2100  
FPU: Integrated  
CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core  
CPU(s) orderable: 1, 2 chip(s)  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 1 MB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 12 (x86\_64) SP2  
Compiler: Kernel 4.4.21-69-default  
C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
Auto Parallel: No  
File System: Btrfs  
System State: Run level 5 (multi-user w/GUI)

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L3 Cache: 16.5 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 192 GB (24 x 8 GB 2Rx8 PC4-2666V-R,  
 running at 2400)  
 Disk Subsystem: 1 x 960 GB SATA SSD, RAID 0  
 Other Hardware: None

Base Pointers: 32/64-bit  
 Peak Pointers: Not Applicable  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	48	780	836	<b>782</b>	<b>834</b>	782	834							
416.gamess	48	1114	844	1116	842	<b>1114</b>	<b>843</b>							
433.milc	48	534	825	<b>534</b>	<b>825</b>	533	827							
434.zeusmp	48	371	1180	371	1180	<b>371</b>	<b>1180</b>							
435.gromacs	48	343	1000	343	998	<b>343</b>	<b>999</b>							
436.cactusADM	48	456	1260	455	1260	<b>456</b>	<b>1260</b>							
437.leslie3d	48	733	616	733	616	<b>733</b>	<b>616</b>							
444.namd	48	<b>559</b>	<b>688</b>	560	687	559	689							
447.dealII	48	450	1220	388	1420	<b>392</b>	<b>1400</b>							
450.soplex	48	<b>662</b>	<b>605</b>	663	604	660	606							
453.povray	48	211	1210	<b>207</b>	<b>1230</b>	207	1230							
454.calculix	48	304	1300	<b>305</b>	<b>1300</b>	307	1290							
459.GemsFDTD	48	888	574	<b>889</b>	<b>573</b>	889	573							
465.tonto	48	513	920	533	886	<b>519</b>	<b>911</b>							
470.lbm	48	580	1140	<b>580</b>	<b>1140</b>	580	1140							
481.wrf	48	529	1010	532	1010	<b>530</b>	<b>1010</b>							
482.sphinx3	48	1081	865	<b>1080</b>	<b>866</b>	1079	867							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

irqbalance disabled with "service irqbalance stop"

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## Operating System Notes (Continued)

tuned profile set with "tuned-adm profile throughput-performance"  
 VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty\_ratio"  
 Numa balancing was disabled using "echo 0 > /proc/sys/kernel numa\_balancing"

## Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling  
 Memory Patrol Scrubbing set to Disabled  
 LLC Prefetcher set to Enabled  
 LLC Dead Line Allocation set to Disabled  
 Workload Profile set to General Throughput Compute  
 Minimum Processor Idle Power Core C-State set to C1E

Sysinfo program /cpu2006/config/sysinfo.rev6993  
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
 running on dl380-sys2-sles Fri Oct 13 19:50:38 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4116 CPU @ 2.10GHz
  2 "physical id"s (chips)
  48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 12
  siblings : 24
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 16896 KB
```

```
From /proc/meminfo
MemTotal:      197552564 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2
```

```
From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 2
  # This file is deprecated and will be removed in a future service pack or
  release.
  # Please check /etc/os-release for details about this release.
```

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## Platform Notes (Continued)

```
os-release:  
  NAME="SLES"  
  VERSION="12-SP2"  
  VERSION_ID="12.2"  
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"  
  ID="sles"  
  ANSI_COLOR="0;32"  
  CPE_NAME="cpe:/o:suse:sles:12:sp2"  
  
uname -a:  
  Linux dl380-sys2-sles 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016  
  (9464f67) x86_64 x86_64 x86_64 GNU/Linux  
  
run-level 5 Oct 13 13:47  
  
SPEC is set to: /cpu2006  
  Filesystem      Type  Size  Used Avail Use% Mounted on  
  /dev/sdb3        btrfs  489G   51G  436G  11% /  
Additional information from dmidecode:  
  
Warning: Use caution when you interpret this section. The 'dmidecode' program  
reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to  
hardware, firmware, and the "DMTF SMBIOS" standard.  
  
BIOS HPE U30 09/29/2017  
Memory:  
  24x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666 MHz, configured at 2400 MHz  
  
(End of data from sysinfo program)
```

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/cpu2006/lib/ia32:/cpu2006/lib/intel64:/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

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## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
  434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
  465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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