SPEC® CFP2006 Result
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Cisco Systems

Cisco UCS B200 M5 (Intel Xeon Gold 6144, 3.50 GHz)

$SPECfp^®2006 =$ Not Run

$SPECfp\_base2006 =$ 153

CPU2006 license: 9019	**Test date:** Sep-2017
Test sponsor: Cisco Systems	**Hardware Availability:** Aug-2017
Tested by: Cisco Systems	**Software Availability:** Apr-2017

```
                0   50.0  100  150  200  250  300  350  400  450  500  550  600  650  700  750  800  850  900  950 1000 1050 1100       1200

 410.bwaves                                                                                                              1050

 416.gamess       53.6

 433.milc         82.6

 434.zeusmp                             266

 435.gromacs      68.8

 436.cactusADM                                                                                                                1120

 437.leslie3d                                                  529

 444.namd        40.5

 447.dealII       79.7

 450.soplex       56.2

 453.povray       79.2

 454.calculix     79.7

 459.GemsFDTD                            280

 465.tonto        62.0

 470.lbm                                                                                                                       1150

 481.wrf                     142

 482.sphinx3      83.8
```

SPECfp_base2006 = 153

Hardware

CPU Name:	Intel Xeon Gold 6144
CPU Characteristics:	Intel Turbo Boost Technology up to 4.20 GHz
CPU MHz:	3500
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core

Software

Operating System:	SUSE Linux Enterprise Server 12 SP2 (x86_64) 4.4.21-69-default
Compiler:	C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux; Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)

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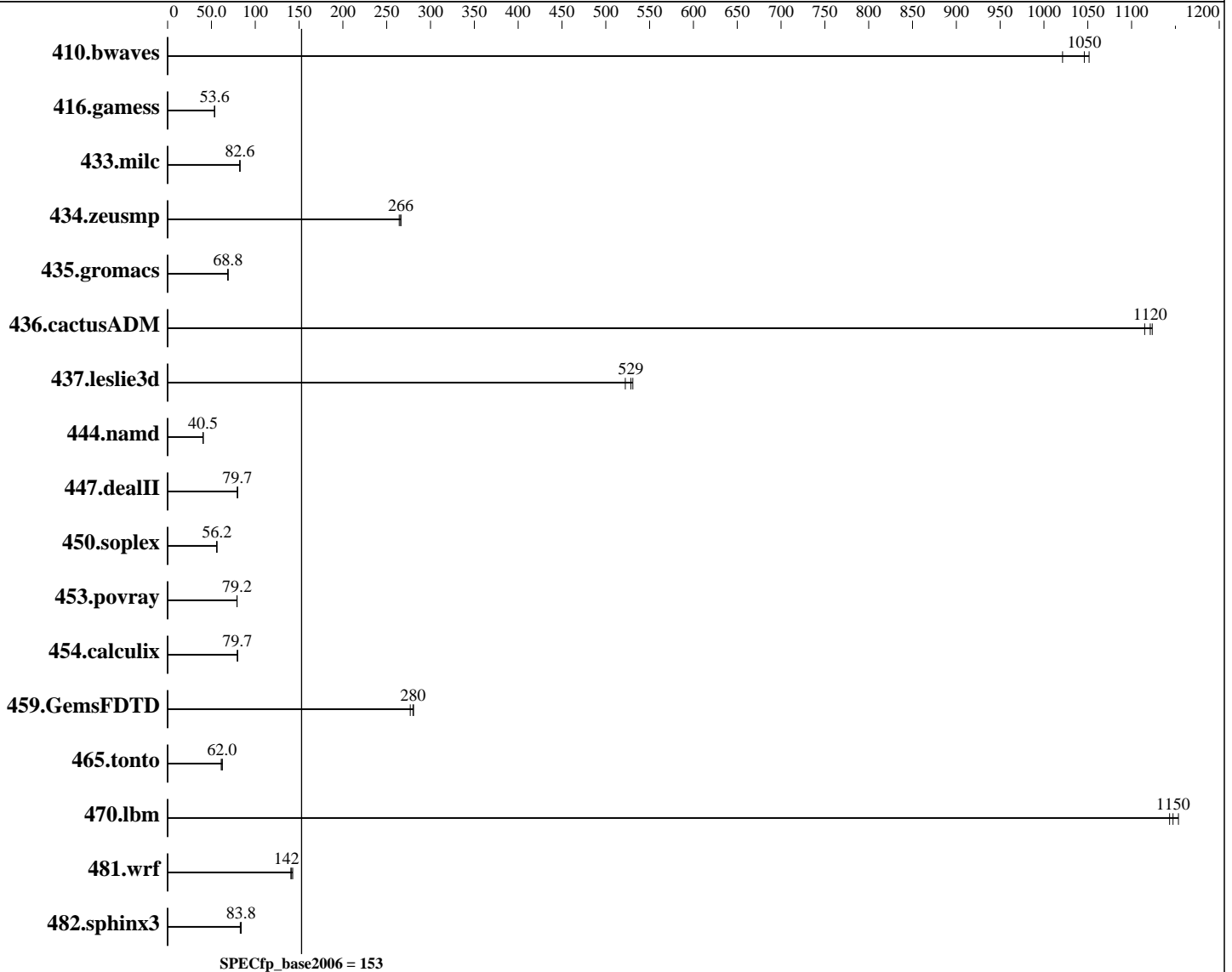
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L3 Cache: 24.75 MB I+D on chip per chip
Other Cache: None
Memory: 384 GB (24 x 16 GB 2Rx4 PC4-2666V-R)
Disk Subsystem: 1 x 600 GB SAS HDD, 10K RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other Software: None

Results Table

Benchmark	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
410.bwaves	13.3	1020	**13.0**	**1050**	12.9	1050						
416.gamess	365	53.6	**365**	**53.6**	365	53.7						
433.milc	111	82.6	**111**	**82.6**	112	82.3						
434.zeusmp	34.2	266	34.4	264	**34.2**	**266**						
435.gromacs	103	69.1	**104**	**68.8**	104	68.6						
436.cactusADM	**10.7**	**1120**	10.6	1120	10.7	1110						
437.leslie3d	**17.8**	**529**	18.0	522	17.7	531						
444.namd	198	40.5	**198**	**40.5**	198	40.5						
447.dealII	**144**	**79.7**	144	79.5	143	80.1						
450.soplex	148	56.3	149	56.0	**148**	**56.2**						
453.povray	67.2	79.1	**67.2**	**79.2**	67.1	79.3						
454.calculix	**104**	**79.7**	103	79.9	104	79.6						
459.GemsFDTD	37.8	281	38.3	277	**37.9**	**280**						
465.tonto	157	62.5	161	61.2	**159**	**62.0**						
470.lbm	12.0	1140	**12.0**	**1150**	11.9	1150						
481.wrf	79.4	141	78.2	143	**78.9**	**142**						
482.sphinx3	**233**	**83.8**	232	84.0	235	82.9						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Settings:
Intel HyperThreading Technology set to Disabled
CPU performance set to Enterprise
Power Performance Tuning set to OS
SNC set to Disabled
IMC Interleaving set to Auto
Patrol Scrub set to Disabled
 Sysinfo program /home/cpu2006-1.2/config/sysinfo.rev6993
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
 running on linux-mys2 Sat Sep  9 13:33:19 2017
```
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Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Gold 6144 CPU @ 3.50GHz
      2 "physical id"s (chips)
      16 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 8
      physical 0: cores 0 2 3 9 16 19 26 27
      physical 1: cores 0 2 3 9 16 19 26 27
   cache size : 25344 KB

From /proc/meminfo
   MemTotal:       394832668 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 2
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP2"
      VERSION_ID="12.2"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
   Linux linux-mys2 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
   (9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 1 21:35

SPEC is set to: /home/cpu2006-1.2
   Filesystem      Type   Size  Used Avail Use% Mounted on
   /dev/sda3       xfs    182G   14G  169G   8% /home
```

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Platform Notes (Continued)

```
  Additional information from dmidecode:

     Warning: Use caution when you interpret this section. The 'dmidecode' program
     reads system data which is "intended to allow hardware to be accurately
     determined", but the intent may not be met, as there are frequent changes to
     hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS Cisco Systems, Inc. B200M5.3.2.1d.5.0727171353 07/27/2017
   Memory:
     24x 0xCE00 M393A2G40EB2-CTD 16 GB 2 rank 2666 MHz

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006-1.2/lib/ia32:/home/cpu2006-1.2/lib/intel64:/home/cpu2006-1.2/sh10.2"
OMP_NUM_THREADS = "16"

 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.2
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc -m64
```

C++ benchmarks:
```
icpc -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
```

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Base Portability Flags (Continued)

```
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
   450.soplex: -DSPEC_CPU_LP64
   453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
     470.lbm: -DSPEC_CPU_LP64
     481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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