



# SPEC® CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

## Lenovo Global Technology

### SPECfp®\_rate2006 = 1280

ThinkSystem SR630  
(2.10 GHz, Intel Xeon Gold 6130T)

### SPECfp\_rate\_base2006 = 1250

CPU2006 license: 9017

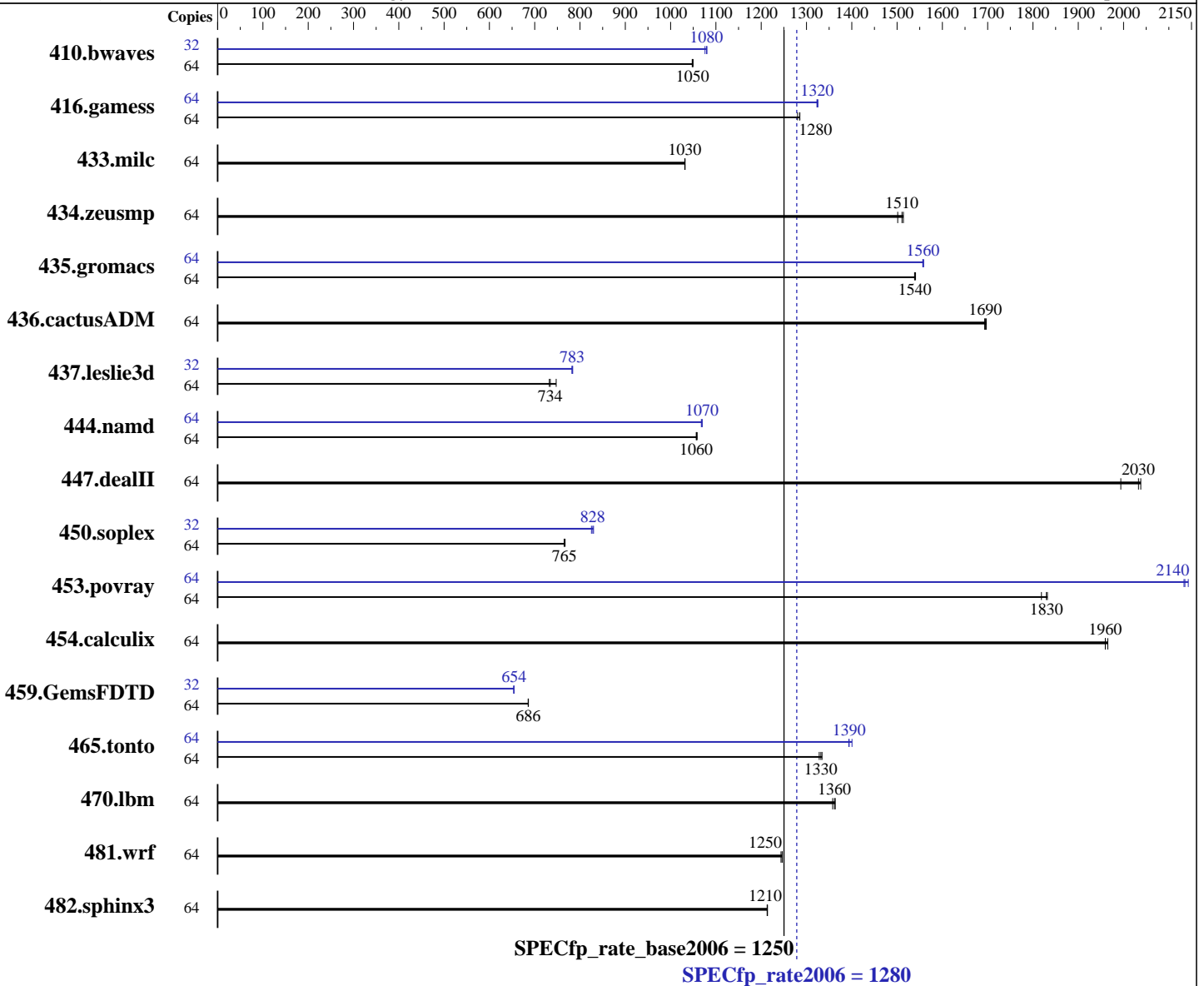
Test date: Aug-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Apr-2017



### Hardware

CPU Name: Intel Xeon Gold 6130T  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86\_64)  
 Kernel 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
 Auto Parallel: Yes  
 File System: btrfs  
 System State: Run level 3 (multi-user)

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECfp\_rate2006 = 1280

ThinkSystem SR630  
(2.10 GHz, Intel Xeon Gold 6130T)

SPECfp\_rate\_base2006 = 1250

CPU2006 license: 9017

Test date: Aug-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Apr-2017

L3 Cache: 22 MB I+D on chip per chip  
Other Cache: None  
Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)  
Disk Subsystem: 1 x 800 GB SAS SSD  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	64	829	1050	<b>830</b>	<b>1050</b>	830	1050	32	403	1080	404	1080	<b>403</b>	<b>1080</b>
416.gamess	64	<b>975</b>	<b>1280</b>	979	1280	975	1290	64	946	1330	947	1320	<b>947</b>	<b>1320</b>
433.milc	64	569	1030	570	1030	<b>569</b>	<b>1030</b>	64	569	1030	570	1030	<b>569</b>	<b>1030</b>
434.zeusmp	64	385	1510	388	1500	<b>385</b>	<b>1510</b>	64	385	1510	388	1500	<b>385</b>	<b>1510</b>
435.gromacs	64	297	1540	<b>297</b>	<b>1540</b>	297	1540	64	293	1560	<b>293</b>	<b>1560</b>	294	1560
436.cactusADM	64	<b>451</b>	<b>1690</b>	452	1690	451	1700	64	<b>451</b>	<b>1690</b>	452	1690	451	1700
437.leslie3d	64	805	747	822	732	<b>819</b>	<b>734</b>	32	385	782	384	784	<b>384</b>	<b>783</b>
444.namd	64	485	1060	<b>486</b>	<b>1060</b>	486	1060	64	480	1070	<b>480</b>	<b>1070</b>	481	1070
447.dealII	64	367	1990	<b>360</b>	<b>2030</b>	359	2040	64	367	1990	<b>360</b>	<b>2030</b>	359	2040
450.soplex	64	<b>697</b>	<b>765</b>	696	767	698	765	32	<b>322</b>	<b>828</b>	322	830	323	825
453.povray	64	<b>186</b>	<b>1830</b>	186	1830	187	1820	64	<b>159</b>	<b>2140</b>	159	2140	160	2130
454.calculix	64	<b>269</b>	<b>1960</b>	269	1960	269	1970	64	<b>269</b>	<b>1960</b>	269	1960	269	1970
459.GemsFDTD	64	990	686	990	686	<b>990</b>	<b>686</b>	32	520	653	519	654	<b>519</b>	<b>654</b>
465.tonto	64	<b>473</b>	<b>1330</b>	472	1330	474	1330	64	450	1400	452	1390	<b>452</b>	<b>1390</b>
470.lbm	64	<b>646</b>	<b>1360</b>	648	1360	645	1360	64	<b>646</b>	<b>1360</b>	648	1360	645	1360
481.wrf	64	<b>574</b>	<b>1250</b>	575	1240	573	1250	64	<b>574</b>	<b>1250</b>	575	1240	573	1250
482.sphinx3	64	1028	1210	1028	1210	<b>1028</b>	<b>1210</b>	64	1028	1210	1028	1210	<b>1028</b>	<b>1210</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Choose Operating Mode set to Maximum Performance  
SNC set to Enable

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECfp\_rate2006 = 1280

ThinkSystem SR630  
(2.10 GHz, Intel Xeon Gold 6130T)

SPECfp\_rate\_base2006 = 1250

CPU2006 license: 9017

Test date: Aug-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Apr-2017

### Platform Notes (Continued)

DCU Streamer Prefetcher set to Disable

Stale AtoS set to Enable

LLC dead line alloc set to Disable

Sysinfo program /home/cpu2006-1.2-ic17.0u3/config/sysinfo.rev6993

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)

running on Cable-SPECcpu2006-SUSE12SP2 Wed Aug 2 16:13:38 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6130T CPU @ 2.10GHz

2 "physical id"s (chips)

64 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 16

siblings : 32

physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

cache size : 22528 KB

From /proc/meminfo

MemTotal: 395892860 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86\_64)

VERSION = 12

PATCHLEVEL = 2

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP2"

VERSION\_ID="12.2"

PRETTY\_NAME="SUSE Linux Enterprise Server 12 SP2"

ID="sles"

ANSI\_COLOR="0;32"

CPE\_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:

Linux Cable-SPECcpu2006-SUSE12SP2 4.4.21-69-default #1 SMP Tue Oct 25

10:58:20 UTC 2016 (9464f67) x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Aug 2 05:27

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

<http://www.spec.org/>



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECfp\_rate2006 = 1280

ThinkSystem SR630  
(2.10 GHz, Intel Xeon Gold 6130T)

SPECfp\_rate\_base2006 = 1250

CPU2006 license: 9017

Test date: Aug-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Apr-2017

## Platform Notes (Continued)

SPEC is set to: /home/cpu2006-1.2-ic17.0u3

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	btrfs	744G	180G	561G	25%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[IVE109Q-1.00]- 06/28/2017

Memory:

24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic17.0u3/lib/ia32:/home/cpu2006-1.2-ic17.0u3/lib/intel64:/home/cpu2006-1.2-ic17.0u3/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 4



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECfp\_rate2006 = 1280

ThinkSystem SR630  
(2.10 GHz, Intel Xeon Gold 6130T)

SPECfp\_rate\_base2006 = 1250

CPU2006 license: 9017

Test date: Aug-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Apr-2017

## Base Portability Flags (Continued)

```

416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

```

Fortran benchmarks:

```

ifort -m64

```

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECfp\_rate2006 = 1280

ThinkSystem SR630  
(2.10 GHz, Intel Xeon Gold 6130T)

SPECfp\_rate\_base2006 = 1250

CPU2006 license: 9017

Test date: Aug-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Apr-2017

## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
         -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -fno-alias -auto-ilp32
         -qopt-mem-layout-trans=3

```

447.dealII: basepeak = yes

```

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
          -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -qopt-malloc-options=3
          -qopt-mem-layout-trans=3

```

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECfp\_rate2006 = 1280

ThinkSystem SR630  
(2.10 GHz, Intel Xeon Gold 6130T)

SPECfp\_rate\_base2006 = 1250

CPU2006 license: 9017

Test date: Aug-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Apr-2017

## Peak Optimization Flags (Continued)

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc  
-qopt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.20171004.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.20171004.xml>



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630  
(2.10 GHz, Intel Xeon Gold 6130T)

SPECfp\_rate2006 = 1280

SPECfp\_rate\_base2006 = 1250

**CPU2006 license:** 9017

**Test sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test date:** Aug-2017

**Hardware Availability:** Aug-2017

**Software Availability:** Apr-2017

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Wed Oct 4 12:35:49 2017 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 3 October 2017.