



# SPEC® CFP2006 Result

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## Supermicro

SuperServer SYS-2049U-TR4  
(X11QPH+, Intel Xeon Platinum 8180)

SPECfp®\_rate2006 = Not Run

SPECfp\_rate\_base2006 = 3470

CPU2006 license: 001176

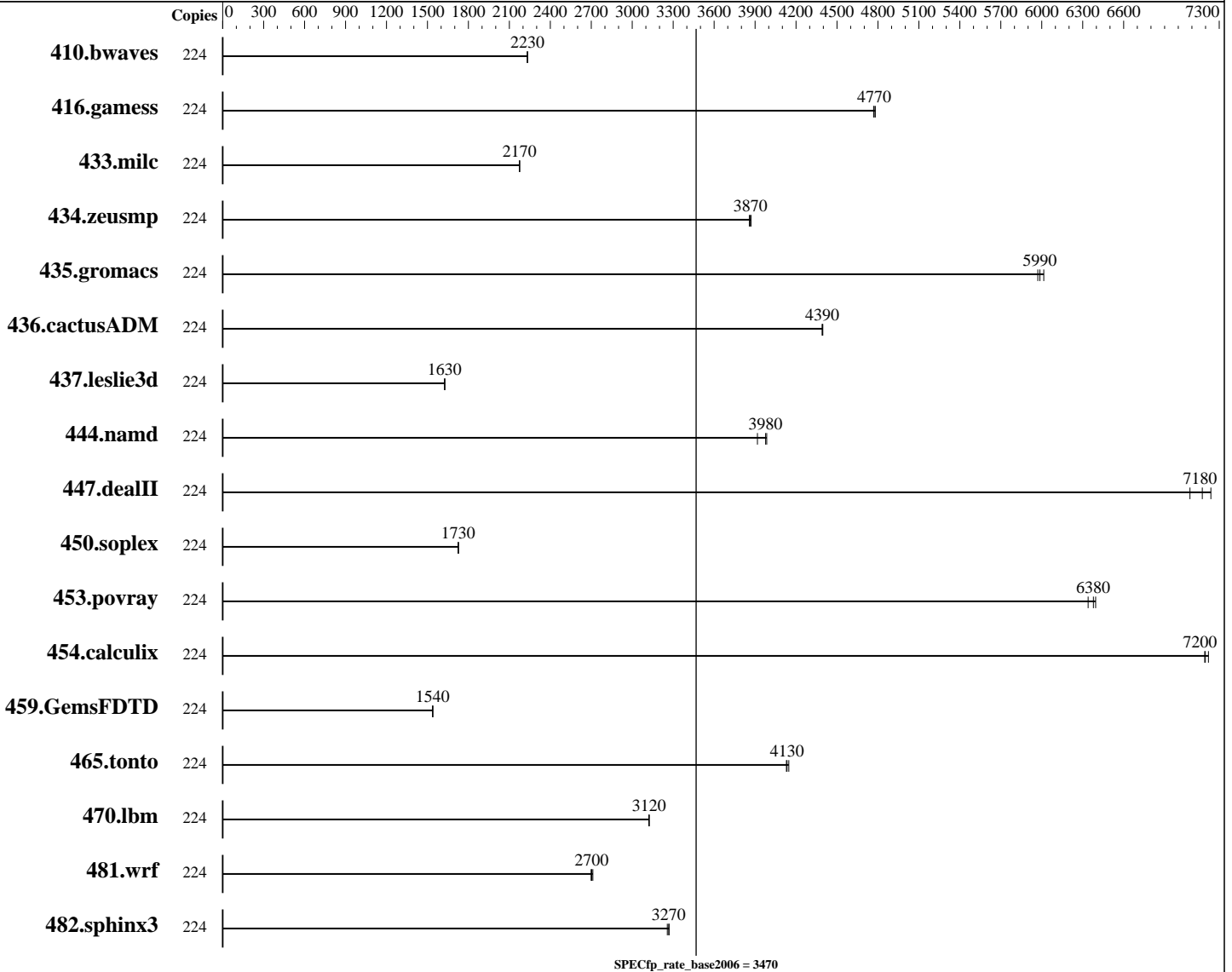
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jul-2017

Hardware Availability: Jul-2017

Software Availability: Apr-2017



### Hardware

CPU Name: Intel Xeon Platinum 8180  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 112 cores, 4 chips, 28 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,4 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 SP2  
 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++  
 Compiler for Linux;  
 Fortran: Version 17.0.0.098 of Intel Fortran  
 Compiler for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)

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L3 Cache: 38.5 MB I+D on chip per chip  
Other Cache: None  
Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R)  
Disk Subsystem: 1 x 3.2 TB SAS3 (RAID0, 2 x 1.8TB SAS3, 10K RPM)  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: No

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	224	1364	2230	<b><u>1364</u></b>	<b><u>2230</u></b>	1364	2230							
416.gamess	224	<b><u>919</u></b>	<b><u>4770</u></b>	917	4780	920	4770							
433.milc	224	<b><u>946</u></b>	<b><u>2170</u></b>	946	2170	945	2180							
434.zeusmp	224	528	3860	<b><u>527</u></b>	<b><u>3870</u></b>	527	3870							
435.gromacs	224	266	6010	<b><u>267</u></b>	<b><u>5990</u></b>	268	5970							
436.cactusADM	224	609	4390	609	4400	<b><u>609</u></b>	<b><u>4390</u></b>							
437.leslie3d	224	1294	1630	<b><u>1295</u></b>	<b><u>1630</u></b>	1295	1630							
444.namd	224	459	3920	<b><u>452</u></b>	<b><u>3980</u></b>	451	3990							
447.dealII	224	354	7240	362	7090	<b><u>357</u></b>	<b><u>7180</u></b>							
450.soplex	224	1083	1730	<b><u>1082</u></b>	<b><u>1730</u></b>	1082	1730							
453.povray	224	<b><u>187</u></b>	<b><u>6380</u></b>	186	6390	188	6340							
454.calculix	224	256	7220	<b><u>257</u></b>	<b><u>7200</u></b>	257	7190							
459.GemsFDTD	224	<b><u>1545</u></b>	<b><u>1540</u></b>	1544	1540	1545	1540							
465.tonto	224	<b><u>533</u></b>	<b><u>4130</u></b>	534	4130	531	4150							
470.lbm	224	986	3120	<b><u>985</u></b>	<b><u>3120</u></b>	985	3120							
481.wrf	224	923	2710	<b><u>926</u></b>	<b><u>2700</u></b>	927	2700							
482.sphinx3	224	<b><u>1336</u></b>	<b><u>3270</u></b>	1340	3260	1335	3270							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
Hardware P-states = Disable  
Intel Virtualization Technology = Disable

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**Software Availability:** Apr-2017

### Platform Notes (Continued)

SNC = Enable  
IMC Interleaving = 1Way Interleave  
LLC dead line alloc = Disable  
Stale AtoS = Enable  
Sysinfo program /home/cpu2006/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on linux-kepK Sun Jul 9 18:17:12 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Platinum 8180 CPU @ 2.50GHz
 4 "physical id"s (chips)
 224 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 28
  siblings  : 56
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
 25 26 27 28 29 30
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
 25 26 27 28 29 30
  physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
 25 26 27 28 29 30
  physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
 25 26 27 28 29 30
cache size : 39424 KB
```

From /proc/meminfo

```
MemTotal: 1583923692 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

/usr/bin/lsb\_release -d

```
SUSE Linux Enterprise Server 12 SP2
```

From /etc/\*release\* /etc/\*version\*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
```

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### Platform Notes (Continued)

```
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="/o:suse:sles:12:sp2"
```

```
uname -a:
Linux linux-kepk 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jul 8 18:57
```

```
SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4       ext4  3.2T  428G  2.8T  14% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 0.9 06/22/2017

Memory:  
48x Hynix HMA84GR7AFR4N-VK 32 GB 2 rank 2666 MHz

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh10.2"
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

### Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

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## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

Fortran benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>  
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-SKL-revA.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-SKL-revA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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