



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

### SPECfp<sup>®</sup>\_rate2006 = 1690

### Huawei 1288H V5 (Intel Xeon Platinum 8176)

### SPECfp\_rate\_base2006 = 1650

CPU2006 license: 3175

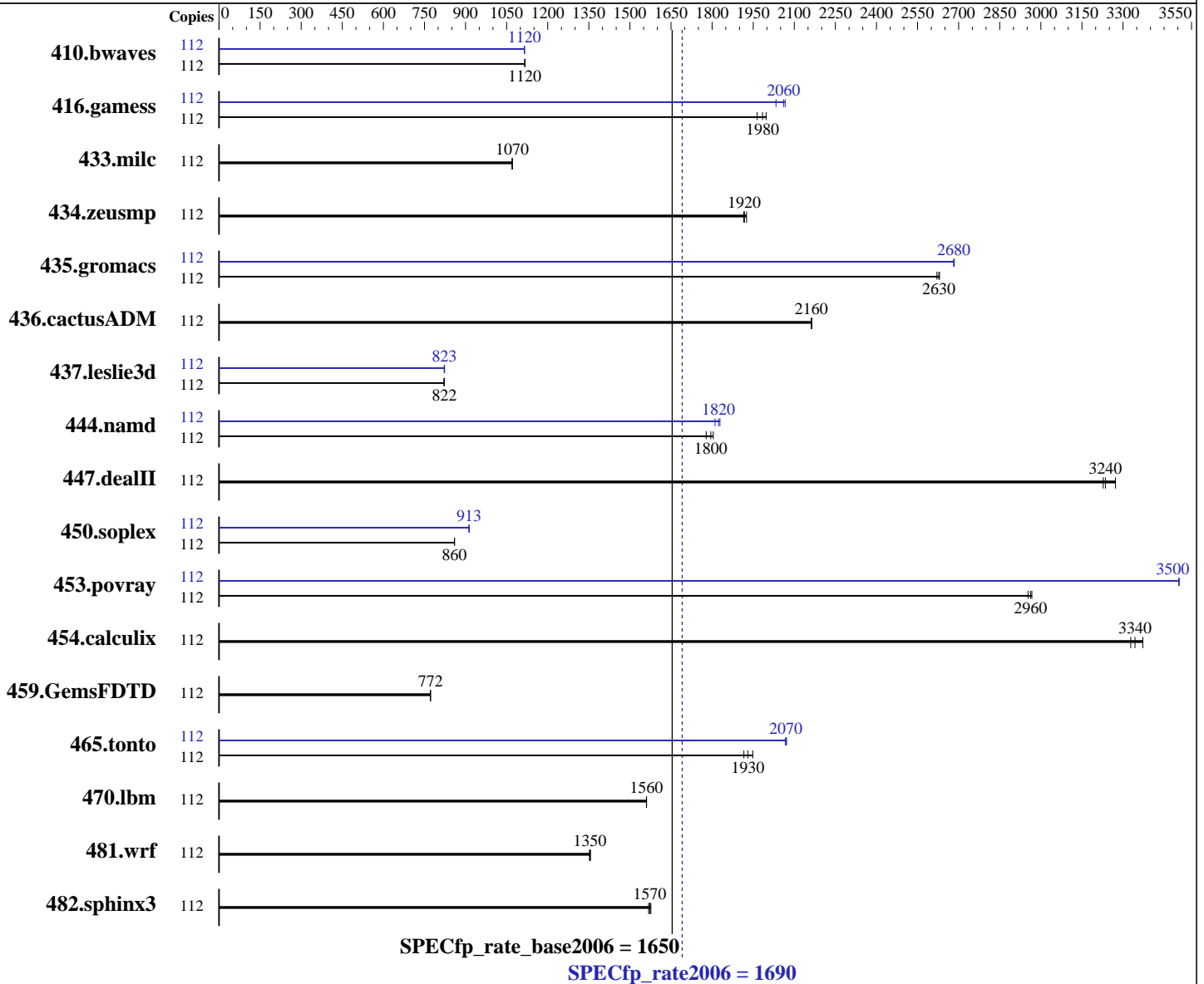
Test date: Jul-2017

Test sponsor: Huawei

Hardware Availability: Aug-2017

Tested by: Huawei

Software Availability: Nov-2016



#### Hardware

CPU Name: Intel Xeon Platinum 8176  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 56 cores, 2 chips, 28 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

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#### Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
 3.10.0-514.el7.x86\_64  
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.0.098 of Intel Fortran Compiler for Linux  
 Auto Parallel: No  
 File System: xfs

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L3 Cache: 38.5 MB I+D on chip per chip  
Other Cache: None  
Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R)  
Disk Subsystem: 1 x 1200 GB SAS, 10000 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	112	1362	1120	<u>1363</u>	<u>1120</u>	1364	1120	112	1365	1120	<u>1365</u>	<u>1120</u>	1366	1110
416.gamess	112	1098	2000	1116	1960	<u>1105</u>	<u>1980</u>	112	<u>1064</u>	<u>2060</u>	1079	2030	1061	2070
433.milc	112	961	1070	960	1070	<u>960</u>	<u>1070</u>	112	961	1070	960	1070	<u>960</u>	<u>1070</u>
434.zeusmp	112	529	1930	<u>531</u>	<u>1920</u>	532	1920	112	529	1930	<u>531</u>	<u>1920</u>	532	1920
435.gromacs	112	<u>304</u>	<u>2630</u>	305	2620	304	2630	112	298	2680	298	2680	<u>298</u>	<u>2680</u>
436.cactusADM	112	619	2160	618	2160	<u>619</u>	<u>2160</u>	112	619	2160	618	2160	<u>619</u>	<u>2160</u>
437.leslie3d	112	1282	821	<u>1280</u>	<u>822</u>	1280	823	112	1279	823	1281	822	<u>1279</u>	<u>823</u>
444.namd	112	505	1780	<u>500</u>	<u>1800</u>	498	1800	112	<u>493</u>	<u>1820</u>	496	1810	491	1830
447.dealII	112	397	3230	<u>396</u>	<u>3240</u>	392	3270	112	397	3230	<u>396</u>	<u>3240</u>	392	3270
450.soplex	112	<u>1087</u>	<u>860</u>	1086	860	1087	859	112	<u>1023</u>	<u>913</u>	1022	914	1024	912
453.povray	112	202	2950	<u>201</u>	<u>2960</u>	201	2970	112	170	3500	170	3500	<u>170</u>	<u>3500</u>
454.calculix	112	278	3330	<u>276</u>	<u>3340</u>	274	3370	112	278	3330	<u>276</u>	<u>3340</u>	274	3370
459.GemsFDTD	112	<u>1539</u>	<u>772</u>	1540	772	1539	772	112	<u>1539</u>	<u>772</u>	1540	772	1539	772
465.tonto	112	<u>571</u>	<u>1930</u>	566	1950	575	1920	112	<u>533</u>	<u>2070</u>	532	2070	533	2070
470.lbm	112	986	1560	<u>986</u>	<u>1560</u>	986	1560	112	986	1560	<u>986</u>	<u>1560</u>	986	1560
481.wrf	112	<u>924</u>	<u>1350</u>	925	1350	923	1360	112	<u>924</u>	<u>1350</u>	925	1350	923	1360
482.sphinx3	112	1385	1580	1392	1570	<u>1388</u>	<u>1570</u>	112	1385	1580	1392	1570	<u>1388</u>	<u>1570</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Performance  
Set SNC to Enable

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### Platform Notes (Continued)

```

Set IMC Interleaving to 1 way
Set Patrol Scrub to Disable
Sysinfo program /spec17/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on localhost.localdomain Sat Jul  1 23:31:16 2017

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) Platinum 8176 CPU @ 2.10GHz
 2 "physical id"s (chips)
 112 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 28
  siblings    : 56
  physical 0   : cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
                    25 26 27 28 29 30
  physical 1   : cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
                    25 26 27 28 29 30
cache size     : 39424 KB

```

```

From /proc/meminfo
MemTotal:      790481628 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server

```

```

uname -a:
Linux localhost.localdomain 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13
EDT 2016 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jun 29 06:52

```

SPEC is set to: /spec17
Filesystem      Type  Size  Used Avail Use% Mounted on

```

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## Platform Notes (Continued)

/dev/sda2 xfs 898G 17G 882G 2% /  
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.13 04/11/2017

Memory:

24x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec17/libs/32:/spec17/libs/64:/spec17/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

```

Fortran benchmarks:

```

ifort -m64

```

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## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -D\_FILE\_OFFSET\_BITS=64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -fno-alias -auto-ilp32  
 -qopt-mem-layout-trans=3

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -qopt-malloc-options=3  
 -qopt-mem-layout-trans=3

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## Peak Optimization Flags (Continued)

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: basepeak = yes

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc  
-qopt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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