



SPEC® CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Inspur Corporation

SPECfp®_rate2006 = 1040

Inspur NF5170M4 (Intel Xeon E5-2698 v4)

SPECfp_rate_base2006 = 1000

CPU2006 license: 3358

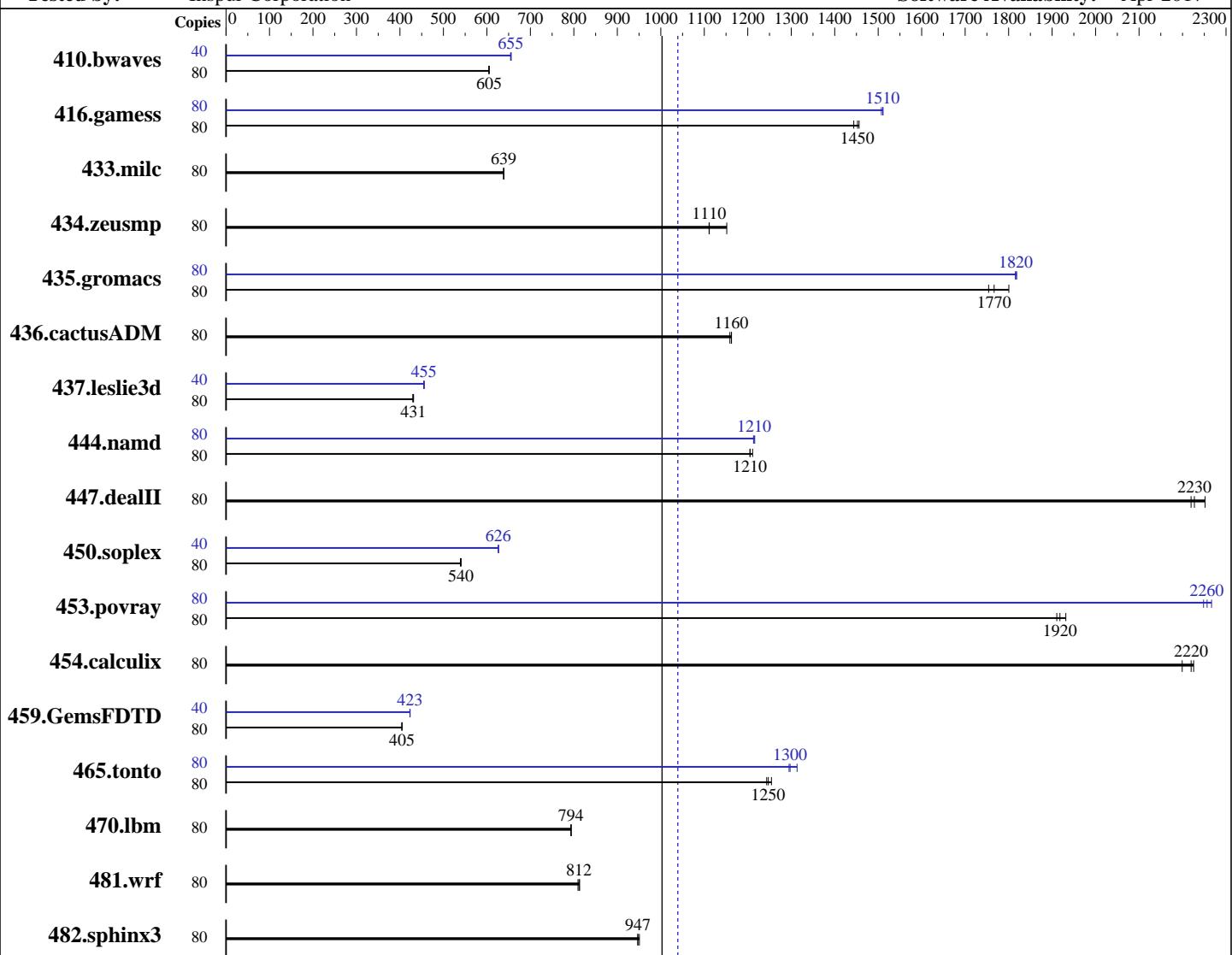
Test date: Jun-2017

Test sponsor: Inspur Corporation

Hardware Availability: Mar-2016

Tested by: Inspur Corporation

Software Availability: Apr-2017



SPECfp_rate_base2006 = 1000

SPECfp_rate2006 = 1040

Hardware

CPU Name: Intel Xeon E5-2698 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 40 cores, 2 chips, 20 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)
 Compiler: 3.10.0-514.el7.x86_64
 C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
 Auto Parallel: Yes
 File System: xfs

Continued on next page

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Inspur Corporation

Inspur NF5170M4 (Intel Xeon E5-2698 v4)

SPECfp_rate2006 = 1040

SPECfp_rate_base2006 = 1000

CPU2006 license: 3358

Test date: Jun-2017

Test sponsor: Inspur Corporation

Hardware Availability: Mar-2016

Tested by: Inspur Corporation

Software Availability: Apr-2017

L3 Cache: 50 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
 Disk Subsystem: 1 x 450 GB SATA SSD
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	80	1797	605	1796	605	1799	604	40	831	654	829	656	830	655
416.gamess	80	1085	1440	1078	1450	1076	1460	80	1039	1510	1037	1510	1037	1510
433.milc	80	1149	639	1150	639	1150	638	80	1149	639	1150	639	1150	638
434.zeusmp	80	632	1150	655	1110	655	1110	80	632	1150	655	1110	655	1110
435.gromacs	80	323	1770	317	1800	326	1750	80	314	1820	315	1820	314	1820
436.cactusADM	80	825	1160	823	1160	822	1160	80	825	1160	823	1160	822	1160
437.leslie3d	80	1751	429	1743	431	1746	431	40	826	455	827	455	824	456
444.namd	80	532	1210	530	1210	532	1210	80	528	1210	528	1220	529	1210
447.dealII	80	411	2230	406	2250	412	2220	80	411	2230	406	2250	412	2220
450.soplex	80	1234	540	1235	540	1238	539	40	533	626	532	627	533	626
453.povray	80	223	1910	222	1920	220	1930	80	188	2270	189	2250	189	2260
454.calculix	80	300	2200	297	2220	297	2230	80	300	2200	297	2220	297	2230
459.GemsFDTD	80	2099	404	2097	405	2097	405	40	1004	423	1003	423	1002	423
465.tonto	80	633	1240	631	1250	627	1250	80	599	1310	608	1290	607	1300
470.lbm	80	1385	794	1385	794	1385	793	80	1385	794	1385	794	1385	793
481.wrf	80	1099	813	1104	809	1100	812	80	1099	813	1104	809	1100	812
482.sphinx3	80	1646	947	1646	947	1640	951	80	1646	947	1646	947	1640	951

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS and OS configuration:
 SCALING_GOVERNOR set to Performance
 Hardware Prefetch set to Disable

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Inspur Corporation

SPECfp_rate2006 = 1040

Inspur NF5170M4 (Intel Xeon E5-2698 v4)

SPECfp_rate_base2006 = 1000

CPU2006 license: 3358

Test date: Jun-2017

Test sponsor: Inspur Corporation

Hardware Availability: Mar-2016

Tested by: Inspur Corporation

Software Availability: Apr-2017

Platform Notes (Continued)

```
VT Support set to Disable
C1E Support set to Disable
Sysinfo program /home/CPU2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on localhost.localdomain Fri Jun 23 23:53:47 2017
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2698 v4 @ 2.20GHz
        2 "physical id"s (chips)
        80 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The
    following excerpts from /proc/cpuinfo might not be reliable. Use with
    caution.)
        cpu cores : 20
        siblings : 40
        physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
        physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
    cache size : 25600 KB
```

```
From /proc/meminfo
    MemTotal:       263845420 kB
    HugePages_Total:      0
    Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.3 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.3"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server
```

```
uname -a:
Linux localhost.localdomain 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13
EDT 2016 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jun 23 11:48
```

```
SPEC is set to: /home/CPU2006
Filesystem           Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   392G   76G  317G  20% /home
Additional information from dmidecode:
```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Inspur Corporation

Inspur NF5170M4 (Intel Xeon E5-2698 v4)

SPECfp_rate2006 = 1040

CPU2006 license: 3358

Test date: Jun-2017

Test sponsor: Inspur Corporation

Hardware Availability: Mar-2016

Tested by: Inspur Corporation

Software Availability: Apr-2017

Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 4.1.11 09/07/2016

Memory:

8x NO DIMM NO DIMM

16x Samsung M393A2K43BB1-CNC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/CPU2006/lib/ia32:/home/CPU2006/lib/intel64:/home/CPU2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Inspur Corporation

SPECfp_rate2006 = 1040

Inspur NF5170M4 (Intel Xeon E5-2698 v4)

SPECfp_rate_base2006 = 1000

CPU2006 license: 3358

Test date: Jun-2017

Test sponsor: Inspur Corporation

Hardware Availability: Mar-2016

Tested by: Inspur Corporation

Software Availability: Apr-2017

Base Portability Flags (Continued)

```
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
437.leslie3d: -DSPEC_CPU_LP64  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
450.soplex: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
470.lbm: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX  
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Inspur Corporation

SPECfp_rate2006 = 1040

Inspur NF5170M4 (Intel Xeon E5-2698 v4)

SPECfp_rate_base2006 = 1000

CPU2006 license: 3358

Test date: Jun-2017

Test sponsor: Inspur Corporation

Hardware Availability: Mar-2016

Tested by: Inspur Corporation

Software Availability: Apr-2017

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -D_FILE_OFFSET_BITS=64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Peak Optimization Flags

C benchmarks:

```
433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
    -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -fno-alias -auto-ilp32
    -qopt-mem-layout-trans=3

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
    -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -qopt-malloc-options=3
    -qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
    -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3
```

Fortran benchmarks:

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Inspur Corporation

SPECfp_rate2006 = 1040

Inspur NF5170M4 (Intel Xeon E5-2698 v4)

SPECfp_rate_base2006 = 1000

CPU2006 license: 3358

Test date: Jun-2017

Test sponsor: Inspur Corporation

Hardware Availability: Mar-2016

Tested by: Inspur Corporation

Software Availability: Apr-2017

Peak Optimization Flags (Continued)

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc
-qopt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>
<http://www.spec.org/cpu2006/flags/Inspur-Platform-Settings-V1.0-HSW.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>
<http://www.spec.org/cpu2006/flags/Inspur-Platform-Settings-V1.0-HSW.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Wed Jul 12 12:11:57 2017 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 11 July 2017.