



SPEC® CFP2006 Result

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Huawei

SPECfp®_rate2006 = 2800

Kunlun 9008 (Intel Xeon E7-4830 v3)

SPECfp_rate_base2006 = 2750

CPU2006 license: 3175

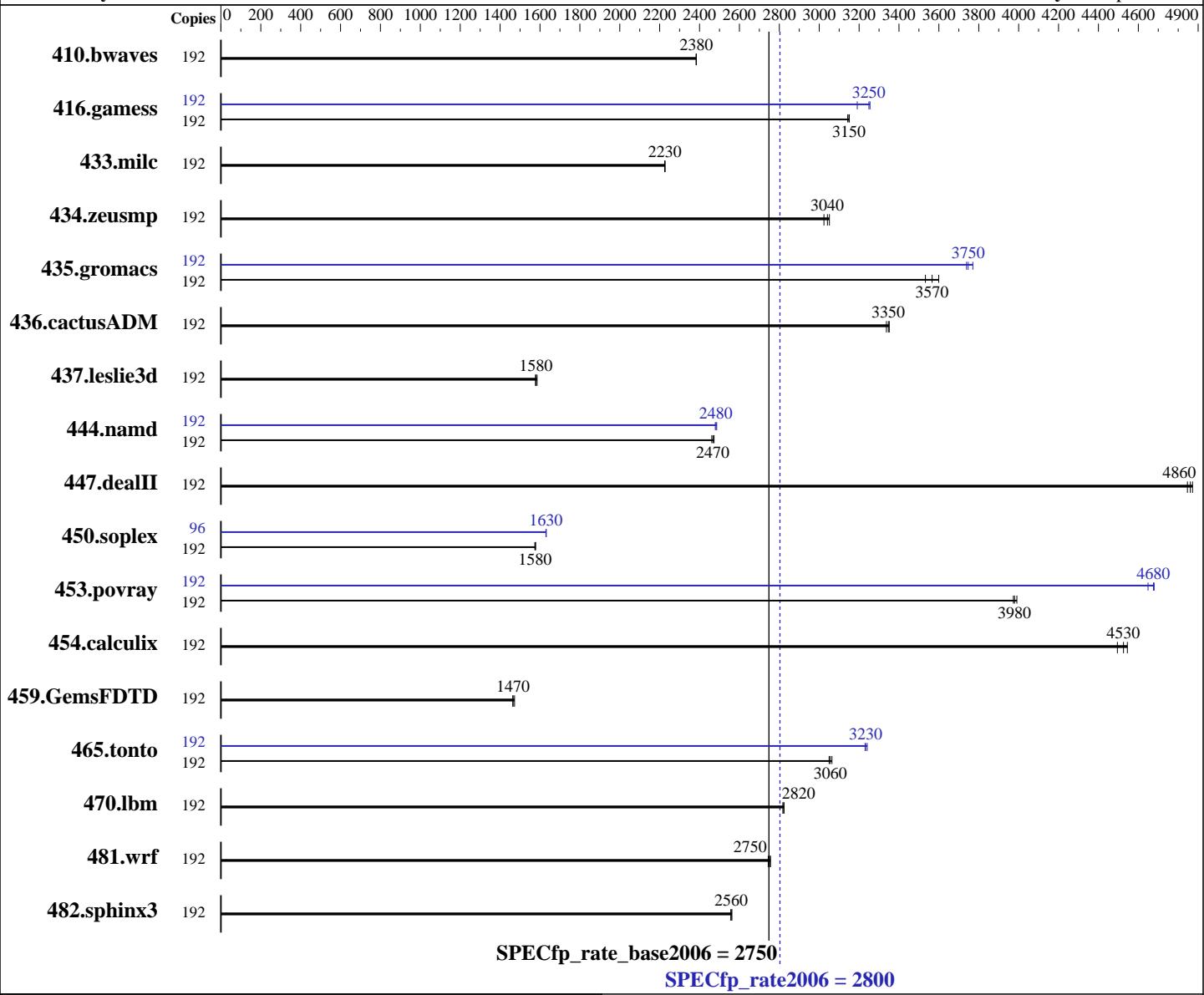
Test date: Nov-2016

Test sponsor: Huawei

Hardware Availability: Jan-2016

Tested by: Huawei

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E7-4830 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 2.70 GHz
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 96 cores, 8 chips, 12 cores/chip, 2 threads/core
 CPU(s) orderable: 4,8 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 7.1 (Maipo)
 Compiler: 3.10.0-229.el7.x86_64
 Auto Parallel: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 File System: Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 No
 xfs

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L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 1 TB (64 x 16 GB 2Rx4 PC4-2133P-R,
 running at 1333 MHz)
 Disk Subsystem: 2 x 300 GB SAS, 10K RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	192	1095	2380	1095	2380	1094	2380	192	1095	2380	1095	2380	1094	2380
416.gamess	192	1194	3150	1196	3140	1193	3150	192	1155	3250	1157	3250	1178	3190
433.milc	192	791	2230	792	2230	792	2230	192	791	2230	792	2230	792	2230
434.zeusmp	192	573	3050	574	3040	578	3020	192	573	3050	574	3040	578	3020
435.gromacs	192	381	3600	384	3570	388	3530	192	367	3740	364	3770	366	3750
436.cactusADM	192	688	3340	686	3350	685	3350	192	688	3340	686	3350	685	3350
437.leslie3d	192	1144	1580	1138	1590	1143	1580	192	1144	1580	1138	1590	1143	1580
444.namd	192	623	2470	626	2460	624	2470	192	620	2480	619	2490	621	2480
447.dealII	192	453	4850	452	4860	451	4870	192	453	4850	452	4860	451	4870
450.soplex	192	1017	1570	1015	1580	1016	1580	96	491	1630	491	1630	491	1630
453.povray	192	257	3980	257	3970	256	3990	192	220	4650	218	4680	218	4680
454.calculix	192	352	4500	350	4530	349	4550	192	352	4500	350	4530	349	4550
459.GemsFDTD	192	1392	1460	1384	1470	1391	1470	192	1392	1460	1384	1470	1391	1470
465.tonto	192	617	3060	620	3050	618	3060	192	585	3230	583	3240	584	3230
470.lbm	192	935	2820	934	2820	936	2820	192	935	2820	934	2820	936	2820
481.wrf	192	782	2740	779	2750	778	2760	192	782	2740	779	2750	778	2760
482.sphinx3	192	1460	2560	1463	2560	1464	2560	192	1460	2560	1463	2560	1464	2560

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
 Turbo mode set with:
 cpupower -c all frequency-set -g performance



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Platform Notes

BIOS configuration:

```
Set Power Efficiency Mode to Performance
Set Lock_step to disabled
Baseboard Management Controller used to adjust the fan speed to 100%
Set C-State to C0/C1
Sysinfo program /home/spec/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Thu Nov 3 09:06:08 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4830 v3 @ 2.10GHz
        8 "physical id"s (chips)
        192 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 12
        siblings   : 24
        physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
        physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
        physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13
        physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13
        physical 4: cores 0 1 2 3 4 5 8 9 10 11 12 13
        physical 5: cores 0 1 2 3 4 5 8 9 10 11 12 13
        physical 6: cores 0 1 2 3 4 5 8 9 10 11 12 13
        physical 7: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB
```

```
From /proc/meminfo
MemTotal:      1053928068 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.1 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.1"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server
```

uname -a:

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Platform Notes (Continued)

```
Linux localhost.localdomain 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38
EST 2015 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 2 17:41
```

```
SPEC is set to: /home/spec
```

```
Filesystem           Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   502G  7.8G  495G   2% /home
```

```
Additional information from dmidecode:
```

```
Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.
```

```
BIOS American Megatrends Inc. BLXSV023 04/01/2016
```

```
Memory:
```

```
64x Micron 18ASF2G72PDZ-2G3B1 16 GB 2 rank , configured at 1333 MHz
128x NO DIMM NO DIMM
```

```
(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/spec/libs/32:/home/spec/libs/64:/home/spec/sh"
```

```
Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
memory using RedHat EL 7.1
```

```
Transparent Huge Pages enabled with:
```

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

```
Filesystem page cache cleared with:
```

```
echo 1> /proc/sys/vm/drop_caches
```

```
runspec command invoked through numactl i.e.:
```

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

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Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

```

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
    -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

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Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-HSW-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-HSW-RevG.xml>



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