



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>\_rate2006 = 2200

Huawei CH242 V3 (Intel Xeon E7-8867 v4)

SPECfp\_rate\_base2006 = 2160

CPU2006 license: 3175

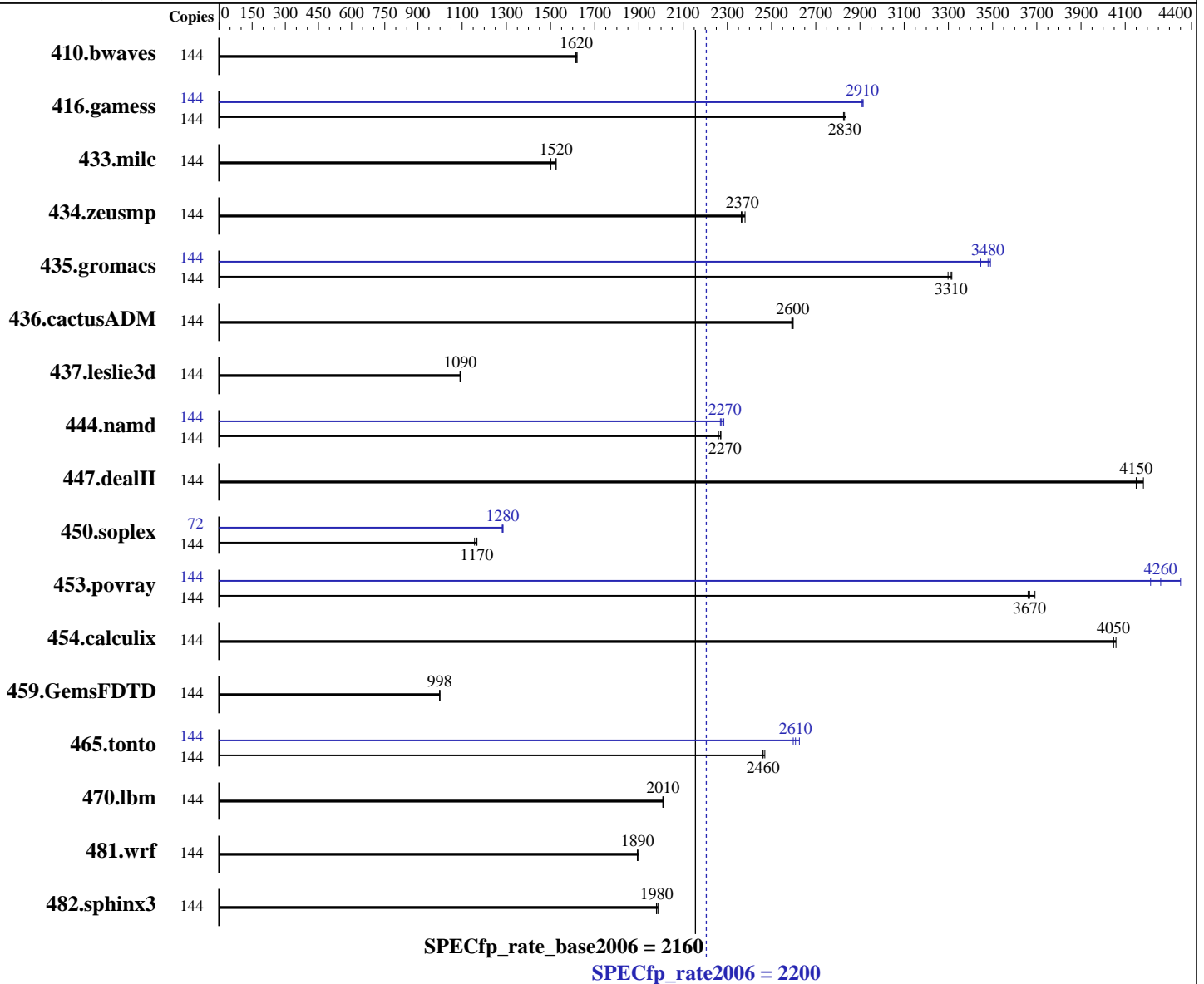
Test date: Dec-2016

Test sponsor: Huawei

Hardware Availability: Jun-2016

Tested by: Huawei

Software Availability: Nov-2015



### Hardware

CPU Name: Intel Xeon E7-8867 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 72 cores, 4 chips, 18 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 3.10.0-327.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit

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L3 Cache: 45 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (32 x 16 GB 2Rx8 PC4-2400T-R, running at 1600 MHz)  
 Disk Subsystem: 1 x 480 GB SSD  
 Other Hardware: None

Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	144	1212	1610	1208	1620	<b><u>1210</u></b>	<b><u>1620</u></b>	144	1212	1610	1208	1620	<b><u>1210</u></b>	<b><u>1620</u></b>
416.gamess	144	994	2840	<b><u>996</u></b>	<b><u>2830</u></b>	998	2830	144	969	2910	967	2910	<b><u>969</u></b>	<b><u>2910</u></b>
433.milc	144	867	1530	880	1500	<b><u>867</u></b>	<b><u>1520</u></b>	144	867	1530	880	1500	<b><u>867</u></b>	<b><u>1520</u></b>
434.zeusmp	144	<b><u>553</u></b>	<b><u>2370</u></b>	551	2380	555	2360	144	<b><u>553</u></b>	<b><u>2370</u></b>	551	2380	555	2360
435.gromacs	144	310	3320	312	3300	<b><u>310</u></b>	<b><u>3310</u></b>	144	295	3490	<b><u>296</u></b>	<b><u>3480</u></b>	298	3450
436.cactusADM	144	<b><u>663</u></b>	<b><u>2600</u></b>	662	2600	664	2590	144	<b><u>663</u></b>	<b><u>2600</u></b>	662	2600	664	2590
437.leslie3d	144	1241	1090	1240	1090	<b><u>1241</u></b>	<b><u>1090</u></b>	144	1241	1090	1240	1090	<b><u>1241</u></b>	<b><u>1090</u></b>
444.namd	144	<b><u>509</u></b>	<b><u>2270</u></b>	509	2270	511	2260	144	509	2270	506	2280	<b><u>508</u></b>	<b><u>2270</u></b>
447.dealII	144	<b><u>397</u></b>	<b><u>4150</u></b>	394	4180	397	4150	144	<b><u>397</u></b>	<b><u>4150</u></b>	394	4180	397	4150
450.soplex	144	1029	1170	1039	1160	<b><u>1030</u></b>	<b><u>1170</u></b>	72	<b><u>468</u></b>	<b><u>1280</u></b>	467	1290	469	1280
453.povray	144	209	3660	208	3690	<b><u>209</u></b>	<b><u>3670</u></b>	144	176	4350	182	4210	<b><u>180</u></b>	<b><u>4260</u></b>
454.calculix	144	294	4050	<b><u>294</u></b>	<b><u>4050</u></b>	293	4060	144	294	4050	<b><u>294</u></b>	<b><u>4050</u></b>	293	4060
459.GemsFDTD	144	1528	1000	1530	998	<b><u>1530</u></b>	<b><u>998</u></b>	144	1528	1000	1530	998	<b><u>1530</u></b>	<b><u>998</u></b>
465.tonto	144	<b><u>576</u></b>	<b><u>2460</u></b>	576	2460	574	2470	144	540	2630	<b><u>543</u></b>	<b><u>2610</u></b>	545	2600
470.lbm	144	<b><u>985</u></b>	<b><u>2010</u></b>	985	2010	984	2010	144	<b><u>985</u></b>	<b><u>2010</u></b>	985	2010	984	2010
481.wrf	144	850	1890	848	1900	<b><u>849</u></b>	<b><u>1890</u></b>	144	850	1890	848	1900	<b><u>849</u></b>	<b><u>1890</u></b>
482.sphinx3	144	1418	1980	<b><u>1417</u></b>	<b><u>1980</u></b>	1413	1990	144	1418	1980	<b><u>1417</u></b>	<b><u>1980</u></b>	1413	1990

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"



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CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Dec-2016

Hardware Availability: Jun-2016

Software Availability: Nov-2015

## Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Performance

Set Lock\_step to disabled

Baseboard Management Controller used to adjust the fan speed to 100%

Set C-State to C0/C1

Set COD Mode to enabled

Sysinfo program /spec16/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on localhost.localdomain Sun Dec 4 03:58:45 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E7-8867 v4 @ 2.40GHz

4 "physical id"s (chips)

144 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 18

siblings : 36

physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27

physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27

physical 2: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27

physical 3: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27

cache size : 23040 KB

From /proc/meminfo

MemTotal: 528065212 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

os-release:

NAME="Red Hat Enterprise Linux Server"

VERSION="7.2 (Maipo)"

ID="rhel"

ID\_LIKE="fedora"

VERSION\_ID="7.2"

PRETTY\_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"

ANSI\_COLOR="0;31"

CPE\_NAME="cpe:/o:redhat:enterprise\_linux:7.2:GA:server"

redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.2:ga:server

uname -a:

Linux localhost.localdomain 3.10.0-327.el7.x86\_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015 x86\_64 x86\_64 x86\_64 GNU/Linux

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info@spec.org

<http://www.spec.org/>

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## Platform Notes (Continued)

run-level 3 Dec 3 15:26

SPEC is set to: /spec16

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sdal	ext4	407G	136G	250G	36%	/

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. BLISV788 11/07/2016

Memory:

32x Hynix HMA82GR7AFR8N-UH 16 GB 2 rank 2400 MHz, configured at 1600 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



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## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

```

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## Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -D\_FILE\_OFFSET\_BITS=64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

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## Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>



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