



# SPEC® CFP2006 Result

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## Lenovo Group Limited

Lenovo System x3250 M6  
(3.60 GHz, Intel Xeon E3-1270 v5)

**SPECfp®\_rate2006 = 195**

**SPECfp\_rate\_base2006 = 191**

CPU2006 license: 9017

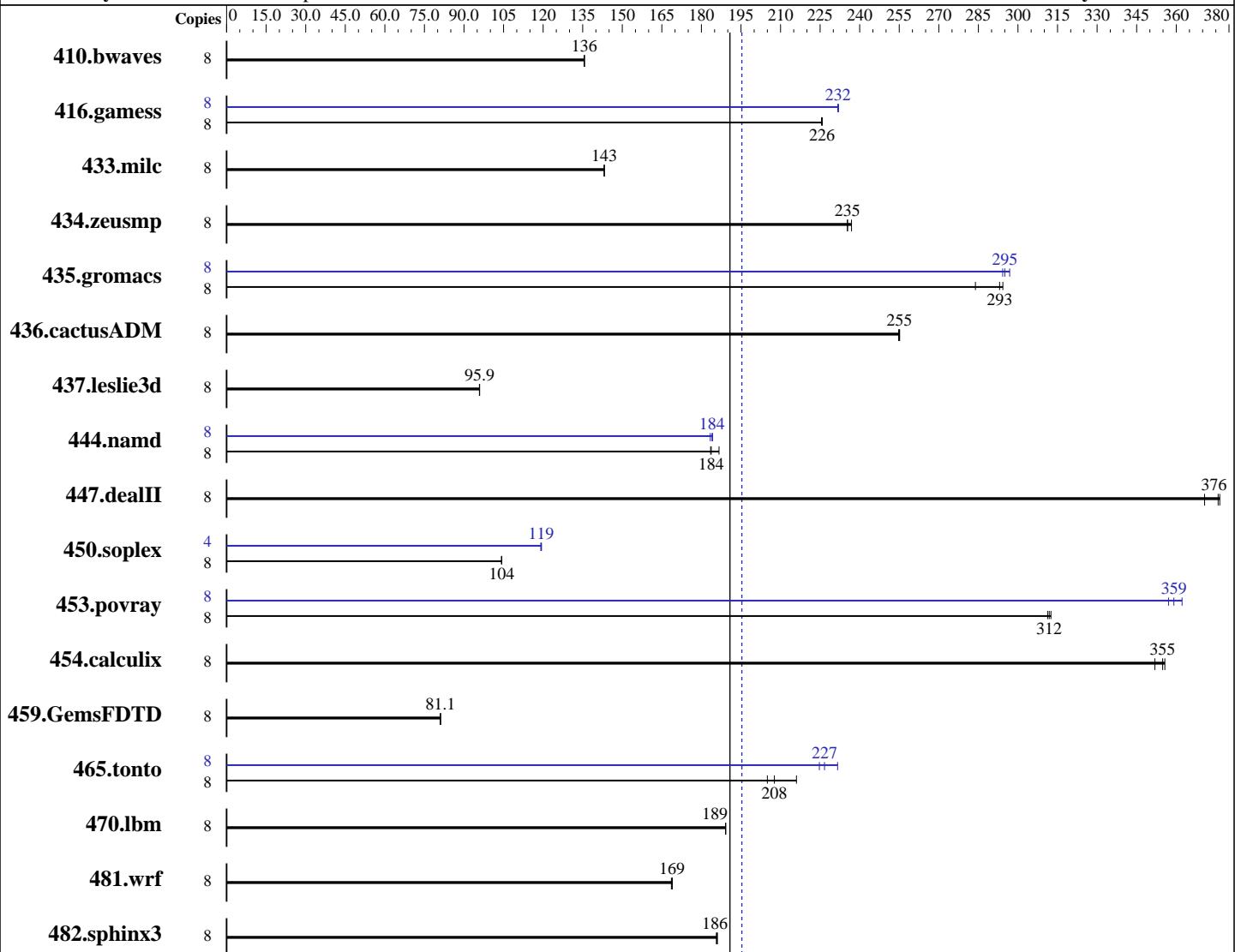
Test date: Nov-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Apr-2016

Tested by: Lenovo Group Limited

Software Availability: Mar-2016



**SPECfp\_rate\_base2006 = 191**

**SPECfp\_rate2006 = 195**

### Hardware

CPU Name: Intel Xeon E3-1270 v5  
CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz  
CPU MHz: 3600  
FPU: Integrated  
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
CPU(s) orderable: 1 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86\_64)  
Kernel 3.12.49-11-default  
Compiler: C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux;  
Fortran: Version 16.0.2.181 of Intel Fortran Studio XE for Linux  
Auto Parallel: No  
File System: xfs  
System State: Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 32 GB (4 x 8 GB 2Rx8 PC4-2133P-E)  
Disk Subsystem: 1 x 240 GB SATA SSD  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	801	136	<u>802</u>	<u>136</u>	802	136	8	801	136	<u>802</u>	<u>136</u>	802	136
416.gamess	8	695	226	<u>694</u>	<u>226</u>	694	226	8	675	232	676	232	<u>676</u>	<u>232</u>
433.milc	8	513	143	513	143	<u>513</u>	<u>143</u>	8	513	143	513	143	<u>513</u>	<u>143</u>
434.zeusmp	8	307	237	309	235	<u>309</u>	<u>235</u>	8	307	237	309	235	<u>309</u>	<u>235</u>
435.gromacs	8	<u>195</u>	<u>293</u>	194	294	201	284	8	<u>194</u>	<u>295</u>	192	297	194	294
436.cactusADM	8	<u>375</u>	<u>255</u>	375	255	375	255	8	<u>375</u>	<u>255</u>	375	255	<u>375</u>	<u>255</u>
437.leslie3d	8	785	95.8	784	95.9	<u>784</u>	<u>95.9</u>	8	785	95.8	784	95.9	<u>784</u>	<u>95.9</u>
444.namd	8	344	187	350	183	<u>349</u>	<u>184</u>	8	<u>349</u>	<u>184</u>	348	184	350	183
447.dealII	8	247	371	243	377	<u>243</u>	<u>376</u>	8	247	371	243	377	<u>243</u>	<u>376</u>
450.soplex	8	639	104	640	104	<u>640</u>	<u>104</u>	4	279	119	<u>280</u>	<u>119</u>	280	119
453.povray	8	<u>136</u>	<u>312</u>	137	311	136	313	8	119	357	118	362	<u>119</u>	<u>359</u>
454.calculix	8	186	356	<u>186</u>	<u>355</u>	188	352	8	186	356	<u>186</u>	<u>355</u>	188	352
459.GemsFDTD	8	1047	81.1	1047	81.1	<u>1047</u>	<u>81.1</u>	8	1047	81.1	1047	81.1	<u>1047</u>	<u>81.1</u>
465.tonto	8	<u>379</u>	<u>208</u>	364	216	384	205	8	<u>347</u>	<u>227</u>	340	232	350	225
470.lbm	8	581	189	581	189	<u>581</u>	<u>189</u>	8	581	189	581	189	<u>581</u>	<u>189</u>
481.wrf	8	<u>529</u>	<u>169</u>	530	169	529	169	8	<u>529</u>	<u>169</u>	530	169	529	169
482.sphinx3	8	840	186	838	186	<u>839</u>	<u>186</u>	8	840	186	838	186	<u>839</u>	<u>186</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```



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## Platform Notes

### BIOS Configuration:

```
Operating Modes set to Maximum Performance
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on TS150 Fri Nov 11 19:16:48 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1270 v5 @ 3.60GHz
        1 "physical id"s (chips)
        8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 4
        siblings : 8
        physical 0: cores 0 1 2 3
cache size : 8192 KB
```

```
From /proc/meminfo
MemTotal:      32969104 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
Linux TS150 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015 (8d714a0)
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Nov 11 11:37

SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem Type Size Used Avail Use% Mounted on
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## Platform Notes (Continued)

/dev/sda3 xfs 170G 15G 155G 9% /home  
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO -[M3E107GUS-1.20]- 10/28/2016

Memory:

4x Samsung M391A1G43EB1-CPB 8 GB 2 rank 2133 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB memory using RedHat EL 7.2 glibc 2.17

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```
447.deall: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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## Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
    450.soplex: -D_FILE_OFFSET_BITS=64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Peak Optimization Flags

C benchmarks:

```
433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
    -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -fno-alias -auto-ilp32
447.dealII: basepeak = yes
450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
    -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -opt-malloc-options=3
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
    -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -unroll4 -ansi-alias
```

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## Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>  
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>  
<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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