



# SPEC<sup>®</sup> CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Huawei

SPECfp<sup>®</sup>\_rate2006 = 775

Huawei RH1288 V3 (Intel Xeon E5-2650L v4)

SPECfp\_rate\_base2006 = 756

CPU2006 license: 3175

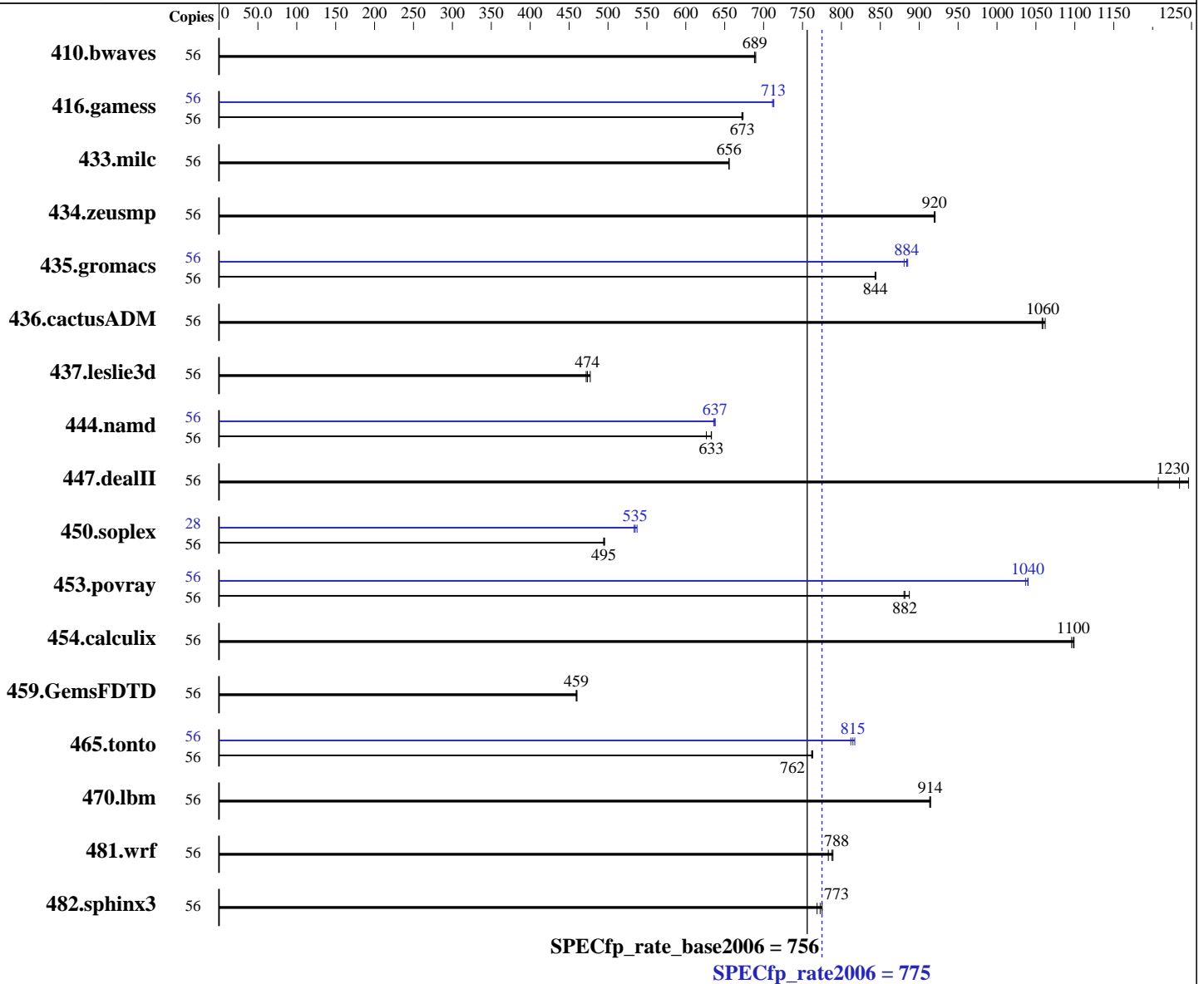
Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015



### Hardware

CPU Name: Intel Xeon E5-2650L v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 1700  
 FPU: Integrated  
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86\_64)  
 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Huawei

SPECfp\_rate2006 = **775**

Huawei RH1288 V3 (Intel Xeon E5-2650L v4)

SPECfp\_rate\_base2006 = **756**

CPU2006 license: 3175

Test date: Nov-2016

Test sponsor: Huawei

Hardware Availability: Mar-2016

Tested by: Huawei

Software Availability: Dec-2015

L3 Cache: 35 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (8 x 32 GB 2Rx4 PC4-2400T-R)  
Disk Subsystem: 1 x 600 GB SAS, 10000 RPM  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	56	<b>1105</b>	<b>689</b>	1106	688	1103	690	56	<b>1105</b>	<b>689</b>	1106	688	1103	690
416.gamess	56	1628	674	1632	672	<b>1630</b>	<b>673</b>	56	1537	713	<b>1539</b>	<b>713</b>	1541	711
433.milc	56	784	655	<b>784</b>	<b>656</b>	784	656	56	784	655	<b>784</b>	<b>656</b>	784	656
434.zeusmp	56	554	921	554	919	<b>554</b>	<b>920</b>	56	554	921	554	919	<b>554</b>	<b>920</b>
435.gromacs	56	474	843	474	844	<b>474</b>	<b>844</b>	56	454	881	<b>452</b>	<b>884</b>	452	885
436.cactusADM	56	<b>632</b>	<b>1060</b>	633	1060	630	1060	56	<b>632</b>	<b>1060</b>	633	1060	630	1060
437.leslie3d	56	<b>1112</b>	<b>474</b>	1115	472	1103	477	56	<b>1112</b>	<b>474</b>	1115	472	1103	477
444.namd	56	709	633	<b>710</b>	<b>633</b>	717	626	56	706	636	704	638	<b>705</b>	<b>637</b>
447.dealII	56	514	1250	531	1210	<b>519</b>	<b>1230</b>	56	514	1250	531	1210	<b>519</b>	<b>1230</b>
450.soplex	56	942	496	<b>944</b>	<b>495</b>	944	495	28	438	534	435	537	<b>437</b>	<b>535</b>
453.povray	56	<b>338</b>	<b>882</b>	338	880	336	887	56	286	1040	<b>287</b>	<b>1040</b>	287	1040
454.calculix	56	422	1100	<b>421</b>	<b>1100</b>	420	1100	56	422	1100	<b>421</b>	<b>1100</b>	420	1100
459.GemsFDTD	56	1291	460	1294	459	<b>1294</b>	<b>459</b>	56	1291	460	1294	459	<b>1294</b>	<b>459</b>
465.tonto	56	<b>723</b>	<b>762</b>	722	763	723	762	56	674	817	679	812	<b>676</b>	<b>815</b>
470.lbm	56	841	915	<b>842</b>	<b>914</b>	842	913	56	841	915	<b>842</b>	<b>914</b>	842	913
481.wrf	56	793	789	799	783	<b>794</b>	<b>788</b>	56	793	789	799	783	<b>794</b>	<b>788</b>
482.sphinx3	56	1408	775	<b>1413</b>	<b>773</b>	1420	769	56	1408	775	<b>1413</b>	<b>773</b>	1420	769

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Performance  
Set Snoop Mode to COD mode

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECfp\_rate2006 = 775

Huawei RH1288 V3 (Intel Xeon E5-2650L v4)

SPECfp\_rate\_base2006 = 756

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

## Platform Notes (Continued)

Set Patrol Scrub to Disable  
 Sysinfo program /spec16/config/sysinfo.rev6914  
 \$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1  
 running on linux-suse Fri Nov 18 09:18:38 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2650L v4@ 1.70GHz
 2 "physical id"s (chips)
 56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores     : 14
  siblings      : 28
  physical 0:   cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1:   cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size      : 17920 KB
```

```
From /proc/meminfo
MemTotal:       264268020 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
Linux linux-suse 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Nov 17 04:00

```
SPEC is set to: /spec16
Filesystem      Type  Size  Used Avail Use% Mounted on
Continued on next page
```



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECfp\_rate2006 = 775

Huawei RH1288 V3 (Intel Xeon E5-2650L v4)

SPECfp\_rate\_base2006 = 756

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

## Platform Notes (Continued)

/dev/sda1 ext4 551G 55G 495G 10% /  
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016

Memory:

8x Micron 36ASF4G72PZ-2G3B1 32 GB 2 rank 2400 MHz  
8x NO DIMM NO DIMM

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 4



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECfp\_rate2006 = 775

Huawei RH1288 V3 (Intel Xeon E5-2650L v4)

SPECfp\_rate\_base2006 = 756

CPU2006 license: 3175

Test date: Nov-2016

Test sponsor: Huawei

Hardware Availability: Mar-2016

Tested by: Huawei

Software Availability: Dec-2015

## Base Portability Flags (Continued)

```

416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

### C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

### C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

### Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

### Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

## Peak Compiler Invocation

### C benchmarks:

```

icc -m64

```

### C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

```

### Fortran benchmarks:

```

ifort -m64

```

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECfp\_rate2006 = 775

Huawei RH1288 V3 (Intel Xeon E5-2650L v4)

SPECfp\_rate\_base2006 = 756

CPU2006 license: 3175

Test date: Nov-2016

Test sponsor: Huawei

Hardware Availability: Mar-2016

Tested by: Huawei

Software Availability: Dec-2015

## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -D\_FILE\_OFFSET\_BITS=64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -opt-malloc-options=3

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECfp\_rate2006 = 775

Huawei RH1288 V3 (Intel Xeon E5-2650L v4)

SPECfp\_rate\_base2006 = 756

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

## Peak Optimization Flags (Continued)

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto  
-inline-alloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Huawei

SPECfp\_rate2006 = 775

Huawei RH1288 V3 (Intel Xeon E5-2650L v4)

SPECfp\_rate\_base2006 = 756

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Dec 15 11:15:36 2016 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 13 December 2016.