



# SPEC® CFP2006 Result

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**Huawei**

**SPECfp<sup>®</sup>\_rate2006 = 622**

**Huawei CH121 V3 (Intel Xeon E5-2643 v4)**

**SPECfp\_rate\_base2006 = 611**

**CPU2006 license:** 3175

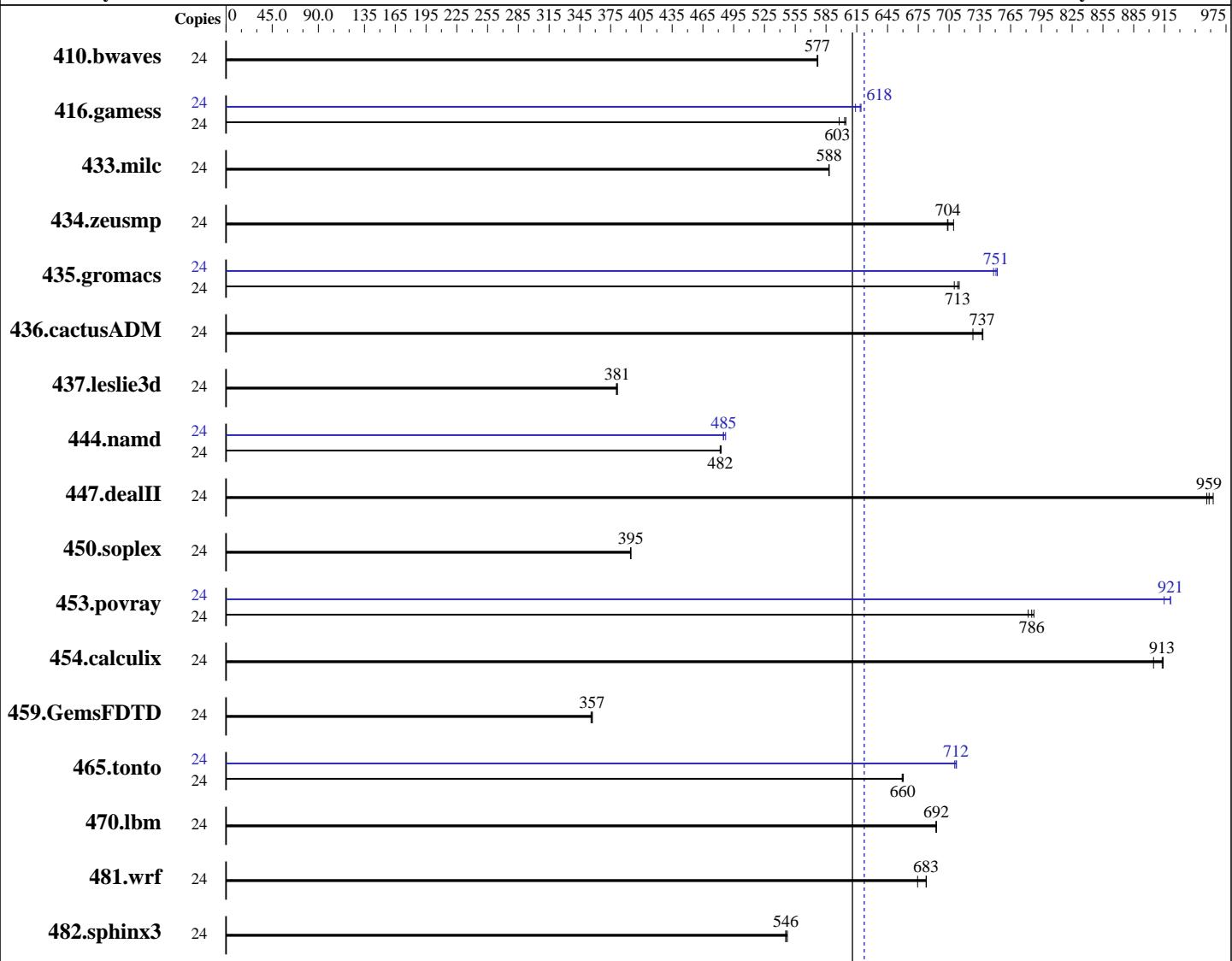
**Test date:** Nov-2016

**Test sponsor:** Huawei

**Hardware Availability:** Mar-2016

**Tested by:** Huawei

**Software Availability:** Nov-2015



**SPECfp\_rate\_base2006 = 611**

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## Hardware

CPU Name: Intel Xeon E5-2643 v4  
CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
CPU MHz: 3400  
FPU: Integrated  
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
CPU(s) orderable: 1,2 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
Compiler: 3.10.0-327.el7.x86\_64  
C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
Auto Parallel: No  
File System: xfs

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L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)  
 Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	566	576	<b>566</b>	<b>577</b>	565	577	24	566	576	<b>566</b>	<b>577</b>	565	577
416.gamess	24	786	598	<b>779</b>	<b>603</b>	778	604	24	759	619	766	614	<b>760</b>	<b>618</b>
433.milc	24	<b>375</b>	<b>588</b>	375	588	375	588	24	<b>375</b>	<b>588</b>	375	588	375	588
434.zeusmp	24	308	709	<b>310</b>	<b>704</b>	310	703	24	308	709	<b>310</b>	<b>704</b>	310	703
435.gromacs	24	241	710	240	715	<b>240</b>	<b>713</b>	24	229	748	<b>228</b>	<b>751</b>	228	752
436.cactusADM	24	<b>389</b>	<b>737</b>	394	728	389	738	24	<b>389</b>	<b>737</b>	394	728	389	738
437.leslie3d	24	<b>592</b>	<b>381</b>	593	381	591	382	24	<b>592</b>	<b>381</b>	593	381	591	382
444.namd	24	399	483	<b>399</b>	<b>482</b>	400	482	24	395	487	397	485	<b>397</b>	<b>485</b>
447.dealII	24	<b>286</b>	<b>959</b>	285	963	287	956	24	<b>286</b>	<b>959</b>	285	963	287	956
450.soplex	24	<b>507</b>	<b>395</b>	508	394	507	395	24	<b>507</b>	<b>395</b>	508	394	<b>507</b>	<b>395</b>
453.povray	24	<b>163</b>	<b>786</b>	162	788	163	782	24	<b>139</b>	<b>921</b>	140	915	139	921
454.calculix	24	<b>217</b>	<b>913</b>	219	904	217	914	24	<b>217</b>	<b>913</b>	219	904	217	914
459.GemsFDTD	24	713	357	715	356	<b>714</b>	<b>357</b>	24	713	357	715	356	<b>714</b>	<b>357</b>
465.tonto	24	<b>358</b>	<b>660</b>	358	659	358	660	24	<b>332</b>	<b>712</b>	332	710	332	712
470.lbm	24	<b>476</b>	<b>692</b>	476	693	476	692	24	<b>476</b>	<b>692</b>	476	693	476	692
481.wrf	24	393	683	398	674	<b>393</b>	<b>683</b>	24	393	683	398	674	<b>393</b>	<b>683</b>
482.sphinx3	24	855	547	857	546	<b>857</b>	<b>546</b>	24	855	547	857	546	<b>857</b>	<b>546</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 Set Power Efficiency Mode to Performance  
 Set Snoop Mode to ES mode

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## Platform Notes (Continued)

Set Patrol Scrub to Disable

Baseboard Management Controller used to adjust the fan speed to 100%

Sysinfo program /spec16/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on localhost.localdomain Thu Nov 10 14:43:49 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2643 v4 @ 3.40GHz
        2 "physical id"s (chips)
        24 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The
    following excerpts from /proc/cpuinfo might not be reliable. Use with
    caution.)
        cpu cores : 6
        siblings : 12
        physical 0: cores 0 1 2 3 6 7
        physical 1: cores 0 1 2 3 6 7
    cache size : 20480 KB
```

```
From /proc/meminfo
    MemTotal:      527793608 kB
    HugePages_Total:       0
    Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.2 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.2"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server
```

```
uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Nov 9 08:57

SPEC is set to: /spec16
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 911G 154G 757G 17% /
Additional information from dmidecode:

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## Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.32 09/14/2016

Memory:

8x NO DIMM NO DIMM

16x Samsung M393A4K40BB1-CRC 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

The Huawei CH121 V3 and Huawei CH222 V3

are electronically equivalent.

The results have been measured on a Huawei CH121 V3 model

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



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## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

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## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

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## Peak Optimization Flags (Continued)

459.GemsFDTD: basepeak = yes

```
465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto
            -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
              -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
              -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
              -prof-use(pass 2) -opt-prefetch -auto-ilp32
```

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

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