SPEC® CFP2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)

Synergy 480 Gen9
(2.40 GHz, Intel Xeon E5-2640 v4)

$SPECfp®\_rate2006 =$ 719
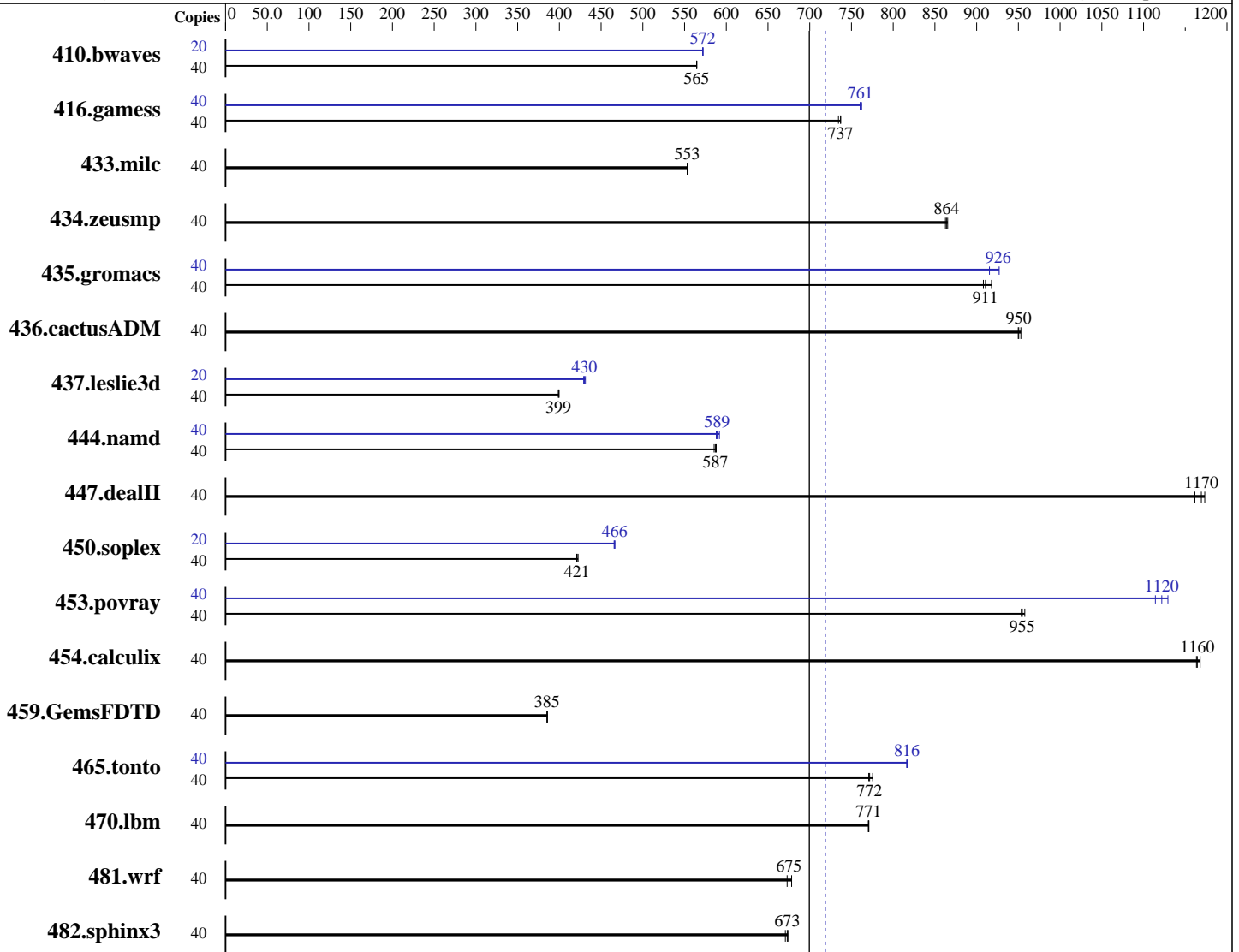
$SPECfp\_rate\_base2006 =$ 700

CPU2006 license:	3		**Test date:**	Oct-2016
Test sponsor:	HPE		**Hardware Availability:**	Dec-2016
Tested by:	HPE		**Software Availability:**	Sep-2016

SPECfp_rate_base2006 = 700

SPECfp_rate2006 = 719

Hardware

CPU Name:	Intel Xeon E5-2640 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.40 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 12 (x86_64) SP1, Kernel 3.12.49-11-default
Compiler:	C/C++: Version 17.0.0.098 of Intel C++ Studio XE for Linux; Fortran: Version 17.0.0.098 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	xfs
System State:	Run level 3 (multi-user)

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L3 Cache:	25 MB I+D on chip per chip	Base Pointers:	32/64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)	Other Software:	None
Disk Subsystem:	1 x 600 GB 10 K SAS, RAID 0		
Other Hardware:	None		

Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
410.bwaves	40	963	565	**963**	**565**	963	564	20	**475**	**572**	475	572	475	572
416.gamess	40	1063	737	**1063**	**737**	1066	734	40	1028	762	**1030**	**761**	1030	760
433.milc	40	664	553	664	553	**664**	**553**	40	664	553	664	553	**664**	**553**
434.zeusmp	40	422	863	421	865	**421**	**864**	40	422	863	421	865	**421**	**864**
435.gromacs	40	**314**	**911**	315	908	311	918	40	**308**	**926**	312	915	308	927
436.cactusADM	40	**503**	**950**	502	953	503	950	40	**503**	**950**	502	953	503	950
437.leslie3d	40	943	399	**942**	**399**	941	400	20	436	431	**437**	**430**	438	429
444.namd	40	546	588	**546**	**587**	548	585	40	**545**	**589**	542	592	546	588
447.dealII	40	**391**	**1170**	394	1160	390	1170	40	**391**	**1170**	394	1160	390	1170
450.soplex	40	793	421	**792**	**421**	790	422	20	**358**	**466**	357	467	358	466
453.povray	40	**223**	**955**	223	954	222	958	40	191	1110	**190**	**1120**	188	1130
454.calculix	40	283	1170	**283**	**1160**	284	1160	40	283	1170	**283**	**1160**	284	1160
459.GemsFDTD	40	1100	386	1102	385	**1102**	**385**	40	1100	386	1102	385	**1102**	**385**
465.tonto	40	511	771	**510**	**772**	508	775	40	482	816	482	817	**482**	**816**
470.lbm	40	713	771	714	770	**713**	**771**	40	713	771	714	770	**713**	**771**
481.wrf	40	**662**	**675**	659	678	664	673	40	**662**	**675**	659	678	664	673
482.sphinx3	40	**1158**	**673**	1162	671	1157	674	40	**1158**	**673**	1162	671	1157	674

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

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SPECfp_rate_base2006 = 700

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Oct-2016
Hardware Availability: Dec-2016
Software Availability: Sep-2016

Platform Notes

```
BIOS Configuration:
  Power Profile set to Custom
  Power Regulator to Static High Performance Mode
  Minimum Processor Idle Power Core C-State set to C6 State
  Minimum Processor Idle Power Package C-State set to No Package State
  Energy/Performance Bias set to Maximum Performance
  Collaborative Power Control set to Disabled
  QPI Snoop Configuration set to Cluster on Die
  Thermal Configuration set to Maximum Cooling
  Processor Power and Utilization Monitoring set to Disabled
  Memory Refresh Rate set to 1x Refresh

Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-xxgs Thu Oct 20 01:23:01 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2640 v4 @ 2.40GHz
      2 "physical id"s (chips)
      40 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 10
      siblings  : 20
      physical 0: cores 0 1 2 3 4 8 9 10 11 12
      physical 1: cores 0 1 2 3 4 8 9 10 11 12
   cache size : 25600 KB

From /proc/meminfo
   MemTotal:       264546416 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 1
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP1"
```

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CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Oct-2016
Hardware Availability: Dec-2016
Software Availability: Sep-2016

Platform Notes (Continued)

```
        VERSION_ID="12.1"
        PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
        ID="sles"
        ANSI_COLOR="0;32"
        CPE_NAME="cpe:/o:suse:sles:12:sp1"

   uname -a:
      Linux linux-xxgs 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
      (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Oct 19 14:11

   SPEC is set to: /home/cpu2006
      Filesystem      Type  Size  Used Avail Use% Mounted on
      /dev/sda4       xfs   424G   68G  357G  16% /home
   Additional information from dmidecode:

      Warning: Use caution when you interpret this section. The 'dmidecode' program
      reads system data which is "intended to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS HP I37 09/14/2016
    Memory:
    8x UNKNOWN NOT AVAILABLE
    16x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz, configured at 2133 MHz

   (End of data from sysinfo program)
   Regarding the sysinfo display about the memory installed, the correct amount of
   memory is 256 GB and the dmidecode description should have one line reading as:
   16x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz, configured at 2133 MHz
```

General Notes

```
  Environment variables set by runspec before the start of the run:
  LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh10.2"

   Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
   memory using Redhat Enterprise Linux 7.2
```

Base Compiler Invocation

C benchmarks:
    icc -m64

C++ benchmarks:
    icpc -m64

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SPECfp_rate2006 =   719

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CPU2006 license: 3	**Test date:** Oct-2016
Test sponsor: HPE	**Hardware Availability:** Dec-2016
Tested by: HPE	**Software Availability:** Sep-2016

Base Compiler Invocation (Continued)

Fortran benchmarks:
    ifort -m64

Benchmarks using both Fortran and C:
    icc -m64 ifort -m64

Base Portability Flags

        410.bwaves: -DSPEC_CPU_LP64
        416.gamess: -DSPEC_CPU_LP64
          433.milc: -DSPEC_CPU_LP64
        434.zeusmp: -DSPEC_CPU_LP64
       435.gromacs: -DSPEC_CPU_LP64 -nofor_main
      436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
       437.leslie3d: -DSPEC_CPU_LP64
          444.namd: -DSPEC_CPU_LP64
         447.dealII: -DSPEC_CPU_LP64
        450.soplex: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
       454.calculix: -DSPEC_CPU_LP64 -nofor_main
      459.GemsFDTD: -DSPEC_CPU_LP64
         465.tonto: -DSPEC_CPU_LP64
          470.lbm: -DSPEC_CPU_LP64
           481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
        482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
    -qopt-mem-layout-trans=3

C++ benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
    -qopt-mem-layout-trans=3

Fortran benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
    -qopt-mem-layout-trans=3

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CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

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Hardware Availability: Dec-2016
Software Availability: Sep-2016

Peak Compiler Invocation

C benchmarks:
      icc -m64

C++ benchmarks (except as noted below):
      icpc -m64

      450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Fortran benchmarks:
      ifort -m64

Benchmarks using both Fortran and C:
      icc -m64 ifort -m64

Peak Portability Flags

        410.bwaves: -DSPEC_CPU_LP64
        416.gamess: -DSPEC_CPU_LP64
          433.milc: -DSPEC_CPU_LP64
        434.zeusmp: -DSPEC_CPU_LP64
       435.gromacs: -DSPEC_CPU_LP64 -nofor_main
      436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
       437.leslie3d: -DSPEC_CPU_LP64
          444.namd: -DSPEC_CPU_LP64
         447.dealII: -DSPEC_CPU_LP64
        450.soplex: -D_FILE_OFFSET_BITS=64
        453.povray: -DSPEC_CPU_LP64
       454.calculix: -DSPEC_CPU_LP64 -nofor_main
      459.GemsFDTD: -DSPEC_CPU_LP64
         465.tonto: -DSPEC_CPU_LP64
          470.lbm: -DSPEC_CPU_LP64
           481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
       482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

        433.milc: basepeak = yes

        470.lbm: basepeak = yes

        482.sphinx3: basepeak = yes

C++ benchmarks:

Continued on next page

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Peak Optimization Flags (Continued)

```
444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
          -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -fno-alias -auto-ilp32
          -qopt-mem-layout-trans=3
```

447.dealII: `basepeak = yes`

```
450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
            -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -qopt-malloc-options=3
            -qopt-mem-layout-trans=3
```

```
453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
            -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3
```

Fortran benchmarks:

410.bwaves: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch`

```
416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
            -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-
```

434.zeusmp: `basepeak = yes`

437.leslie3d: `Same as 410.bwaves`

459.GemsFDTD: `basepeak = yes`

```
465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
           -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -unroll4 -auto -inline-calloc
           -qopt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
             -par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32
             -qopt-mem-layout-trans=3
```

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

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The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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