



# SPEC® CFP2006 Result

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## Huawei

SPECfp®\_rate2006 = 366

Huawei XH622 V3(Intel Xeon E5-2623 v4)

SPECfp\_rate\_base2006 = 359

CPU2006 license: 3175

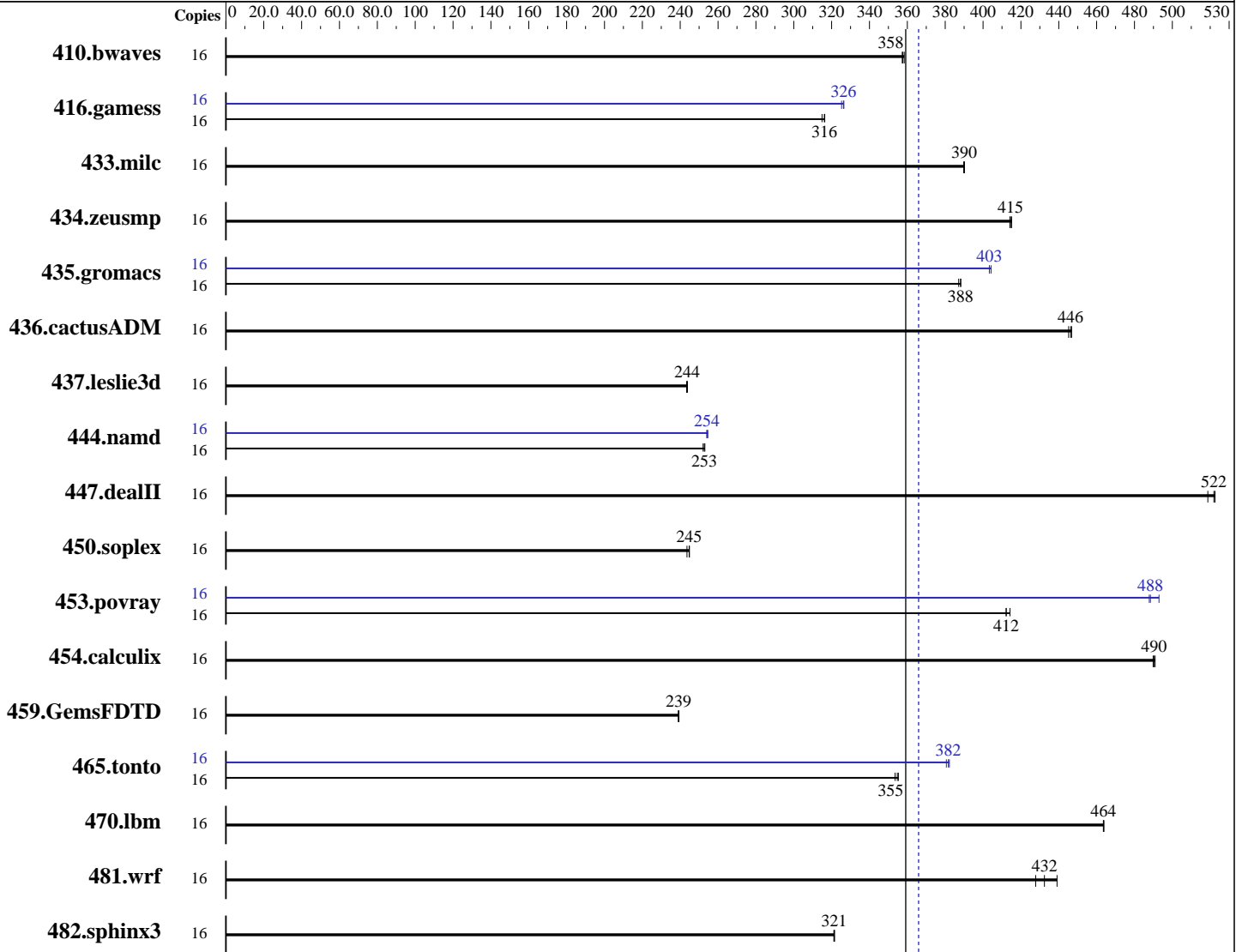
Test sponsor: Huawei

Tested by: Huawei

Test date: Oct-2016

Hardware Availability: Mar-2016

Software Availability: Mar-2016



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### Hardware

CPU Name: Intel Xeon E5-2623 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)

Continued on next page



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L3 Cache: 10 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

| Benchmark     | Base   |            |            |            |            |            |            | Peak   |            |            |            |            |            |            |
|---------------|--------|------------|------------|------------|------------|------------|------------|--------|------------|------------|------------|------------|------------|------------|
|               | Copies | Seconds    | Ratio      | Seconds    | Ratio      | Seconds    | Ratio      | Copies | Seconds    | Ratio      | Seconds    | Ratio      | Seconds    | Ratio      |
| 410.bwaves    | 16     | 607        | 358        | 609        | 357        | <b>608</b> | <b>358</b> | 16     | 607        | 358        | 609        | 357        | <b>608</b> | <b>358</b> |
| 416.gamess    | 16     | <u>991</u> | <u>316</u> | 995        | 315        | 990        | 316        | 16     | 960        | 326        | <u>960</u> | <u>326</u> | 963        | 325        |
| 433.milc      | 16     | <u>377</u> | <u>390</u> | 377        | 390        | 376        | 390        | 16     | <u>377</u> | <u>390</u> | 377        | 390        | 376        | 390        |
| 434.zeusmp    | 16     | 351        | 415        | <u>351</u> | <u>415</u> | 352        | 414        | 16     | 351        | 415        | <u>351</u> | <u>415</u> | 352        | 414        |
| 435.gromacs   | 16     | 294        | 388        | 295        | 387        | <u>294</u> | <u>388</u> | 16     | <u>283</u> | <u>403</u> | 283        | 403        | 283        | 404        |
| 436.cactusADM | 16     | 428        | 447        | <u>428</u> | <u>446</u> | 429        | 445        | 16     | 428        | 447        | <u>428</u> | <u>446</u> | 429        | 445        |
| 437.leslie3d  | 16     | 618        | 243        | 617        | 244        | <u>617</u> | <u>244</u> | 16     | 618        | 243        | 617        | 244        | <u>617</u> | <u>244</u> |
| 444.namd      | 16     | 507        | 253        | 509        | 252        | <u>508</u> | <u>253</u> | 16     | <u>505</u> | <u>254</u> | 505        | 254        | 504        | 255        |
| 447.dealII    | 16     | 350        | 523        | <u>351</u> | <u>522</u> | 353        | 519        | 16     | 350        | 523        | <u>351</u> | <u>522</u> | 353        | 519        |
| 450.soplex    | 16     | 548        | 244        | 545        | 245        | <u>545</u> | <u>245</u> | 16     | 548        | 244        | 545        | 245        | <u>545</u> | <u>245</u> |
| 453.povray    | 16     | 206        | 412        | <u>206</u> | <u>412</u> | 205        | 414        | 16     | 174        | 488        | 173        | 493        | <u>174</u> | <u>488</u> |
| 454.calculix  | 16     | 269        | 490        | <u>269</u> | <u>490</u> | 269        | 491        | 16     | 269        | 490        | <u>269</u> | <u>490</u> | 269        | 491        |
| 459.GemsFDTD  | 16     | 709        | 239        | <u>710</u> | <u>239</u> | 710        | 239        | 16     | 709        | 239        | <u>710</u> | <u>239</u> | 710        | 239        |
| 465.tonto     | 16     | <u>444</u> | <u>355</u> | 443        | 355        | 445        | 354        | 16     | <u>413</u> | <u>382</u> | 412        | 382        | 414        | 381        |
| 470.lbm       | 16     | <u>474</u> | <u>464</u> | 474        | 464        | 474        | 464        | 16     | <u>474</u> | <u>464</u> | 474        | 464        | 474        | 464        |
| 481.wrf       | 16     | <u>413</u> | <u>432</u> | 418        | 428        | 407        | 439        | 16     | <u>413</u> | <u>432</u> | 418        | 428        | 407        | 439        |
| 482.sphinx3   | 16     | 971        | 321        | 970        | 322        | <u>970</u> | <u>321</u> | 16     | 971        | 321        | 970        | 322        | <u>970</u> | <u>321</u> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Performance  
Set Snoop Mode to ES mode  
Set Patrol Scrub to Disable  
Sysinfo program /spec16/config/sysinfo.rev6914

Continued on next page



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CPU2006 license: 3175  
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Tested by: Huawei

Test date: Oct-2016  
Hardware Availability: Mar-2016  
Software Availability: Mar-2016

## Platform Notes (Continued)

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on linux-j81m Sat Oct 29 01:18:21 2016

This section contains SUT (System Under Test) info as seen by  
some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2623 v4 @ 2.60GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 8
  physical 0: cores 0 1 2 3
  physical 1: cores 0 1 2 3
 cache size : 10240 KB
```

```
From /proc/meminfo
MemTotal:      264077600 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
Linux linux-j81m 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 25 12:54
```

```
SPEC is set to: /spec16
```

Continued on next page



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## Platform Notes (Continued)

| Filesystem | Type | Size | Used | Avail | Use% | Mounted on |
|------------|------|------|------|-------|------|------------|
| /dev/sda3  | ext4 | 884G | 44G  | 840G  | 5%   | /          |

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016

Memory:  
16x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

The Huawei XH622 V3 and Huawei XH628 V3 and Huawei XH620 V3 are electronically equivalent.

The results have been measured on a Huawei XH620 V3 model

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64



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Software Availability: Mar-2016

## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Continued on next page



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## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

Continued on next page



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## Peak Optimization Flags (Continued)

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto  
-inline-alloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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