



SPEC® CFP2006 Result

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Fujitsu

PRIMERGY CX2550 M2, Intel Xeon E5-2650 v4, 2.20 GHz

SPECfp®2006 = 112
SPECfp_base2006 = 108

CPU2006 license: 19

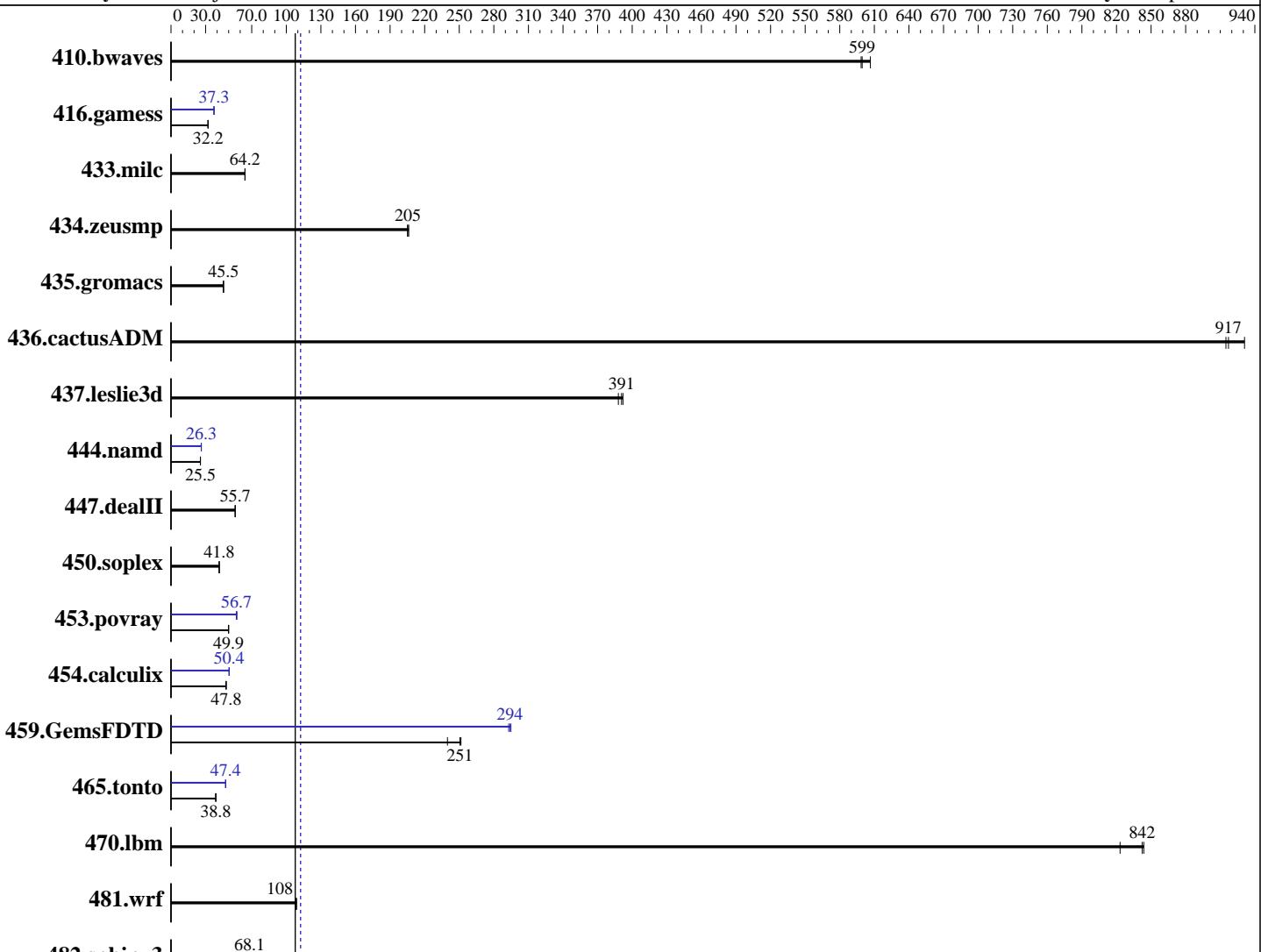
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2016

Hardware Availability: Apr-2016

Software Availability: Sep-2015



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Hardware

CPU Name: Intel Xeon E5-2650 v4
CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
Compiler: Kernel 3.12.49-11-default
C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)

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L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
 Disk Subsystem: 1 x SATA, 1000 GB, 7200 RPM
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	<u>22.7</u>	<u>599</u>	22.7	599	22.4	607	<u>22.7</u>	<u>599</u>	22.7	599	22.4	607
416.gamess	611	32.0	<u>609</u>	<u>32.2</u>	608	32.2	524	37.3	525	37.3	<u>525</u>	<u>37.3</u>
433.milc	143	64.2	143	64.2	<u>143</u>	<u>64.2</u>	143	64.2	143	64.2	<u>143</u>	<u>64.2</u>
434.zeusmp	44.4	205	<u>44.3</u>	<u>205</u>	44.1	206	44.4	205	<u>44.3</u>	<u>205</u>	44.1	206
435.gromacs	<u>157</u>	<u>45.5</u>	157	45.6	157	45.4	<u>157</u>	<u>45.5</u>	157	45.6	157	45.4
436.cactusADM	<u>13.0</u>	<u>917</u>	12.8	931	13.1	915	<u>13.0</u>	<u>917</u>	12.8	931	13.1	915
437.leslie3d	24.2	388	<u>24.1</u>	<u>391</u>	24.0	392	24.2	388	<u>24.1</u>	<u>391</u>	24.0	392
444.namd	<u>314</u>	<u>25.5</u>	314	25.5	314	25.5	305	26.3	306	26.2	<u>305</u>	<u>26.3</u>
447.dealII	206	55.6	205	55.8	<u>205</u>	<u>55.7</u>	206	55.6	205	55.8	<u>205</u>	<u>55.7</u>
450.soplex	199	41.9	<u>199</u>	<u>41.8</u>	200	41.7	199	41.9	<u>199</u>	<u>41.8</u>	200	41.7
453.povray	107	49.8	<u>107</u>	<u>49.9</u>	106	50.0	93.9	56.6	92.9	57.2	<u>93.8</u>	<u>56.7</u>
454.calculix	173	47.7	172	47.9	<u>173</u>	<u>47.8</u>	164	50.3	<u>164</u>	<u>50.4</u>	163	50.6
459.GemsFDTD	44.3	240	<u>42.3</u>	<u>251</u>	42.2	251	36.0	295	<u>36.1</u>	<u>294</u>	36.2	293
465.tonto	252	39.0	<u>254</u>	<u>38.8</u>	254	38.8	207	47.5	208	47.3	<u>208</u>	<u>47.4</u>
470.lbm	16.7	823	16.3	844	<u>16.3</u>	<u>842</u>	16.7	823	16.3	844	<u>16.3</u>	<u>842</u>
481.wrf	103	109	104	108	<u>103</u>	<u>108</u>	103	109	104	108	<u>103</u>	<u>108</u>
482.sphinx3	<u>286</u>	<u>68.1</u>	285	68.3	286	68.1	<u>286</u>	<u>68.1</u>	285	68.3	286	68.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:

Energy Performance = Performance

Utilization Profile = Unbalanced

QPI snoop mode: Home Snoop

COD Enable = Disabled, Early Snoop = Disabled, Home Snoop Dir OSB = Disabled

CPU C1E Support = Disabled

Sysinfo program /home/SPECcpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 ## e3fb8667b5a285932ceab81e28219e1

running on CX2550M2 Tue Jul 26 08:52:07 2016

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info@spec.org

http://www.spec.org/

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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2650 v4@ 2.20GHz
        2 "physical id"s (chips)
        48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 12
    siblings   : 24
    physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
    physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
    cache size : 30720 KB
```

```
From /proc/meminfo
MemTotal:      264318868 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
    NAME="SLES"
    VERSION="12-SP1"
    VERSION_ID="12.1"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
Linux CX2550M2 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jul 26 08:51 last=5
```

```
SPEC is set to: /home/SPECcpu2006
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sda3        xfs   890G   90G  800G  11% /home
Additional information from dmidecode:
```

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Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS FUJITSU // American Megatrends Inc. V5.0.0.11 R1.4.0 for D3343-B1x
03/17/2016

Memory:

16x Hyundai Electronics (Hynix) HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

OMP_NUM_THREADS = "24"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
437.leslie3d: -DSPEC_CPU_LP64  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
450.soplex: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
470.lbm: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX  
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

447.dealII: basepeak = yes

450.soplex: basepeak = yes

```
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14
            -ansi-alias
```

Fortran benchmarks:

410.bwaves: basepeak = yes

```
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
             -inline-level=0 -scalar-rep-
```

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

```
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
                -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
                -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
                -inline-level=0 -opt-prefetch -parallel
```

```
465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
```

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Peak Optimization Flags (Continued)

465.tonto (continued):

-opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-BDW-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-BDW-RevB.xml>

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