



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = 396

PowerEdge R530 (Intel Xeon E5-2623 v4, 2.60 GHz)

SPECint_rate_base2006 = 377

CPU2006 license: 55

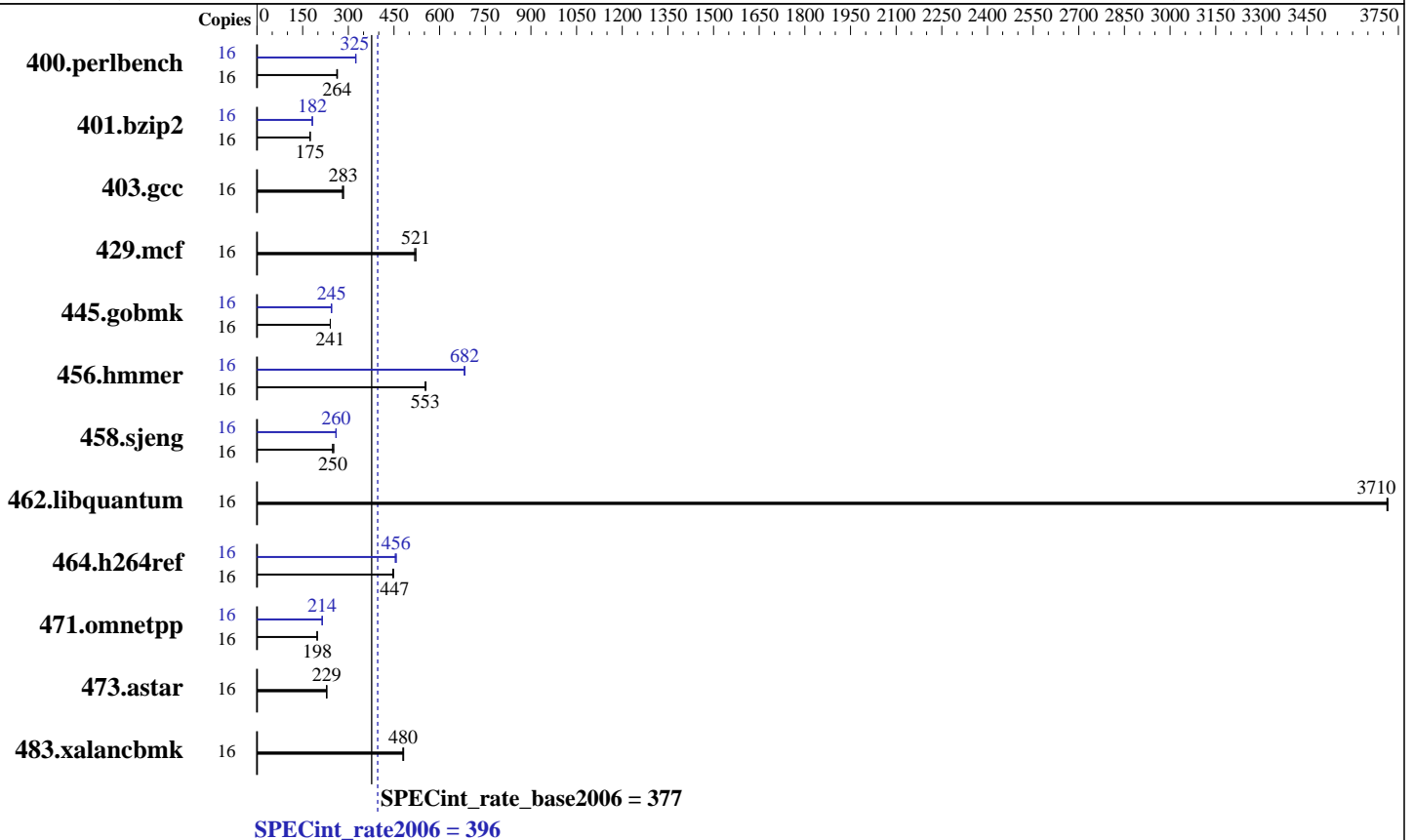
Test date: Jun-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Dec-2015



Hardware

CPU Name: Intel Xeon E5-2623 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 10 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (8 x 16 GB 2Rx8 PC4-2400T-R, running at 2133 MHz)
 Disk Subsystem: 1 x 250 GB 7200 RPM SATA HDD
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default
 Compiler: C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	592	264	<u>593</u>	<u>264</u>	597	262	16	483	324	481	325	<u>482</u>	<u>325</u>
401.bzip2	16	884	175	886	174	<u>884</u>	<u>175</u>	16	<u>850</u>	<u>182</u>	849	182	850	182
403.gcc	16	453	284	<u>455</u>	<u>283</u>	459	281	16	453	284	<u>455</u>	<u>283</u>	459	281
429.mcf	16	<u>280</u>	<u>521</u>	282	518	280	522	16	<u>280</u>	<u>521</u>	282	518	280	522
445.gobmk	16	<u>697</u>	<u>241</u>	696	241	697	241	16	688	244	684	245	<u>686</u>	<u>245</u>
456.hammer	16	<u>270</u>	<u>553</u>	269	555	270	553	16	219	681	219	683	<u>219</u>	<u>682</u>
458.sjeng	16	767	253	781	248	<u>775</u>	<u>250</u>	16	745	260	746	259	<u>746</u>	<u>260</u>
462.libquantum	16	89.2	3720	89.3	3710	<u>89.3</u>	<u>3710</u>	16	89.2	3720	89.3	3710	<u>89.3</u>	<u>3710</u>
464.h264ref	16	793	446	789	449	<u>792</u>	<u>447</u>	16	774	458	<u>776</u>	<u>456</u>	780	454
471.omnetpp	16	<u>506</u>	<u>198</u>	507	197	505	198	16	468	214	<u>466</u>	<u>214</u>	466	214
473.astar	16	492	228	490	229	<u>491</u>	<u>229</u>	16	492	228	490	229	<u>491</u>	<u>229</u>
483.xalancbmk	16	229	482	<u>230</u>	<u>480</u>	230	479	16	229	482	<u>230</u>	<u>480</u>	230	479

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
 Snoop Mode set to Cluster on Die
 Virtualization Technology disabled
 System Profile set to custom
 CPU Power Management set to Hardware P States
 C States set to Autonomous
 C1E disabled
 Energy Efficient Turbo disabled
 Uncore Frequency set to Dynamic
 Energy Efficiency Policy set to Balanced Performance
 Memory Patrol Scrub disabled
 Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
 running on linux-g0aw Wed Jun 1 18:42:12 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-2623 v4@ 2.60GHz
 2 "physical id"s (chips)
 16 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 4
siblings   : 8
physical 0: cores 0 1 2 3
physical 1: cores 0 1 2 3
cache size : 10240 KB
```

From /proc/meminfo

```
MemTotal:      132185432 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

/usr/bin/lsb_release -d

```
SUSE Linux Enterprise Server 12 SP1
```

From /etc/*release* /etc/*version*

SuSE-release:

```
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
```

This file is deprecated and will be removed in a future service pack or release.

Please check /etc/os-release for details about this release.

os-release:

```
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

uname -a:

```
Linux linux-g0aw 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jun 1 18:38

SPEC is set to: /root/cpu2006-1.2

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext4  221G  8.7G  212G   4% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
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Platform Notes (Continued)

reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 2.0.1 04/11/2016

Memory:

7x 00AD063200AD HMA82GR7MFR8N-UH 16 GB 2 rank 2400 MHz, configured at 2133 MHz

1x 00CE00B300CE M393A2K43BB1-CRC 16 GB 2 rank 2400 MHz, configured at 2133 MHz

4x Not Specified Not Specified

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB memory using RedHat EL 7.2 glibc 2.17

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32

401.bzip2: -D_FILE_OFFSET_BITS=64

403.gcc: -D_FILE_OFFSET_BITS=64

429.mcf: -D_FILE_OFFSET_BITS=64

445.gobmk: -D_FILE_OFFSET_BITS=64

456.hmmer: -D_FILE_OFFSET_BITS=64

458.sjeng: -D_FILE_OFFSET_BITS=64

462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.aster: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

```

403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.aster: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
               -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
               -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
            -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias
            -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
            -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
              -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
              -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
              -ansi-alias

```

C++ benchmarks:

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Peak Optimization Flags (Continued)

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml>

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Tested with SPEC CPU2006 v1.2.

Report generated on Tue Aug 9 17:04:18 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 9 August 2016.