



SPEC® CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp®2006 = 111

PowerEdge R830 (Intel Xeon E5-4650 v4, 2.20 GHz)

SPECfp_base2006 = 105

CPU2006 license: 55

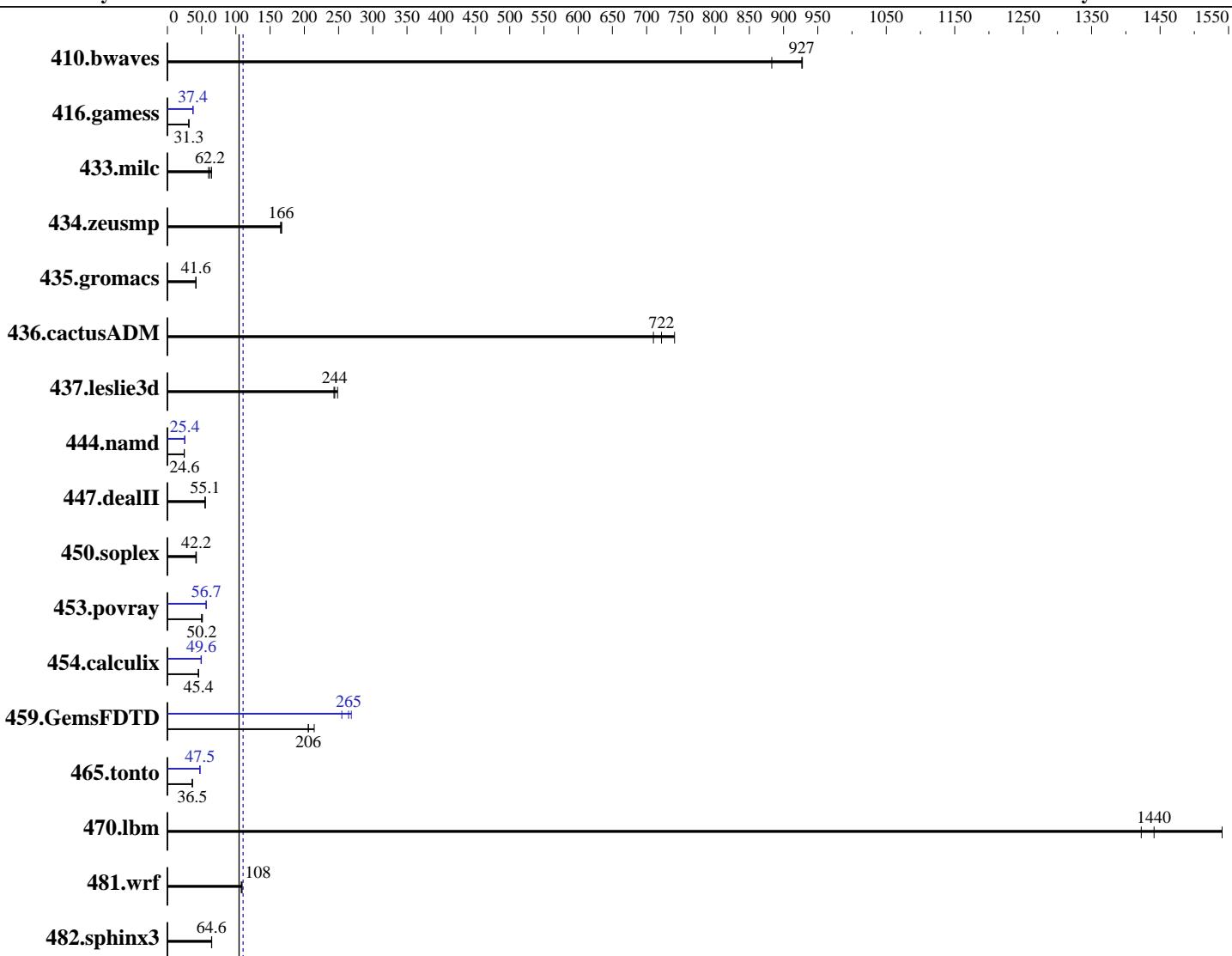
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Apr-2016

Hardware Availability: Jun-2016

Software Availability: Dec-2015



Hardware

CPU Name: Intel Xeon E5-4650 v4
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 56 cores, 4 chips, 14 cores/chip, 2 threads/core
CPU(s) orderable: 2,4 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: btrfs
System State: Run level 3 (multi-user)

Continued on next page

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp2006 = 111

PowerEdge R830 (Intel Xeon E5-4650 v4, 2.20 GHz)

SPECfp_base2006 = 105

CPU2006 license: 55

Test date: Apr-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Dec-2015

L3 Cache: 35 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (32 x 16 GB 2Rx8 PC4-2400T-R)
 Disk Subsystem: 1 x 800 GB SATA SSD
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	15.4	883	14.7	927	14.7	928	15.4	883	14.7	927	14.7	928
416.gamess	627	31.2	625	31.3	625	31.3	524	37.4	525	37.3	524	37.4
433.milc	153	59.9	148	62.2	142	64.6	153	59.9	148	62.2	142	64.6
434.zeusmp	54.5	167	55.1	165	54.7	166	54.5	167	55.1	165	54.7	166
435.gromacs	171	41.8	171	41.6	171	41.6	171	41.8	171	41.6	171	41.6
436.cactusADM	16.1	741	16.8	710	16.6	722	16.1	741	16.8	710	16.6	722
437.leslie3d	38.6	243	37.8	249	38.5	244	38.6	243	37.8	249	38.5	244
444.namd	326	24.6	326	24.6	326	24.6	316	25.4	316	25.4	317	25.3
447.dealII	208	55.0	208	55.1	207	55.3	208	55.0	208	55.1	207	55.3
450.soplex	198	42.2	201	41.5	198	42.2	198	42.2	201	41.5	198	42.2
453.povray	104	51.2	107	49.9	106	50.2	94.1	56.6	93.9	56.7	93.8	56.7
454.calculix	182	45.3	182	45.4	182	45.4	166	49.6	168	49.1	166	49.6
459.GemsFDTD	49.5	214	51.5	206	51.6	206	39.5	269	40.1	265	41.6	255
465.tonto	269	36.5	271	36.3	270	36.5	207	47.4	207	47.5	207	47.6
470.lbm	8.92	1540	9.66	1420	9.53	1440	8.92	1540	9.66	1420	9.53	1440
481.wrf	102	109	103	108	103	108	102	109	103	108	103	108
482.sphinx3	302	64.6	301	64.8	302	64.5	302	64.6	301	64.8	302	64.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:

Snoop Mode set to Home Snoop

Virtualization Technology disabled

System Profile set to custom

CPU Power Management set to Maximum Performance

C States set to Autonomous

C1E disabled

Energy Efficient Turbo disabled

Uncore Frequency set to Dynamic

Energy Efficiency Policy set to Performance

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp2006 =

111

PowerEdge R830 (Intel Xeon E5-4650 v4, 2.20 GHz)

SPECfp_base2006 =

105

CPU2006 license: 55

Test date: Apr-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Dec-2015

Platform Notes (Continued)

Memory Patrol Scrub disabled

Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\\$ e3fbb8667b5a285932ceab81e28219e1
running on linux-t2sb Mon Apr 25 07:09:45 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-4650 v4 @ 2.20GHz
 4 "physical id"s (chips)
 112 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
 cpu cores : 14
 siblings : 28
 physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
 physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
 physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
 physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB

From /proc/meminfo
MemTotal: 529325584 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
SuSE-release:
 SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
This file is deprecated and will be removed in a future service pack or release.
Please check /etc/os-release for details about this release.
os-release:
 NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
Linux linux-t2sb 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp2006 = 111

PowerEdge R830 (Intel Xeon E5-4650 v4, 2.20 GHz)

SPECfp_base2006 = 105

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Apr-2016

Hardware Availability: Jun-2016

Software Availability: Dec-2015

Platform Notes (Continued)

run-level 3 Apr 25 01:18

```
SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3        btrfs  461G   11G  447G   3% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 0.2.4 03/31/2016

Memory:

```
32x 00AD063200AD HMA82GR7MFR8N-UH 16 GB 2 rank 2400 MHz
16x Not Specified Not Specified
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/root/cpu2006-1.2/lib32:/root/cpu2006-1.2/lib64:/root/cpu2006-1.2/sh"

OMP_NUM_THREADS = "56"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R830 (Intel Xeon E5-4650 v4, 2.20 GHz)

SPECfp2006 =

111

SPECfp_base2006 =

105

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date:

Apr-2016

Hardware Availability: Jun-2016

Software Availability: Dec-2015

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R830 (Intel Xeon E5-4650 v4, 2.20 GHz)

SPECfp2006 =

111

SPECfp_base2006 =

105

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date:

Apr-2016

Hardware Availability: Jun-2016

Software Availability: Dec-2015

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R830 (Intel Xeon E5-4650 v4, 2.20 GHz)

SPECfp2006 =

111

SPECfp_base2006 =

105

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date:

Apr-2016

Hardware Availability:

Jun-2016

Software Availability:

Dec-2015

Peak Optimization Flags (Continued)

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Jul 26 16:13:13 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 26 July 2016.