



# SPEC® CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen9

(2.10 GHz, Intel Xeon E5-2620 v4)

SPECint®\_rate2006 = 662

SPECint\_rate\_base2006 = 631

CPU2006 license: 3

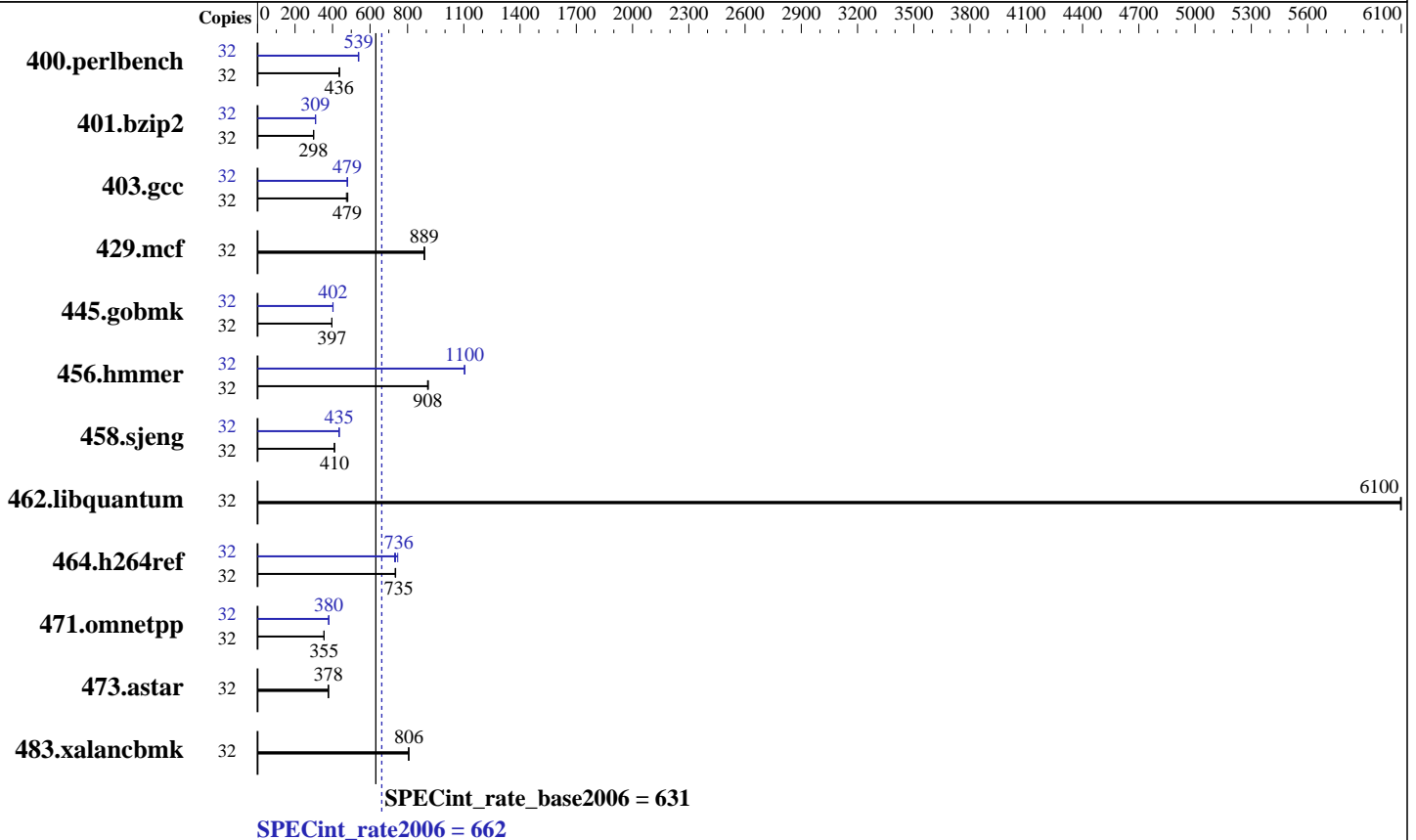
Test sponsor: HPE

Tested by: HPE

Test date: Jun-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015



## Hardware

CPU Name: Intel Xeon E5-2620 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)  
 Disk Subsystem: 2 x 400 GB SAS SSD, RAID 1  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 Kernel 3.10.0-327.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen9

(2.10 GHz, Intel Xeon E5-2620 v4)

SPECint\_rate2006 = 662

SPECint\_rate\_base2006 = 631

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Jun-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	718	435	716	437	<b><u>717</u></b>	<b><u>436</u></b>	32	<b><u>580</u></b>	<b><u>539</u></b>	580	539	582	537
401.bzip2	32	1035	298	<b><u>1035</u></b>	<b><u>298</u></b>	1030	300	32	<b><u>999</u></b>	<b><u>309</u></b>	998	309	1001	309
403.gcc	32	535	481	<b><u>538</u></b>	<b><u>479</u></b>	544	474	32	536	480	539	478	<b><u>537</u></b>	<b><u>479</u></b>
429.mcf	32	328	889	<b><u>328</u></b>	<b><u>889</u></b>	327	891	32	328	889	<b><u>328</u></b>	<b><u>889</u></b>	327	891
445.gobmk	32	847	396	846	397	<b><u>846</u></b>	<b><u>397</u></b>	32	836	402	833	403	<b><u>835</u></b>	<b><u>402</u></b>
456.hammer	32	328	911	329	907	<b><u>329</u></b>	<b><u>908</u></b>	32	270	1100	271	1100	<b><u>270</u></b>	<b><u>1100</u></b>
458.sjeng	32	945	410	<b><u>944</u></b>	<b><u>410</u></b>	943	411	32	<b><u>889</u></b>	<b><u>435</u></b>	891	435	889	436
462.libquantum	32	<b><u>109</u></b>	<b><u>6100</u></b>	109	6100	109	6100	32	<b><u>109</u></b>	<b><u>6100</u></b>	109	6100	109	6100
464.h264ref	32	<b><u>963</u></b>	<b><u>735</u></b>	964	734	961	737	32	<b><u>963</u></b>	<b><u>736</u></b>	969	731	947	748
471.omnetpp	32	564	355	565	354	<b><u>564</u></b>	<b><u>355</u></b>	32	526	380	527	379	<b><u>527</u></b>	<b><u>380</u></b>
473.astar	32	593	379	596	377	<b><u>594</u></b>	<b><u>378</u></b>	32	593	379	596	377	<b><u>594</u></b>	<b><u>378</u></b>
483.xalanbmk	32	274	805	274	807	<b><u>274</u></b>	<b><u>806</u></b>	32	274	805	274	807	<b><u>274</u></b>	<b><u>806</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Platform Notes

BIOS Configuration:  
HP Power Profile set to Custom  
HP Power Regulator to HP Static High Performance Mode  
Minimum Processor Idle Power Core C-State set to C1E State  
Minimum Processor Idle Power Package C-State set to No Package State  
QPI Snoop Configuration set to Cluster on Die  
Collaborative Power Control set to Disabled  
Thermal Configuration set to Maximum Cooling  
Processor Power and Utilization Monitoring set to Disabled  
Memory Refresh Rate set to 1x Refresh  
Intel Hyperthreading set to Enabled

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL360 Gen9**

(2.10 GHz, Intel Xeon E5-2620 v4)

**SPECint\_rate2006 = 662**

**SPECint\_rate\_base2006 = 631**

**CPU2006 license:** 3

**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Jun-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Nov-2015

## Platform Notes (Continued)

```
Sysinfo program /cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Fri Jun 17 10:48:19 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2620 v4 @ 2.10GHz
 2 "physical id"s (chips)
 32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 8
  siblings  : 16
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      528066716 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server
```

```
uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jun 17 10:46

```
SPEC is set to: /cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4       xfs   368G  125G  243G  35% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program  
Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen9

(2.10 GHz, Intel Xeon E5-2620 v4)

SPECint\_rate2006 = 662

SPECint\_rate\_base2006 = 631

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Jun-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

## Platform Notes (Continued)

reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P89 02/22/2016

Memory:

8x UNKNOWN NOT AVAILABLE

16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as: 16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmer: -D\_FILE\_OFFSET\_BITS=64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen9

(2.10 GHz, Intel Xeon E5-2620 v4)

SPECint\_rate2006 = 662

SPECint\_rate\_base2006 = 631

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Jun-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64

403.gcc: -D\_FILE\_OFFSET\_BITS=64

429.mcf: -D\_FILE\_OFFSET\_BITS=64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmer: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64

458.sjeng: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64

462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

464.h264ref: -D\_FILE\_OFFSET\_BITS=64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -D\_FILE\_OFFSET\_BITS=64

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 5



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen9

(2.10 GHz, Intel Xeon E5-2620 v4)

SPECint\_rate2006 = 662

SPECint\_rate\_base2006 = 631

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Jun-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

## Peak Portability Flags (Continued)

483.xalanbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias  
-opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen9

(2.10 GHz, Intel Xeon E5-2620 v4)

SPECint\_rate2006 = 662

SPECint\_rate\_base2006 = 631

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Jun-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015

## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Jul 26 16:12:43 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 26 July 2016.

Standard Performance Evaluation Corporation

[info@spec.org](mailto:info@spec.org)

<http://www.spec.org/>

Page 7