SPEC® CINT2006 Result

Lenovo Group Limited

Lenovo ThinkServer RD650
(2.40 GHz, Intel Xeon E5-2640 v4)

SPECint®_rate2006 =   869

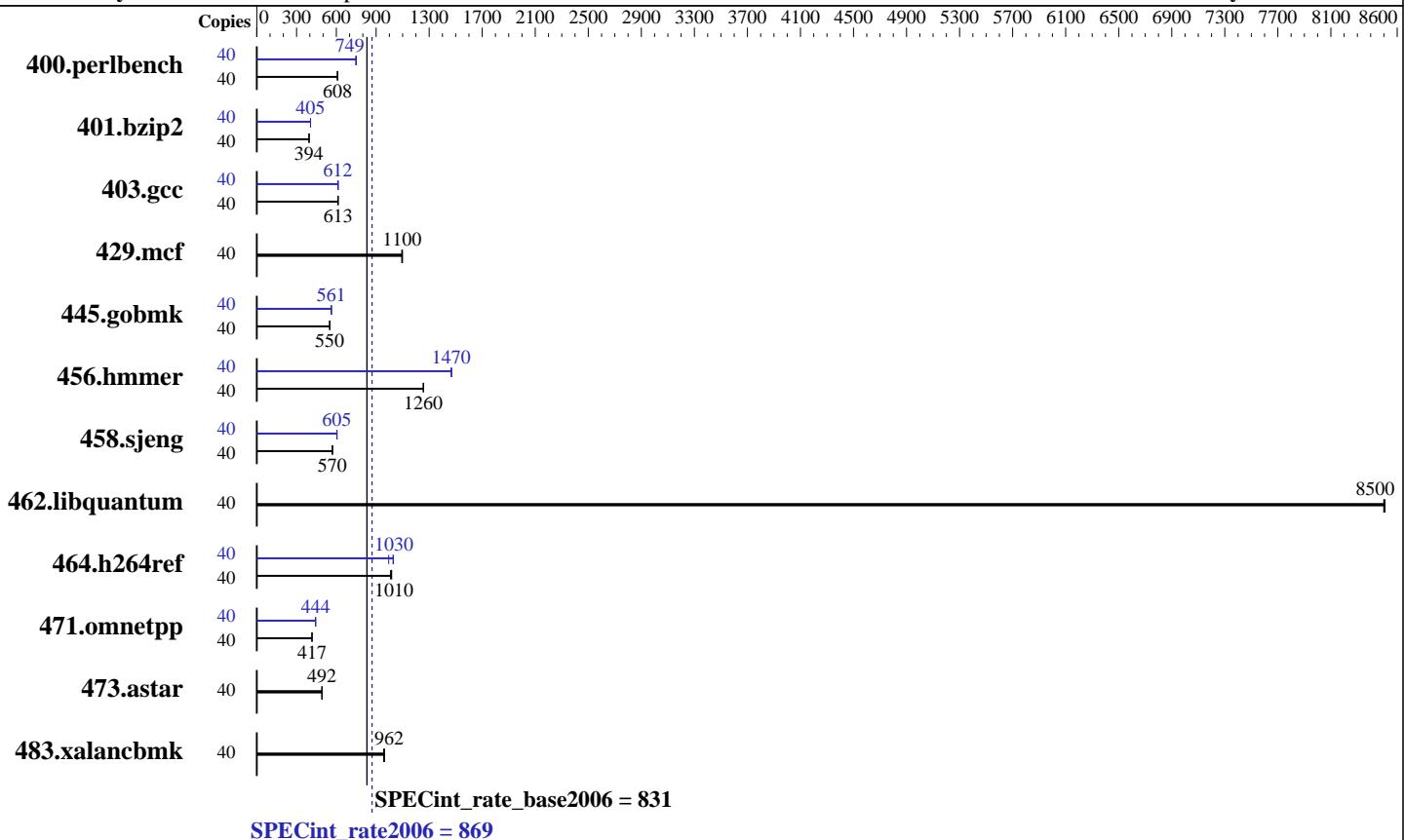SPECint_rate_base2006 =   831

CPU2006 license: 9017

Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

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Hardware

CPU Name:	Intel Xeon E5-2640 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.40 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)
Disk Subsystem:	1 x 800 GB SATA SSD
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 12 SP1 (x86_64) Kernel 3.12.49-11-default
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2

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Results Table

| Benchmark | Base | | | | | | Peak | | | | | | |
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	642	608	**642**	**608**	642	609	40	523	747	**522**	**749**	521	750
401.bzip2	40	979	394	**979**	**394**	980	394	40	952	405	954	405	**952**	**405**
403.gcc	40	525	614	**525**	**613**	526	612	40	**526**	**612**	524	615	526	612
429.mcf	40	**333**	**1100**	334	1090	332	1100	40	**333**	**1100**	334	1090	332	1100
445.gobmk	40	762	550	764	550	**763**	**550**	40	**747**	**561**	748	561	744	564
456.hmmer	40	297	1260	**297**	**1260**	297	1260	40	**254**	**1470**	254	1470	255	1470
458.sjeng	40	848	571	849	570	**848**	**570**	40	**800**	**605**	801	604	800	605
462.libquantum	40	**97.5**	**8500**	97.4	8510	97.5	8500	40	**97.5**	**8500**	97.4	8510	97.5	8500
464.h264ref	40	878	1010	**873**	**1010**	870	1020	40	**862**	**1030**	890	995	859	1030
471.omnetpp	40	601	416	600	417	**600**	**417**	40	564	444	564	443	**564**	**444**
473.astar	40	572	491	**571**	**492**	571	492	40	572	491	**571**	**492**	571	492
483.xalancbmk	40	288	957	287	963	**287**	**962**	40	288	957	287	963	**287**	**962**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches

Platform Notes

BIOS Configuration:
Cluster On Die set to Disabled
Early Snoop set to Disabled
Performance Profile set to Custom
C1E Support set to Disabled
Core C3 set to Disabled
Core C6 set to Disabled
Thermal Profile set to High Fan Speed
Memory Power Savings set to Disabled
 Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
 $Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
 running on rd650-mlk-rackA01 Tue May 24 21:26:58 2016

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Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2640 v4 @ 2.40GHz
      2 "physical id"s (chips)
      40 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 10
      siblings  : 20
      physical 0: cores 0 1 2 3 4 8 9 10 11 12
      physical 1: cores 0 1 2 3 4 8 9 10 11 12
   cache size : 25600 KB

From /proc/meminfo
   MemTotal:       264391384 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 1
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP1"
      VERSION_ID="12.1"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
   Linux rd650-mlk-rackA01 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC
   2015 (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 24 21:25

SPEC is set to: /home/cpu2006-1.2-ic16.0
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda3      xfs   100G   11G   90G  11% /home
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
```
<div align="center">Continued on next page</div>

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Platform Notes (Continued)

```
  determined", but the intent may not be met, as there are frequent changes to
  hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS LENOVO PB2TS335 01/09/2016
   Memory:
   16x Hynix Semiconductor HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz, configured at
   2133 MHz
   8x NO DIMM NO DIMM

 (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"


 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>        /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
    icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

C++ benchmarks:
```
    icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

Base Portability Flags

```
  400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
      401.bzip2: -D_FILE_OFFSET_BITS=64
        403.gcc: -D_FILE_OFFSET_BITS=64
        429.mcf: -D_FILE_OFFSET_BITS=64
      445.gobmk: -D_FILE_OFFSET_BITS=64
      456.hmmer: -D_FILE_OFFSET_BITS=64
      458.sjeng: -D_FILE_OFFSET_BITS=64
  462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
     464.h264ref: -D_FILE_OFFSET_BITS=64
     471.omnetpp: -D_FILE_OFFSET_BITS=64
       473.astar: -D_FILE_OFFSET_BITS=64
```

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Base Portability Flags (Continued)

    483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
    -opt-mem-layout-trans=3

C++ benchmarks:

    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
    -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

    icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

    400.perlbench: icc -m64

       401.bzip2: icc -m64

      456.hmmer: icc -m64

       458.sjeng: icc -m64

C++ benchmarks:

    icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Peak Portability Flags

    400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
       401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
         403.gcc: -D_FILE_OFFSET_BITS=64
         429.mcf: -D_FILE_OFFSET_BITS=64
      445.gobmk: -D_FILE_OFFSET_BITS=64

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Peak Portability Flags (Continued)

```
    456.hmmer: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
    458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
   464.h264ref: -D_FILE_OFFSET_BITS=64
   471.omnetpp: -D_FILE_OFFSET_BITS=64
     473.astar: -D_FILE_OFFSET_BITS=64
 483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
                 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
                 -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

      401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
                 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
                 -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
                 -auto-ilp32 -ansi-alias

        403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

        429.mcf: basepeak = yes

      445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
                 -prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias
                 -opt-mem-layout-trans=3

      456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

      458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
                 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
                 -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
                 -auto-ilp32

  462.libquantum: basepeak = yes

    464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
                 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
                 -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
                 -ansi-alias
```

C++ benchmarks:

```
    471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
                 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
                 -par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias
                 -opt-ra-region-strategy=block -Wl,-z,muldefs
```

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Peak Optimization Flags (Continued)

471.omnetpp (continued):
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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