



# SPEC® CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECint®\_rate2006 = 741

Lenovo Flex System x240 M5  
(3.40 GHz, Intel Xeon E5-2643 v4)

SPECint\_rate\_base2006 = 705

CPU2006 license: 9017

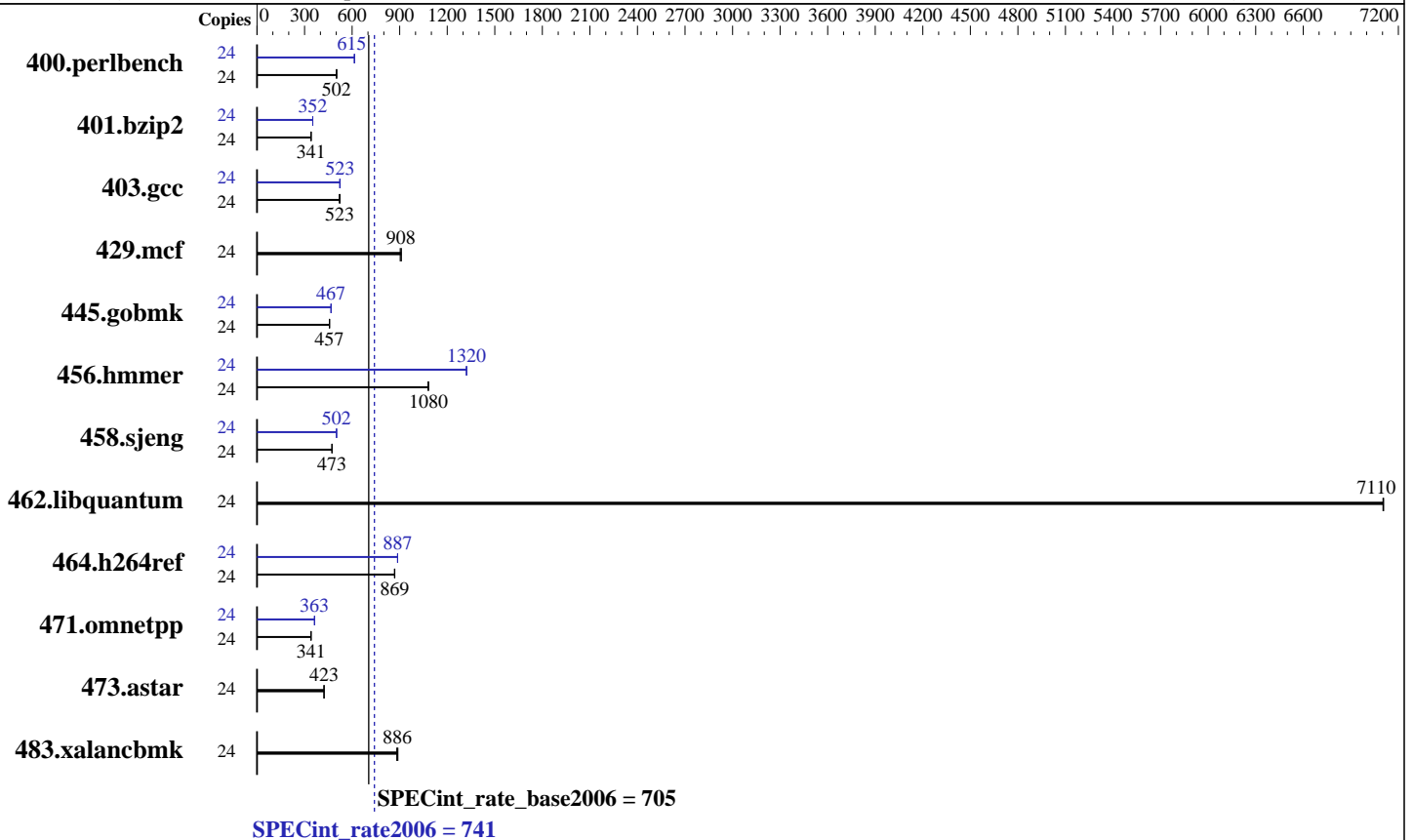
Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015



### Hardware

CPU Name: Intel Xeon E5-2643 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)  
 Disk Subsystem: 2 x 300 GB 10000 RPM SAS, RAID 0  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86\_64)  
 Kernel 3.12.49-11-default  
 Compiler: C/C++; Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECint\_rate2006 = 741

Lenovo Flex System x240 M5  
(3.40 GHz, Intel Xeon E5-2643 v4)

SPECint\_rate\_base2006 = 705

CPU2006 license: 9017

Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	<b><u>467</u></b>	<b><u>502</u></b>	466	503	469	500	24	382	613	381	616	<b><u>381</u></b>	<b><u>615</u></b>
401.bzip2	24	678	342	680	341	<b><u>679</u></b>	<b><u>341</u></b>	24	<b><u>659</u></b>	<b><u>352</u></b>	659	352	658	352
403.gcc	24	<b><u>370</u></b>	<b><u>523</u></b>	369	524	373	519	24	<b><u>369</u></b>	<b><u>523</u></b>	370	522	368	525
429.mcf	24	240	910	<b><u>241</u></b>	<b><u>908</u></b>	242	903	24	240	910	<b><u>241</u></b>	<b><u>908</u></b>	242	903
445.gobmk	24	<b><u>551</u></b>	<b><u>457</u></b>	551	457	550	458	24	539	467	539	467	<b><u>539</u></b>	<b><u>467</u></b>
456.hammer	24	207	1080	207	1080	<b><u>207</u></b>	<b><u>1080</u></b>	24	170	1320	<b><u>169</u></b>	<b><u>1320</u></b>	169	1320
458.sjeng	24	614	473	<b><u>614</u></b>	<b><u>473</u></b>	614	473	24	<b><u>579</u></b>	<b><u>502</u></b>	578	502	579	501
462.libquantum	24	70.0	7100	70.0	7110	<b><u>70.0</u></b>	<b><u>7110</u></b>	24	70.0	7100	70.0	7110	<b><u>70.0</u></b>	<b><u>7110</u></b>
464.h264ref	24	<b><u>611</u></b>	<b><u>869</u></b>	613	867	611	869	24	<b><u>599</u></b>	<b><u>887</u></b>	599	887	599	886
471.omnetpp	24	441	340	<b><u>440</u></b>	<b><u>341</u></b>	440	341	24	414	363	<b><u>413</u></b>	<b><u>363</u></b>	413	363
473.astar	24	398	423	<b><u>398</u></b>	<b><u>423</u></b>	399	422	24	398	423	<b><u>398</u></b>	<b><u>423</u></b>	399	422
483.xalancbmk	24	186	889	<b><u>187</u></b>	<b><u>886</u></b>	188	882	24	186	889	<b><u>187</u></b>	<b><u>886</u></b>	188	882

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop\_caches

## Platform Notes

BIOS Configuration:  
Operating Mode set to "Maximum Performance"  
COD Preference set to Disable  
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on x240m5-cpu2006 Thu May 26 10:19:55 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2643 v4 @ 3.40GHz  
Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECint\_rate2006 = 741

Lenovo Flex System x240 M5  
(3.40 GHz, Intel Xeon E5-2643 v4)

SPECint\_rate\_base2006 = 705

CPU2006 license: 9017

Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

### Platform Notes (Continued)

```

2 "physical id"s (chips)
24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 6
siblings  : 12
physical 0: cores 0 1 2 3 6 7
physical 1: cores 0 1 2 3 6 7
cache size : 20480 KB

From /proc/meminfo
MemTotal:      263829224 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
Linux x240m5-cpu2006 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 26 10:12

SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4       xfs   515G  7.4G  508G   2% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO -[C4E123H-2.10]- 03/25/2016
Memory:
16x Hynix HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz
Continued on next page

```



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

**Lenovo Group Limited**

**SPECint\_rate2006 = 741**

Lenovo Flex System x240 M5  
(3.40 GHz, Intel Xeon E5-2643 v4)

**SPECint\_rate\_base2006 = 705**

**CPU2006 license:** 9017

**Test date:** May-2016

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Mar-2016

**Tested by:** Lenovo Group Limited

**Software Availability:** Dec-2015

## Platform Notes (Continued)

8x NO DIMM Unknown

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmmer: -D\_FILE\_OFFSET\_BITS=64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

SPECint\_rate2006 = 741

Lenovo Flex System x240 M5  
(3.40 GHz, Intel Xeon E5-2643 v4)

SPECint\_rate\_base2006 = 705

CPU2006 license: 9017

Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

## Base Optimization Flags (Continued)

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmer: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Lenovo Group Limited

SPECint\_rate2006 = 741

Lenovo Flex System x240 M5  
(3.40 GHz, Intel Xeon E5-2643 v4)

SPECint\_rate\_base2006 = 705

CPU2006 license: 9017

Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias  
-opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



# SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

**Lenovo Group Limited**

**SPECint\_rate2006 = 741**

Lenovo Flex System x240 M5  
(3.40 GHz, Intel Xeon E5-2643 v4)

**SPECint\_rate\_base2006 = 705**

**CPU2006 license:** 9017

**Test date:** May-2016

**Test sponsor:** Lenovo Group Limited

**Hardware Availability:** Mar-2016

**Tested by:** Lenovo Group Limited

**Software Availability:** Dec-2015

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Tue Jun 28 17:32:42 2016 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 28 June 2016.