



SPEC® CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo System x3550 M5
(1.60 GHz, Intel Xeon E5-2608L v4)

SPECfp®2006 = 76.6

SPECfp_base2006 = 74.4

CPU2006 license: 9017

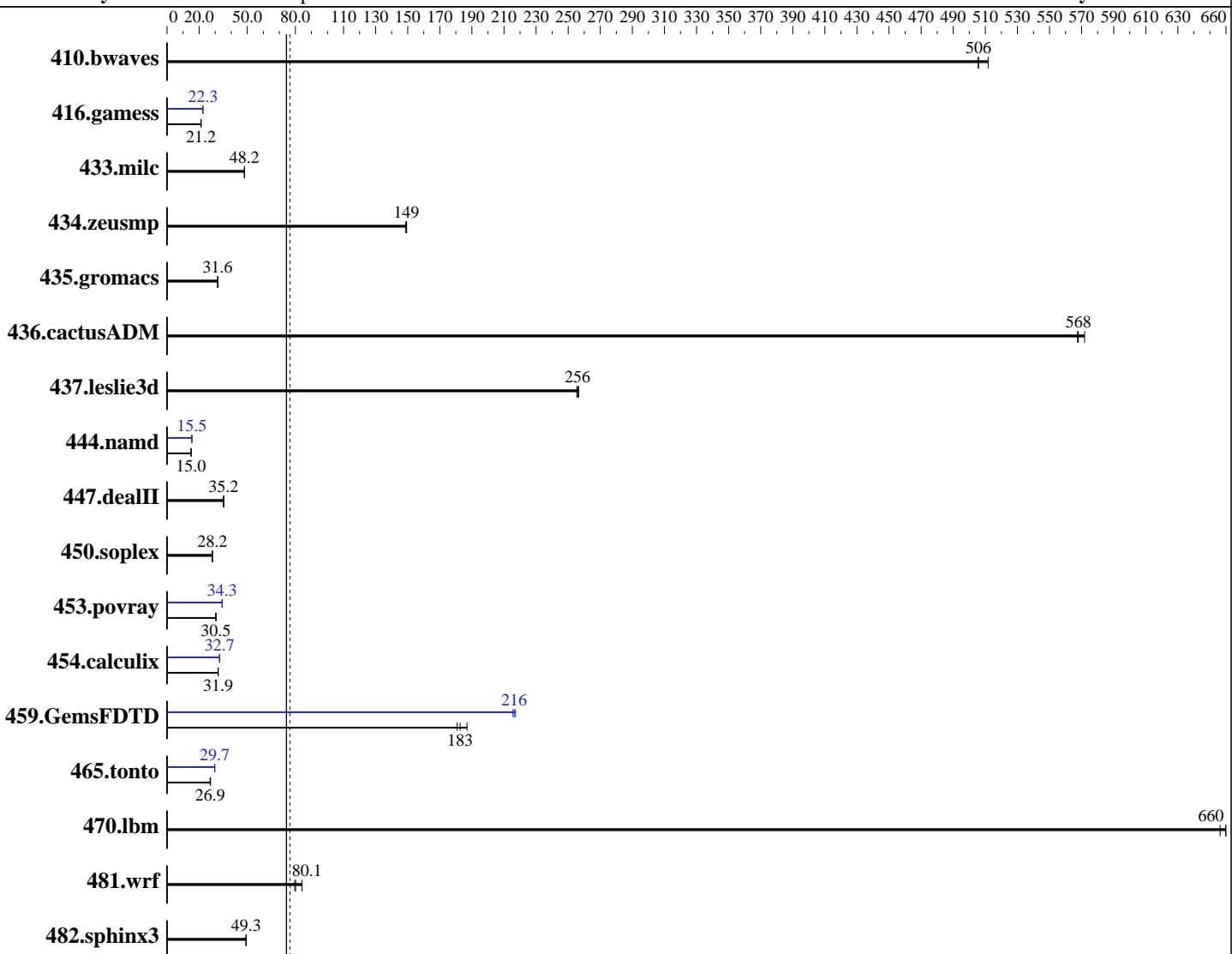
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Jun-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015



SPECfp_base2006 = 74.4

SPECfp2006 = 76.6

Hardware

CPU Name: Intel Xeon E5-2608L v4
CPU Characteristics: Intel Turbo Boost Technology up to 1.70 GHz
CPU MHz: 1600
FPU: Integrated
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
Compiler: Kernel 3.12.49-11-default
C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)

Continued on next page

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo System x3550 M5
(1.60 GHz, Intel Xeon E5-2608L v4)

SPECfp2006 = 76.6

SPECfp_base2006 = 74.4

CPU2006 license: 9017

Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Jun-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

L3 Cache:	20 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 1866 MHz)
Disk Subsystem:	1 x 800 GB SATA SSD
Other Hardware:	None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	26.9	506	26.9	506	26.6	512	26.9	506	26.9	506	26.6	512
416.gamess	927	21.1	923	21.2	923	21.2	876	22.3	877	22.3	876	22.3
433.milc	190	48.2	190	48.3	191	48.2	190	48.2	190	48.3	191	48.2
434.zeusmp	61.0	149	60.9	149	61.2	149	61.0	149	60.9	149	61.2	149
435.gromacs	226	31.6	226	31.6	226	31.6	226	31.6	226	31.6	226	31.6
436.cactusADM	21.0	568	21.1	567	20.9	572	21.0	568	21.1	567	20.9	572
437.leslie3d	36.7	256	36.8	255	36.6	257	36.7	256	36.8	255	36.6	257
444.namd	535	15.0	535	15.0	535	15.0	519	15.5	519	15.4	519	15.5
447.dealII	325	35.2	324	35.3	325	35.2	325	35.2	324	35.3	325	35.2
450.soplex	295	28.2	296	28.1	295	28.2	295	28.2	296	28.1	295	28.2
453.povray	174	30.5	174	30.5	175	30.4	155	34.3	155	34.3	155	34.4
454.calculix	259	31.9	259	31.8	259	31.9	252	32.7	252	32.7	252	32.7
459.GemsFDTD	58.1	183	56.8	187	58.7	181	49.2	216	48.8	217	49.0	216
465.tonto	366	26.9	366	26.9	363	27.1	331	29.7	331	29.7	331	29.7
470.lbm	20.9	656	20.8	660	20.8	660	20.9	656	20.8	660	20.8	660
481.wrf	133	84.1	140	79.6	139	80.1	133	84.1	140	79.6	139	80.1
482.sphinx3	395	49.3	397	49.1	395	49.3	395	49.3	397	49.1	395	49.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Configuration:

Operating Mode set to Maximum Performance

Intel Hyperthreading set to Disabled

Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 ## e3fb8667b5a285932ceab81e28219e1

running on 123123 Fri Jun 3 15:54:27 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo System x3550 M5
(1.60 GHz, Intel Xeon E5-2608L v4)

SPECfp2006 = 76.6

SPECfp_base2006 = 74.4

CPU2006 license: 9017

Test date: Jun-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2608L v4 @ 1.60GHz
        2 "physical id"s (chips)
        16 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The
    following excerpts from /proc/cpuinfo might not be reliable. Use with
    caution.)
        cpu cores : 8
        siblings : 8
        physical 0: cores 0 1 2 3 4 5 6 7
        physical 1: cores 0 1 2 3 4 5 6 7
    cache size : 20480 KB
```

```
From /proc/meminfo
    MemTotal:      263830304 kB
    HugePages_Total:       0
    Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
    NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
Linux 123123 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015 (8d714a0)
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jun 3 06:36

```
SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   689G   28G  662G   5%  /home
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo System x3550 M5
(1.60 GHz, Intel Xeon E5-2608L v4)

SPECfp2006 = 76.6

SPECfp_base2006 = 74.4

CPU2006 license: 9017

Test date: Jun-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

Platform Notes (Continued)

BIOS LENOVO - [TBE123H-2.10] - 03/25/2016

Memory:

8x NO DIMM Unknown

16x Samsung M393A2G40DB1-CRC 16 GB 2 rank 2400 MHz, configured at 1867 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages disabled with:

echo never > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64

```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo System x3550 M5
(1.60 GHz, Intel Xeon E5-2608L v4)

SPECfp2006 = 76.6

SPECfp_base2006 = 74.4

CPU2006 license: 9017

Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Jun-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

Base Portability Flags (Continued)

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo System x3550 M5
(1.60 GHz, Intel Xeon E5-2608L v4)

SPECfp2006 = 76.6

SPECfp_base2006 = 74.4

CPU2006 license: 9017

Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Jun-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

447.dealII: basepeak = yes

450.soplex: basepeak = yes

```
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
            -ansi-alias
```

Fortran benchmarks:

410.bwaves: basepeak = yes

```
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep-
```

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

```
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
               -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
               -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
               -inline-level=0 -opt-prefetch -parallel
```

```
465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
            -opt-malloc-options=3 -auto -unroll4
```

Benchmarks using both Fortran and C:

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Lenovo Group Limited

Lenovo System x3550 M5
(1.60 GHz, Intel Xeon E5-2608L v4)

SPECfp2006 = 76.6

SPECfp_base2006 = 74.4

CPU2006 license: 9017

Test date: Jun-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015

Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Jun 28 17:31:36 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 28 June 2016.