



SPEC® CFP2006 Result

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Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant BL460c Gen9
(1.70 GHz, Intel Xeon E5-2603 v4)

SPECfp®_rate2006 = 352

SPECfp_rate_base2006 = 344

CPU2006 license: 3

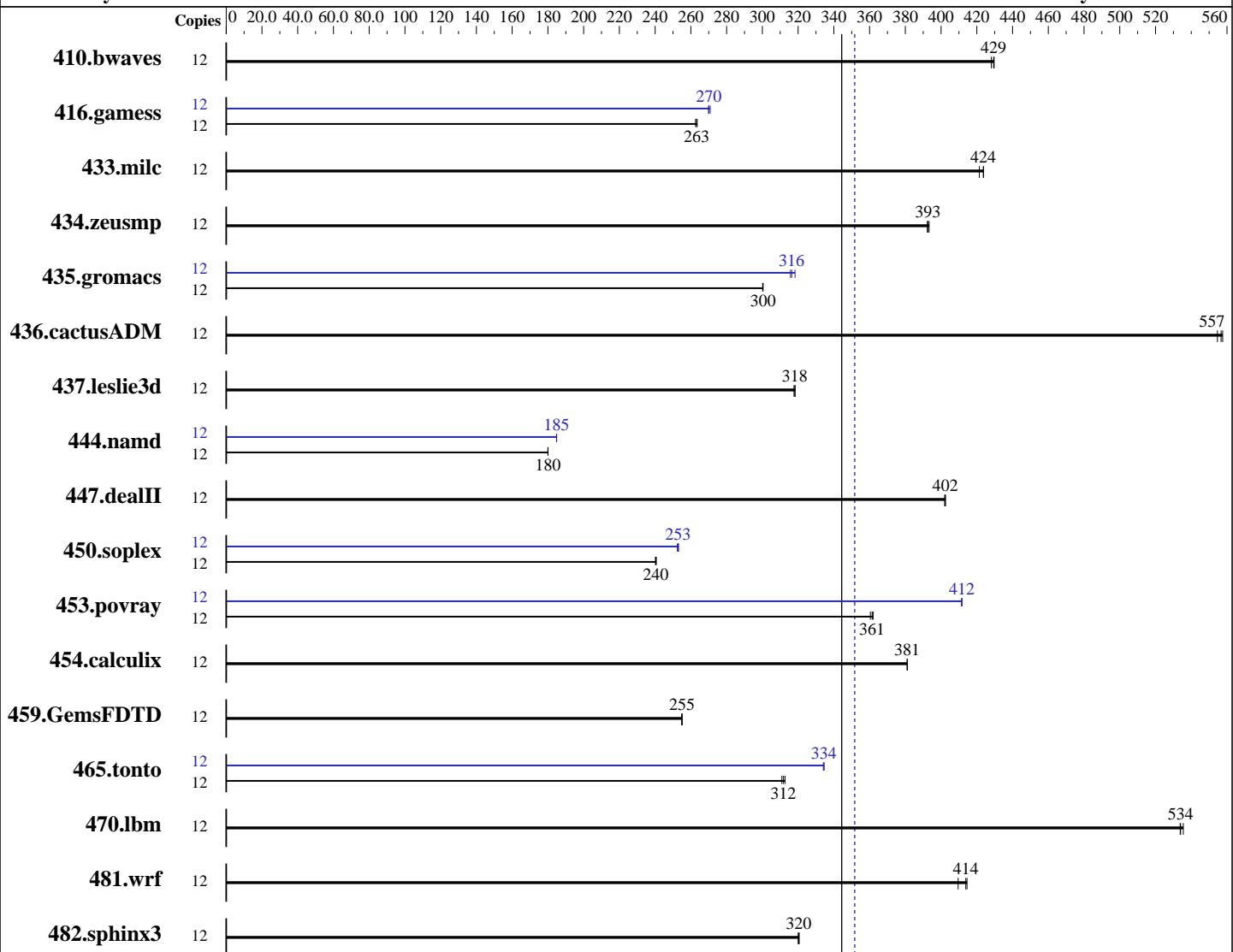
Test sponsor: HPE

Tested by: HPE

Test date: Jun-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015



SPECfp_rate_base2006 = 344

SPECfp_rate2006 = 352

Hardware

CPU Name: Intel Xeon E5-2603 v4
CPU Characteristics:
CPU MHz: 1700
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 7.2, (Maipo)
Compiler: Kernel 3.10.0-327.el7.x86_64
C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: xfs

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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (8 x 32 GB 2Rx4 PC4-2400T-R,
running at 1866 MHz)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	380	430	<u>380</u>	<u>429</u>	381	428	12	380	430	<u>380</u>	<u>429</u>	381	428
416.gamess	12	<u>893</u>	<u>263</u>	892	264	895	263	12	<u>870</u>	<u>270</u>	868	271	871	270
433.milc	12	260	424	<u>260</u>	<u>424</u>	261	421	12	260	424	<u>260</u>	<u>424</u>	261	421
434.zeusmp	12	<u>278</u>	<u>393</u>	278	392	278	393	12	<u>278</u>	<u>393</u>	278	392	278	393
435.gromacs	12	285	300	<u>285</u>	<u>300</u>	285	300	12	<u>271</u>	<u>316</u>	271	316	269	318
436.cactusADM	12	259	555	<u>258</u>	<u>557</u>	257	558	12	259	555	<u>258</u>	<u>557</u>	257	558
437.leslie3d	12	<u>355</u>	<u>318</u>	355	318	354	318	12	<u>355</u>	<u>318</u>	355	318	354	318
444.namd	12	535	180	<u>535</u>	<u>180</u>	534	180	12	520	185	<u>521</u>	<u>185</u>	521	185
447.dealII	12	<u>341</u>	<u>402</u>	341	402	341	402	12	<u>341</u>	<u>402</u>	341	402	341	402
450.soplex	12	<u>416</u>	<u>240</u>	417	240	416	241	12	<u>396</u>	<u>253</u>	397	252	395	253
453.povray	12	<u>177</u>	<u>361</u>	177	360	176	362	12	155	412	<u>155</u>	<u>412</u>	155	411
454.calculix	12	260	381	260	381	<u>260</u>	<u>381</u>	12	260	381	260	381	<u>260</u>	<u>381</u>
459.GemsFDTD	12	500	255	<u>499</u>	<u>255</u>	499	255	12	500	255	<u>499</u>	<u>255</u>	499	255
465.tonto	12	378	313	<u>379</u>	<u>312</u>	380	311	12	<u>353</u>	<u>334</u>	<u>353</u>	<u>334</u>	353	335
470.lbm	12	308	536	309	534	<u>309</u>	<u>534</u>	12	308	536	309	534	<u>309</u>	<u>534</u>
481.wrf	12	327	410	323	415	<u>324</u>	<u>414</u>	12	327	410	323	415	<u>324</u>	<u>414</u>
482.sphinx3	12	729	321	<u>730</u>	<u>320</u>	731	320	12	729	321	<u>730</u>	<u>320</u>	731	320

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with -
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```



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Platform Notes

BIOS Configuration:

Power Profile set to Custom

Power Regulator set to Static High Performance Mode

Minimum Processor Idle Power Core C-State set to C1E State

Minimum Processor Idle Power Package C-State set to No Package State

Collaborative Power Control set to Disabled

QPI Snoop Configuration set to Home Snoop

Thermal Configuration set to Maximum Cooling

Processor Power and Utilization Monitoring set to Disabled

Memory Refresh Rate set to 1x Refresh

Sysinfo program /home/cpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on BL460c-Gen9-B Wed Jun 1 21:19:04 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2603 v4 @ 1.70GHz

2 "physical id"s (chips)

12 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 6

siblings : 6

physical 0: cores 0 1 2 3 4 5

physical 1: cores 0 1 2 3 4 5

cache size : 15360 KB

From /proc/meminfo

MemTotal: 263844004 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

os-release:

NAME="Red Hat Enterprise Linux Server"

VERSION="7.2 (Maipo)"

ID="rhel"

ID_LIKE="fedora"

VERSION_ID="7.2"

PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"

ANSI_COLOR="0;31"

CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"

redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:

Linux BL460c-Gen9-B 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015

Continued on next page



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Platform Notes (Continued)

x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 1 09:34

SPEC is set to: /home/cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda5 xfs 314G 192G 123G 61% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP I36 02/29/2016

Memory:

8x UNKNOWN NOT AVAILABLE
8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 1866 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have one line reading as:
8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 1866 MHz

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



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Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

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Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

```

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

```

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
    -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
    -prof-use(pass 2) -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

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Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>
<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>
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